CALIBRATION AND SERVICING HANDBOOK

1061 1061A

datron
INSTRUMENTS

digital voltmeters

CALIBRATION AND SERVICING HANDBOOK

for

THE DATRON AUTOCAL 1061 and 1061A DIGITAL VOLTMETERS

(for operating procedures
refer to the User's Handbook)

850045 Issue 9 (OCT. 1986)

For any assistance contact your nearest Datron Sales and Service center.
Addresses can be found at the back of this handbook.

Due to our policy of continuously updating our products, this handbook may contain minor differences in specification, components and circuit design to the instrument actually supplied. Amendment sheets precisely matched to your instrument serial number are available on request.

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SECTION 1

CALIBRATION

1.1 INTRODUCTION

1.1.1 General

The purpose of calibration is to take account of any long-term drifts in the components of the instrument and to restore the accuracy, traceable to a known standard.

The period between calibrations depends upon the accuracy performance required from the instrument and for guidance, guaranteed accuracies for 24 hours, 90 days and 1 year are quoted.

The calibration procedures presented in the following pages should cater for most calibration situations. If, however, a special problem arises, please contact our Customer Service Section.

1.1.2 The Essentials for Good Calibration

Temperature - So that the instrument can meet its specification over the quoted temperature range, the temperature environment should be stabilised at 23°C ±1°C. In addition, temperature gradients around the instrument should be considered, therefore calibrate the instrument in its normal operating position and allow plenty of room for ventilation.

Warm up - It is essential that the instrument has fully temperature stabilised if the best results from calibration are to be achieved. Therefore, at least a 2 hour warm-up period is recommended during which time the line supply or the covers should not be removed even for a short period. In addition, if the covers have been removed, make certain that they are correctly fitted and that the leaf contacts to the Earth and Guard Shields are in good shape.

Calibration Source - To perform a useful calibration the accuracy of the source should always be at least four times that of the instrument being calibrated. In most cases, examples of likely sources are given for each calibration function.

With some calibration sources, the output may take several seconds to settle to a final value, therefore unless a shorter settling time is assured, a period of 10 seconds is recommended before each calibration operation.

Guarding - It is preferable to arrange for the DVM to be calibrated with 'Local Guard' selected. Furthermore to arrange for the 'Lo' terminal of the DVM to remain at 'earth' throughout and let the calibration source float. If a 'Remote Guard' connection is necessary then examples are shown in the Operating Manual.

1.1.3 The 'AUTOCAL' Process

1.1.3.1 General

The Datron 'AUTOCAL' process means that complete calibration of AC, DC, Ohms and Current on every range can be carried out from the instruments own front panel. In the process, an internal non-volatile memory stores calibration constants for each function and range as determined when the instrument takes a series of 16 readings of the applied calibration source. Internally, each of the readings is deviated by one sixteenth of a digit and when an average is taken, the instrument is able to resolve to better than one least significant digit displayed.

Access to the non-volatile memory is gained using a key inserted into the rear panel. When calibration is complete, the key is removed, therefore preventing accidental or unauthorised use of the calibration routine.

1.1.3.2 Procedure Outline

- Select the 'FUNCTION' and 'RANGE' to be calibrated and cancel any 'MODE' or 'COMPUTE' buttons.

Insert the key into the 'CALIBRATE ENABLE' keyswitch on the rear panel and turn to the 'CAL' position. (The 'cal' legend will be displayed on the front panel.)

If the instrument is fitted with Option 50 IEEE Bus, set the rear panel address switch to 31 i.e. all 1's.

- Connect the calibration source to the input terminals and operate the keys shown in the tables in the following pages. When a 'CALIBRATE' button is operated, its associated L.E.D. indicator will light and extinguish when the calibration operation is executed.

- When all calibration is complete turn the keyswitch to 'RUN' and remove the key.

1.1.3.3 The Five 'AUTOCAL' Keys

'Zero' - This takes account of offsets in the instrument and in the calibration source.

'Gain' - This sets a scaling factor for each range and function.

'Ib' - This nulls the input bias current of the DC voltage measurement circuits to around 10pA. Therefore it only has a significant effect on the low DC voltage ranges and high resistance Ohms ranges. It can be operated as often as required and independently of other calibration operations. It will be seen that successive operations of 'Ib' approach the final nulled value of current iteratively.

'AcHf' - This flattens the response of the A.C. amplifier used for AC voltage measurement. It should only be used when a full calibration i.e. 'Zero', 'Gain' and 'AcHf' is carried out. As with 'Ib' the calibration action is iterative and requires several operations of the key to complete.

'Lin' - This is an important calibration operation as it optimises the basic linearity of the internal measurement circuitry used for all ranges and functions. It must be used before any DC voltage or Ohms calibration is carried out.

1.1.3.4 'AUTOCAL' using 'KEYBOARD'

This is an extension of the 'AUTOCAL' process which is useful when using a calibration source set to a nominal value but with known errors. This means for example that calibration directly to a standard cell is possible. A full explanation of the procedure is covered in section 1.7.

1.1.3.5 'AUTOCAL' over the Bus

Each of the five calibration operations can be controlled using Option 50, the IEEE bus. This means that the instrument can be entirely calibrated remotely or under program control. As mentioned in the 'Procedure Outline' for a manual calibration, the rear panel address switch should be set to 31, i.e. all 1's. When a bus calibration is required the address switch must be set to the address number assigned to the DVM in the system. More details of calibration with the bus are included in section 1.8.

1.1.3.6 'Error 4'

If during calibration 'Error 4' is displayed, this indicates that the Calibration Source deviates too far from the calibration span of the instrument. Under these circumstances, the calibration memory is not updated and the instrument goes into 'Hold' with the calibration button calibration key LED remains on.

In the case of 'Zero', 'Gain' or 'AcHf' the Calibration Source should be checked and the same 'CALIBRATE' key repressed. The 'Hold' mode may be released any time and the instrument will free run again. If 'Error 4' follows 'Ib' or 'Lin' or persistently appears following 'Zero', 'Gain' or 'AcHf' then an instrument failure may have occurred. Therefore either consult our Customer Service Section or the Servicing Section of this Handbook.

1.2 DC VOLTAGE CALIBRATION

1.2.1 General

The procedure in the table opposite is all that is necessary to completely 'AUTOCAL' the DC voltage function. Steps 1 and 2 affect the accuracy on all ranges and should therefore be carried out even if just one range is being calibrated.

On each range a 'Zero' and 'Gain' calibration is required for each polarity of input. The two 'Zero' calibrations are included to overcome a possible zero difference with the polarity setting of the DC calibration source.

If the 'DVM Reading After Calibration' is not in accordance with the table, repeat operations of the same 'CALIBRATE' key are permissible. Where no tolerance is shown in this column, only the exact reading quoted with an occasional least significant digit showing is to be expected.

1.2.2 Equipment Required

- 1MΩ 'Lin' Source. This is a 1MΩ 5% resistor in parallel with a 1nF capacitor, shielded to reduce noise interference.

- 10MΩ 'Ib' Source. This is a 10MΩ 5% resistor in parallel with a 1nF capacitor, shielded to reduce noise interference.

Datron products, number 400391 and 400392, are available as 'Lin' and 'Ib' sources and are recommended.

- A DC Calibration Source. e.g.: Datron 4000/4000A Autocal Standard.

1.2.3 Checking Accuracy after 'AUTOCAL'

To check the accuracy after 'AUTOCAL' the 'Specification Verification' section of the User's Handbook will be useful; it provides tables for quick reference of accuracy on all ranges and functions in displayed digits.

DC VOLTAGE CALIBRATION

Step	Calibration Operation	Calibration Source Output	DVM Setting	'CALIBRATE' Key	DVM Reading After Calibration	Remarks
1	Linearity	1MΩ Lin Source	DC,1 Filter[1]	'Lin'	<10 digits (<100 digits)	This calibration step may take around 30 seconds to complete
2	Input Bias Current	10MΩ Ib Source	DC,.1	'Ib'	<100 digits	Each subsequent operation of 'Ib' should approximately halve the DVM reading
3	10V Range Zero	+0.0000V	DC,10	'Zero'	±0.0000V	
4	10V Positive Full Range	+10.0000V	DC,10	'Gain'	+10.0000V	
5	10V Range Zero	−0.0000V	DC,10	'Zero'	±0.0000V	
6	10V Negative Full Range	−10.0000V	DC,10	'Gain'	−10.0000V	
7	1V Range Zero	+0.00000V	DC,1	'Zero'	±.00000V	
8	1V Positive Full Range	+1.00000V	DC,1	'Gain'	+1.00000V	
9	1V Range Zero	−0.00000V	DC,1	'Zero'	±.00000V	
10	1V Negative Full Range	−1.00000V	DC,1	'Gain'	−1.00000V	
11	.1V Range Zero	+0.000mV	DC,.1	'Zero'	±0.000mV ±1 digit	Wait for the reading to stabilize before operating 'Zero'
12	.1V Positive Full Range	+100.000mV	DC,.1	'Gain'	+100.000V ±1 digit	
13	.1V Range Zero	−0.000mV	DC,.1	'Zero'	±0.000mV ±1 digit	Wait for the reading to stabilize before operating 'Zero'
14	.1V Negative Full Range	−100.000mV	DC,.1	'Gain'	−100.000V ±1 digit	
15	100V Range Zero	+0.000V	DC,100	'Zero'	±0.000V	
16	100V Positive Full Range	+100.000V	DC,100	'Gain'	+100.000V	
17	100V Range Zero	−0.000V	DC,100	'Zero'	±0.000V	
18	100V Negative Full Range	−100.000V	DC,100	'Gain'	−100.000V	
19	1000V Range Zero	+0.00V	DC,1000	'Zero'	±0.00V	
20	1000V Positive Full Range	+1000.00V	DC,1000	'Gain'	+1000.00V	ϟ Lethal voltages present - increase calibration source in 100V steps if possible
21	1000V Range Zero	−0.00V	DC,1000	'Zero'	±0.00V	
22	1000V Negative Full Range	−1000.00V	DC,1000	'Gain'	−1000.00V	ϟ Lethal voltages present - increase calibration source in 100V steps if possible

[1] For 1061A, Input Filter increases resolution by 1 digit – 1061A tolerance given in brackets ( ).

1.3 OHMS CALIBRATION

1.3.1 General

The procedure in the table opposite is all that is necessary to completely 'AUTOCAL' the Ohms function. If just the Ohms or just one range of the Ohms is to be calibrated, then steps 1 and 2 in the DC Voltage Calibration table should be carried out first. Then on each Ohms range just a 'Zero' and 'Gain' calibration is required.

If the 'DVM Reading After Calibration' is not in accordance with the table, repeat operations of the same 'CALIBRATE' key is permissible to improve the reading. Where no tolerance is shown in this column, only the exact reading quoted with an occasional least significant digit showing is to be expected.

1.3.2 'Zero' Resistance Source

For accurate 'Zero' calibration on Ohms it is ESSENTIAL that a correctly connected zero source is used. Two arrangements are necessary as shown in Fig. 1.1; it can be seen that '4 wire Ω' selection is recommended on all ranges

1.3.3 Equipment Required

A set of resistance standards from 10Ω to 10MΩ in decades, it is essential that 10Ω to 100kΩ standards are 4 terminal devices. e.g. Datron 4000/4000A Autocal Standard with Option 20.

1.3.4 Checking Accuracy after 'AUTOCAL'

To check the accuracy after 'AUTOCAL' the 'Specification Verification' section of the Operating Handbook will be needed. It provides tables for quick reference of accuracy on all ranges and functions in displayed digits.

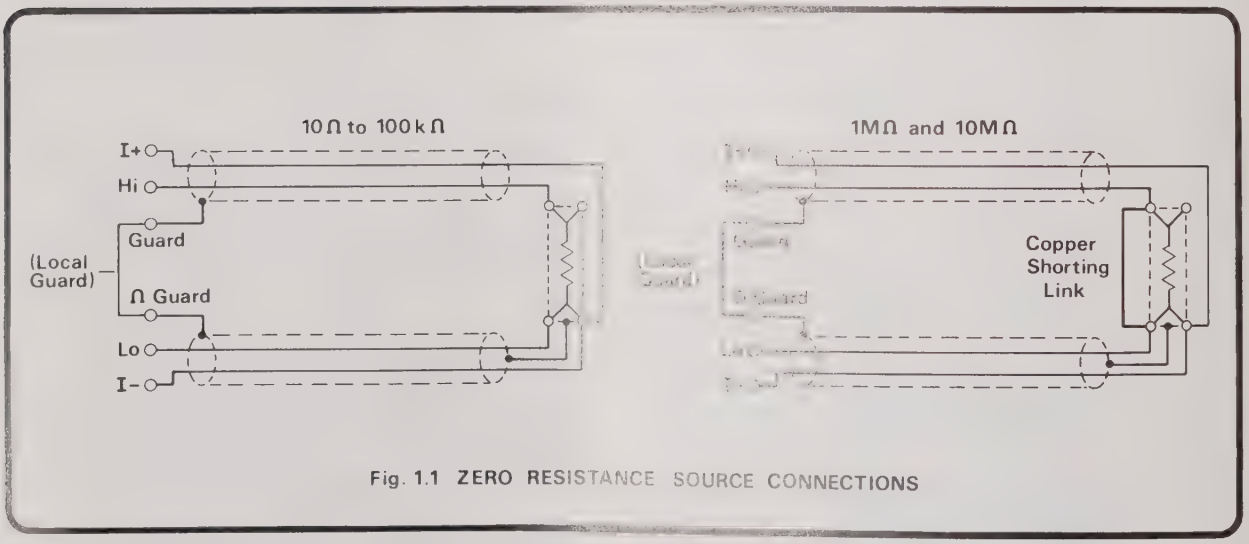


Fig. 1.1 ZERO RESISTANCE SOURCE CONNECTIONS

OHMS CALIBRATION TABLE

Step	Calibration Operation	Calibration Source	DVM Setting	'CALIBRATE' Key	DVM Reading After Calibration	Remarks
1	10Ω Range Zero	4 wire zero	kΩ, 4 wire, 10Ω	'Zero'	±0.0000Ω ±1 digit	Wait for the reading to stabilize before operating 'Zero'
2	10Ω Full Range	10Ω [1] Standard Resistor	kΩ, 4 wire, 10Ω	'Gain'	10.0000Ω ±1 digit	Wait for the reading to stabilize before operating 'Gain'
3	.1kΩ Range Zero	4 wire zero	kΩ, 4 wire, .1	'Zero'	±0.000Ω	
4	.1kΩ Full Range	100Ω [1] Standard Resistor	kΩ, 4 wire, .1	'Gain'	100.000Ω	
5	1kΩ Range Zero	4 wire zero	kΩ, 4 wire, 1	'Zero'	±.00000kΩ	
6	1kΩ Full Range	1kΩ [1] Standard Resistor	kΩ, 4 wire, 1	'Gain'	1.00000kΩ	
7	10kΩ Range Zero	4 wire zero	kΩ, 4 wire, 10	'Zero'	±0.0000kΩ	
8	10kΩ Full Range	10kΩ [1] Standard Resistor	kΩ, 4 wire, 10	'Gain'	10.0000kΩ	
9	100kΩ Range Zero	4 wire zero	kΩ, 4 wire, 100	'Zero'	±0.000kΩ	
10	100kΩ Full Range	100kΩ [1] Standard Resistor	kΩ, 4 wire, 100	'Gain'	100.000kΩ	
11	1000kΩ Range Zero	4 wire zero	kΩ, 4 wire, 1000 Input Filter[2]	'Zero'	±0.00kΩ (±0.000kΩ)	
12	1000kΩ Full Range	1000kΩ [1] Standard Resistor	kΩ, 4 wire, 1000 Input Filter[2]	'Gain'	1000.00kΩ ±1 digit (1000.000kΩ) (±10 digits)	
13	10MΩ Range Zero	4 wire zero	kΩ, 4 wire, 10MΩ Input Filter[2]	'Zero'	±0.0000MΩ (±0.00000MΩ)	
14	10MΩ Full Range	10MΩ [1] Standard Resistor	kΩ, 4 wire, 10MΩ Input Filter[2]	'Gain'	10.0000MΩ ±5 digits (10.00000MΩ) (±50 digits)	

[1] - With Standard Resistor sources it may be useful to use the 'KEYBOARD' method of calibration - see section 1.7

[2] - For 1061A, Input filter increases resolution by 1 digit, so 1061A figures are given in brackets ( ).

1.4 AC VOLTAGE CALIBRATION – OPTION 10 ONLY

1.4.1 General

The procedure in the table opposite is all that is necessary to completely 'AUTOCAL' the AC voltage function. On each range just a 'Zero', 'Gain' and 'AcHf' calibration is required.

If the 'DVM Reading After Calibration' is not in accordance with the table, repeat operation of the same 'CALIBRATE' key is permissible to improve the readings. This will be necessary with the AcHf key.

1.4.2 Equipment Required

A [illegible] shorting link and an AC calibration source e.g. Datron 4200 Autocal AC Standard.

1.4.3 Checking Accuracy after 'AUTOCAL'

To check the accuracy after 'AUTOCAL' the 'Specification Verification' section of the Operating Handbook will be useful. It provides tables for quick reference of accuracy on all ranges and functions in displayed digits.

AC VOLTAGE CALIBRATION TABLE (OPTION 10 ONLY)

Step	Calibration Operation	Calibration Source Output	DVM Setting	'CALIBRATE' Key	DVM Reading After Calibration	Remarks
1	DC coupled AC Zero	Copper Shorting link	AC,DC,.1	'Zero'	0.000mV ±3 digits	Set 'Local Guard'. Do not set 'Input filter'. Wait for reading to stabilize before operating 'Zero'
2	.1V Range Zero	Copper Shorting link	AC,.1	Check only	< 100 digits	
3	1V Range Zero	Copper Shorting link	AC,1	'Zero'	00000V ±1 digit	
4	10V Range Zero	Copper Shorting link	AC,10	'Zero'	0.0000V ±1 digit	
5	100V Range Zero	Copper Shorting link	AC,100	'Zero'	0.000V ±1 digit	
6	1000V Range Zero	Copper Shorting link	AC,1000	'Zero'	0.00V ±1 digit	
7	10V Full Range LF	10V rms 500 Hz	AC,10 Input Filter	'Gain'	10.0000V ±1 digit	Select 'Input filter' for remaining steps
8	10V Full Range HF	10V rms 30 kHz	AC, 10 Input filter	'AcHf'	10.0000V ±5 digits	
9	1V Full Range LF	1V rms 500Hz	AC,1 Input filter	'Gain'	1.00000V ±1 digit	
10	1V Full Range HF	1V rms 30 kHz	AC,1 Input filter	'AcHf'	1.00000V ±5 digits	
11	.1V Full Range LF	.1V rms 500 Hz	AC,.1 Input filter	'Gain'	100.000mV ±2 digits	
12	.1V Full Range HF	.1V rms 30 kHz	AC,.1 Input filter	'AcHf'	100.000mV ±5 digits	
13	100V Full Range LF	100V rms 500 Hz	AC,100 Input filter	'Gain'	100.000V ±1 digit	
14	100V Full Range HF	100V rms 30 kHz	AC,100 Input filter	'AcHf'	100.000V ±5 digits	
15	1000V Full Range LF	1000V rms 500 Hz	AC,1000 Input filter	'Gain'	1000.00V ±1 digit	⚡ Lethal voltage present. - increase calibration source in 100V steps if possible
16	1000V Full Range HF	1000V rms 20kHz	AC,1000 Input filter	'AcHf'	1000.00V ±5 digits	⚡ Lethal voltage present - increase calibration source in 100V steps if possible. DO NOT EXCEED 25 kHz

1.4 AC VOLTAGE CALIBRATION — 1061A OPTION 12 ONLY

1.4.4 General

The procedure in the table opposite is all that is necessary to completely 'AUTOCAL' the AC voltage function. On each range just a 'Zero', 'Gain' and 'AcHf' calibration is required.

If the 'DVM Reading After Calibration' is not in accordance with the table, repeat operation of the same 'CALIBRATE' key is permissible to improve the readings. This will be necessary with the AcHf key.

Note: To reduce the effect of noise at low input levels, AC zero calibration is carried out at 0.1% Range; and for 100mV Range zero (steps 1 & 2 of the table), Guard is connected to Lo using a copper shorting link.

1.4.5 Equipment Required

A copper shorting link and an AC calibration source e.g. Datron 4200 Autocal AC Standard.

1.4.6 Checking Accuracy after 'AUTOCAL'

To check the accuracy after 'AUTOCAL' the 'Specification Verification' section of the User's Handbook can be used. It describes the use of 'Spec' mode to verify the performance of the instrument, also providing a report facility for compilation of permanent records.

AC VOLTAGE CALIBRATION TABLE (1061A OPTION 12 ONLY)

Step	Calibration Operation	Calibration Source Output	DVM Setting	'CALIBRATE' Key	DVM Reading After Calibration	Remarks
1	DC coupled AC Zero	0.100mV 500Hz (short Guard to Lo)	AC,DC,.1	'Zero'	0.100mV ±10 digits	Set 'Local Guard'. Do not set filter in. Wait for reading to stabilize before operating 'Zero'
2	.1V Range Zero	Short Hi to Lo to Guard	AC,.1	Check only	< 100 digits	
3	1V Range Zero	0.00100V 500Hz	AC,1	'Zero'	0.00100V ±1 digit	
4	10V Range Zero	0.0100V 500Hz	AC,10	'Zero'	0.010,0V ±1 digit	
5	100V Range Zero	0.100V 500Hz	AC,100	'Zero'	0.100V ±1 digit	
6	1000V Range Zero	1.00V 500Hz	AC,1000	'Zero'	1.00V ±1 digit	
7	10V Full Range LF	10V rms 500Hz	AC,10	'Gain'	10.000,0V ±1 digit	
8	10V Full Range HF	10V rms 30KHz	AC,10	'AcHf'	10.000,0V ±10 digits	
9	1V Full Range LF	1V rms 500Hz	AC,1	'Gain'	1.000,00V ±1 digit	
10	1V Full Range HF	1V rms 30kHz	AC,1	'AcHf'	1.000,00V ±10 digits	
11	.1V Full Range LF	.1V rms 500Hz	AC,.1	'Gain'	100.000mV ±2 digits	
12	.1V Full Range HF	.1V rms 30kHz	AC,.1	'AcHf'	100.000mV ±10 digits	
13	100V Full Range LF	100V rms 500Hz	AC,100	'Gain'	100.000V ±1 digit	
14	100V Full Range HF	100V rms 30kHz	AC,100	'AcHf'	100.000V ±10 digits	
15	1000V LF Range Gain	500V rms 500Hz	AC,1000	'KEYBOARD 500V' 'Gain'	500.00V ±1 digit	Lethal voltage present - increase calibration source in 100v steps if possible
16	1000V HF Range Gain	500V rms 20kHz	AC,1000	'KEYBOARD 500V' 'AcHf'	500.00V ±15 digits	Lethal voltage present - increase calibration source in 100V steps if possible. DO NOT EXCEED 25kHz

1.5 DC CURRENT CALIBRATION

(No DC Current facility if Option 12 is fitted)

1.5.1 General

The procedure in the table below shows all that is necessary to completely 'AUTOCAL' the DC Current function. If just the DC Current or just one range of DC Current is to be calibrated, then step 11 to 14 of the DC Voltage Calibration table should be carried out first. Then on each DC Current range just a 'Zero' and 'Gain' calibration is required.

If the 'DVM Reading After Calibration' is not in accordance with the table then repeat operation of the same 'CALIBRATE' key is permissible to improve the reading. Where no tolerance is shown in this column, only the exact reading quoted with an occasional least significant digit showing is to be expected.

1.5.2 Equipment Required

A DC Current calibration source. e.g. Datron 4000/4000A Autocal Standard with Option 20.

1.5.3 Checking Accuracy after 'AUTOCAL'

To check the accuracy after 'AUTOCAL' the 'Specification' section of the Operating Handbook will be used. It provides tables for quick reference of accuracies for all ranges and functions in displayed digits.

DC CURRENT CALIBRATION

Step	Calibration Operation	Calibration Source Output	DVM Setting		DVM Reading After Calibration	Remarks
1	.1mA Range Zero	0.000μA	DC,I,.1		±0.000μA ±1 digit	Do not select 'Input filter'
2	.1mA Full Range	+100.000μA	DC,I,.1		+100.000μA ±2 digits	
3	1mA Range Zero	0.00000mA	DC,I,1		±.00000mA ±1 digit	
4	1mA Full Range	+1.00000mA	DC,I,1		+1.00000mA ±2 digits	
5	10mA Range Zero	0.0000mA	DC,I,10		±0.0000mA ±1 digit	
6	10mA Full Range	+10.0000mA	DC,I,10		+10.0000mA	
7	100mA Range Zero	0.000mA	DC,I,100		±0.000mA	
8	100mA Full Range	+100.000mA	DC,I,100		+100.000mA	
9	1000mA Range Zero	0.00mA	DC,I,1000		±0.00mA	
10	1000mA Full Range	+1000.00mA	DC,I,1000		+1000.00mA	

1.6 AC CURRENT CALIBRATION

(In conjunction with Option 10 only)

1.6.1 General

The procedure in the table below shows all that is required to completely 'AUTOCAL' the AC Current function. If just the AC Current or just one range of AC Current is to be calibrated, then steps 1, 2, 11 & 12 of the Option 10 AC Voltage Calibration table must be carried out first. Then on each range just a 'Zero' and 'Gain' calibration is required.

If the 'DVM Reading After Calibration' is not in accordance with the table then repeat operations of the same 'CALIBRATE' key is permissible to improve the reading.

1.6.2 Equipment Required

An AC Current calibration source at 1kHz. e.g Datron 4200 Autocal AC Standard with option 30

1.6.3 Checking Accuracy after 'AUTOCAL'

To check the accuracy after 'AUTOCAL' the specification Verification section of the Operation Handbook will be useful. It provides tables for quick reference of accuracy on all ranges and functions in displayed digits

AC CURRENT CALIBRATION TABLE

Step	Calibration Operation	Calibration Source Output	DVM Setting	'CALIBRATE' Key	DVM Reading After Calibration	Remarks
1	DC coupled AC Zero	No connections to DVM input terminals	I,DC,AC,.1	'Zero'	0.000μA ±5 digits	Do not select 'Input filter'
2	.1mA Range Zero	"	I,AC,.1	Check only	±100 digits	Cancel DC coupled
3	1mA Range Zero	"	I,DC,AC,1	'Zero'	00000mA ±5 digits	
4	10mA Range Zero	"	I,DC,AC,10	'Zero'	0.0000mA ±5 digits	
5	100mA Range Zero	"	I,DC,AC,100	'Zero'	0.000mA ±5 digits	
6	1000mA Range Zero	"	I,DC,AC,1000	'Zero'	0.00mA ±5 digits	
7	.1mA Full Range	100μA, 1kHz	I,DC,AC,.1	'Gain'	100.000μA ±10 digits	
8	1mA Full Range	1mA, 1 kHz	I,DC,AC,1	'Gain'	1.00000mA ±10 digits	
9	10mA Full Range	10mA, 1 kHz	I,DC,AC,10	'Gain'	10,0000mA ±10 digits	
10	100mA Full Range	100mA, 1 kHz	I,DC,AC,100	'Gain'	100,000mA ±10 digits	
11	1000mA Full Range	1A, 1 kHz	I,DC,AC,1000	'Gain'	1000.00mA ±10 digits	

1.7 CALIBRATION USING 'KEYBOARD'

1.7.1 General

The 'KEYBOARD' method of calibration is useful when a calibration source although set to a nominal value has known errors. In this situation the known value of the calibration source can be entered into the DVM before the 'AUTOCAL' process is executed. The process is functional during any calibration with a source of magnitude between 20% and 200% of the range selected, but it should be noted that for equal magnitude source errors, calibrating at the lower percentage end of range produces a higher percentage calibration error. The 'KEYBOARD' method operates for both the 'Gain' and 'AcHf' calibration operations. An example using 'KEYBOARD' to calibrate directly against a Standard Cell is shown in the table below.

1.7.2 'KEYBOARD' with Negative Inputs

The 'KEYBOARD' method is used on DC Voltage calibration when Negative polarity sources, it is important NOT to enter a negative sign with the keyed-in source value. The instrument itself can determine the polarity of the source and update the appropriate calibration memory location.

1.7.3 'KEYBOARD' Calibration Example

The example shown in the table below uses 'KEYBOARD' to calibrate the 1000V AC LF Range Gain at [illegible] of the AC Voltage Calibration table for [illegible].

CALIBRATION EXAMPLE USING 'KEYBOARD'

Step	Calibration Operation	Calibration Source	DMM Setting	'CALIBRATE' Key	DMM Reading After Calibration	Remarks
1	1000V Range Zero	1.00V rms 500Hz	AC,1000	'Zero'	1.00V ±1 digit	
2	Set and Enter Source Value	500.00V rms 500Hz	'KEYBOARD' then 5,0,0,-,0,0	–	0 then +500.00	Lethal voltage present. Increase Calibration Source in 100V steps if possible
3	1000V AC LF Range Gain Calibration	As above	–	'Gain'	500.00V ±1 digit	

1.8 'AUTOCAL' OVER THE BUS

All the calibration procedures covered in this manual can be carried out remotely using Option 50, the IEEE Bus.

Effectively, the five calibration keys are replaced by five Bus instructions and these are used instead of the 'CALIBRATE' keys listed in the Calibration tables on previous pages.

An example of calibration with the Bus is given in the table below. A complete program listing for the same calibration operation assuming an HP9825 controller is as follows:—

```
Ø: dim D$[15]          define 15 character string variable
1:  clr 728            send 'device clear' to DVM (interface 7, address 28)
2: wrt 728,"F3R3Q1W1="  program to DC 1V, SRQ Mode 1, Enable Cal.
3  Ø→S
4  wrt 728,"GØ="       program zero cal. trigger
                       jump to SRQ service routine on interrupt
                       enable SRQ interrupts from interface 7
                       check status byte S obtained by service routine
                       prompt operator to apply calibration source on completing zero cal
                       program gain cal. trigger
                       program to Internal Trigger, Disable Cal. on completion of gain cal
                       program DVM to local state
                       SRQ service routine to read status byte
```

CALIBRATION EXAMPLE USING THE BUS

Step	Calibration Operation	Calibration Source	DVM Setting	Bus Controller Instruction	DVM Reading After Calibration	Remarks
1	Set DVM to known state	—	In Remote State	'Device Clear'	—	Program DVM to predetermined state A0C0DXE0F3M0N0 O0P0Q0R6S0T5
2	Set DVM to DCV, 1V Range, and prepare for calibration	+0.00000V	Calibration key to 'CAL'	'F3R3Q1W1='	—	Program DVM to Function: DC V (F3) Range: 1V (R3) SRQ Mode 1 (Q1) Enable Cal. (W1)
3	1V Range Zero	+0.00000V	In Remote State	'GØ='	± 00000V	Program 'Zero' cal., SRQ indicates when calibration operation completed
4	1V Positive Full Range	+1.00000V	In Remote State	'G1='	+1.00000V	Program 'Gain' cal., SRQ indicates when calibration operation completed
5	Set DVM to Internal Trigger, Disable Cal.	—	In Remote State	'TØWØ='	—	Program DVM to Internal Trigger (TØ), Disable Cal. (WØ)
6	—	—	In Local State, Calibration key to 'RUN'	'Local'	—	DVM in normal mode, free-running

SECTION 2

MECHANICAL DESCRIPTION

2.1 GENERAL

The 1061 has been designed to be either rack mounted in a standard 19" rack (3½" (2U) height required) or bench top/portable with integral tilt stand. An exploded view of the instrument is shown in Fig 2.1.

2.2 FRONT PANEL

The front panel incorporates the signal input terminals, range, function, mode, keyboard, compute and power switches and a numeric/legend gas discharge display.

2.3 REAR PANEL

The rear panel incorporates the mains supply, power input socket and fuses, digital and analog output sockets, rear and ratio signal input sockets, rear/front panel signal input selection switch, run/calibrate keyswitch, calibration interval (error) select switch and current option fuse.

2.4 EXTERNAL CONSTRUCTION

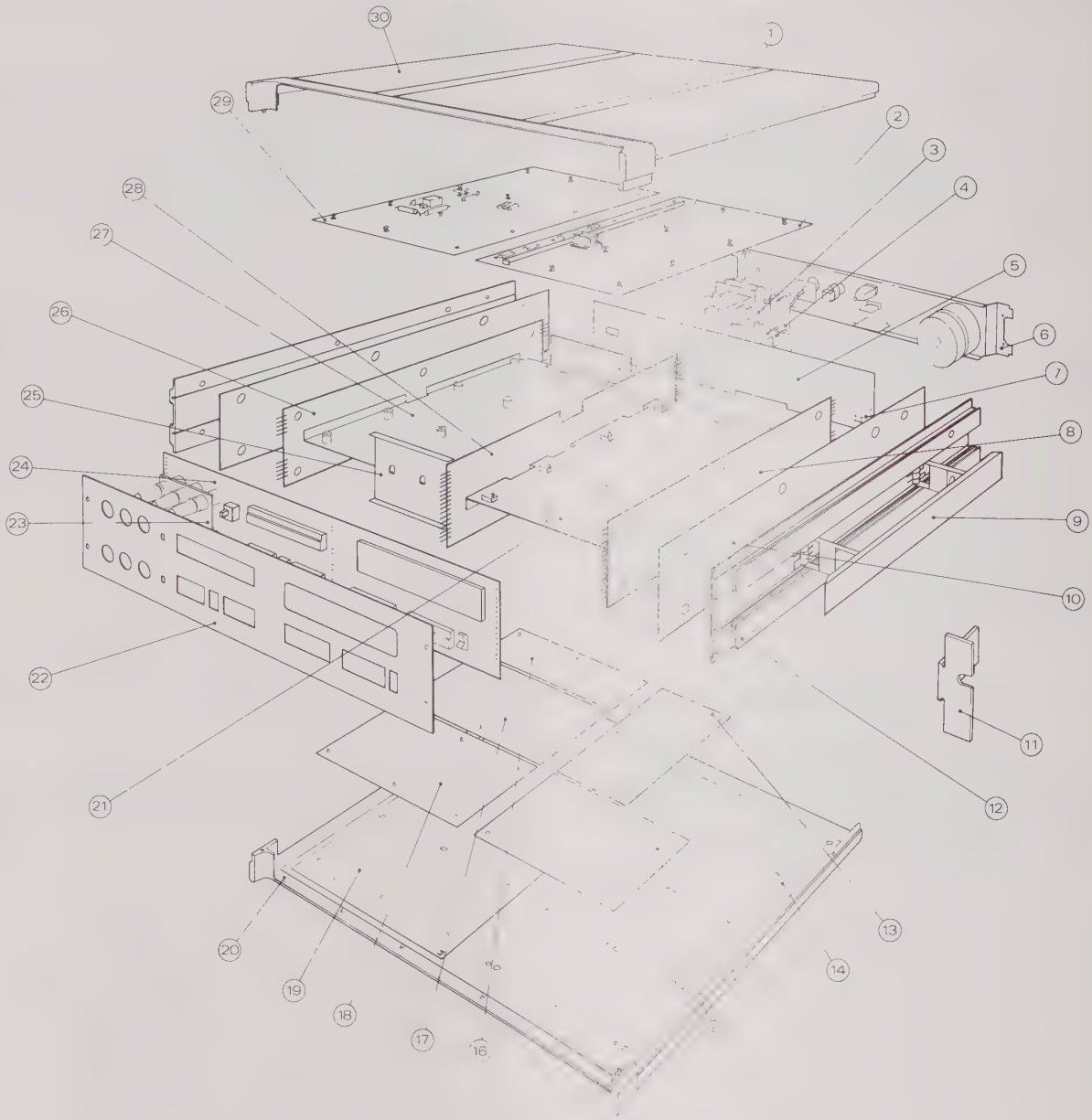
A screen printed key designation overlay adheres to the front panel retaining the polarising filter in front of the display. Both the front and rear panels are held together by two side extrusions running from front to rear. These side extrusions provide both slots for the handles or rack mounting 'ears' and locating points for the structural foam covers. The bottom cover is fitted with the tilt-stand, rubber feet and instruction card. Earth screening of the covers and guarding is provided by aluminium plates, heat staked to the inside of the covers with electrical connections made by spring contacts.

2.5 INTERNAL CONSTRUCTION

An internal chassis is constructed from five printed circuit boards, held together by connectors at each corner and held rigid by two inner aluminium shields fixed horizontally on the instrument's centre line running from front to rear. Input terminals, switches and display are mounted on the front printed circuit board (pcb) and the power supply on the rear pcb. The two side and centre pcb's are used for interconnections between the main circuit boards.

All the main circuit boards are mounted on the inner shields with hinges and quick release fasteners with flexible connections to allow operation in the 'hinged-up' position. The Analog output circuitry is fixed on to the rear pcb of the chassis and the Ratio Rear Input circuitry on to the rear panel. The options are mechanically fitted and require no soldering.

The chassis is mounted on to the side extrusions with nylon screws, spacers and an insulation sheet to ensure that the 'electrical spacings' of the BSI, UL and VDE specifications are achieved.



1. REAR GUARD SCREEN
2. DIGITAL ASSEMBLY
3. RATIO/REAR INPUT ASSEMBLY (OPTION)
4. ANALOG OUTPUT ASSEMBLY (OPTION)
5. REAR (POWER SUPPLY) PCB ASSEMBLY
6. REAR PANEL ASSEMBLY
7. POWER SUPPLY VOLTAGE SELECTION LINKS
8. R.H. PCB ASSEMBLY
9. HANDLE ASSEMBLY
10. INSULATION SHEET
11. RACK MOUNTING BRACKET
12. SIDE EXTRUSION
13. DIGITAL INTERFACE ASSEMBLY (OPTION)
14. EARTH SCREEN
15. DISPLAY DRIVER ASSEMBLY
16. CURRENT ASSEMBLY (OPTION) } See Note [1]
17. AC ASSEMBLY (OPTION 10) } See Note [1]
18. OHMS ASSEMBLY (OPTION)
19. OUTER GUARD SCREEN
20. BOTTOM COVER ASSEMBLY
21. R.H. CENTRE GUARD SCREEN
22. FRONT PANEL AND OVERLAY
23. TERMINAL SUPPORT PLATE
24. FRONT PCB ASSEMBLY
25. FRONT GUARD SCREEN
26. L.H. PCB ASSEMBLY
27. L.H. CENTRE GUARD SCREEN
28. CENTRE PCB ASSEMBLY
29. ANALOG ASSEMBLY
30. TOP COVER ASSEMBLY

FIG. 2.1 EXPLODED VIEW OF INSTRUMENT

[1] AC Assembly for 1061A Option 12 is fitted in place of AC Option 10 and Current Option 30 pcb assemblies

SECTION 3

TECHNICAL DESCRIPTION

3.1 INTRODUCTION

The internal circuits of the basic DC only instrument are divided between five printed circuit board assemblies (shown in bold outline in Fig. 3.1).

For the purpose of explanation, each assembly will be described separately and each assembly further sub-divided according to the various functions involved.

3.2 ANALOG ASSEMBLY (Circuit Drawing No. 430328)

The Analog assembly is split into three distinct sections: (i) the Analog Interface, (ii) the DC Isolator and (iii) the Analog to Digital (A - D) Converter.

The Analog Interface receives data from the Digital assembly to control the selection, range scaling and other features of the analog circuitry. Messages between the Analog and Digital assemblies are passed via opto-isolators, electrically isolating one from the other.

The DC Isolator includes the preamplifier, range scaling circuits and bootstrapped supplies. The A - D section converts the scaled input signal to a time period proportional to the signal using a modified triple slope technique.

3.2.1 Analog Interface (430328 sheet 5)

3.2.1.1 Introduction

The Analog Interface provides electrical isolation between the Digital and Analog circuitry. Latched data from the microprocessor is passed through opto-isolators, decoded and latched again on an analog assembly to select function, range, test, average and the D - A converter set up conditions. A line is also provided to instruct the micro-processor which options are present and if the AC assembly is measuring a signal above 5kHz.

3.2.1.2 Power-On

At power on the A - D converter is placed into the RESET condition (See Section 3.2.3.8). The analog circuitry is then interrogated to discern which options (if any) are fitted. Finally the analog circuitry is placed into the DC, 1000V range until a different range or function is selected (See Fig. 3.3).

To determine which options are fitted the Digital assembly sends a series of messages across the isolation barrier, decodes them on the analog side and gates them with lines from the option assemblies to feed a signal back across the isolation barrier to the micro-processor.

Looking at the procedure, in more detail, the Analog Interface Data (ID) lines are all set to a logic '1' except one, which is set to a logic '0', depending on the option being interrogated (See Fig. 3.2). As an example we will check to see if the AC option is fitted. ID1 is set low, the rest of the ID lines set high and the Analog Interface Address lines, IA0 and IA1 set low. The opto-isolators *invert* all signals, thus M17-3 is low and M19 pins 10, 4 and 11 are high. If the AC option is *not* fitted M19-2 is driven low via R55 from M17-3, causing M19-3 to be high, producing a logic '0' (−15 volts) on M18-4. If the AC option *is* fitted a 33kΩ resistor on the AC assembly (R14) overrides R55 and and a high is placed on M19-2. The effect is to produce a

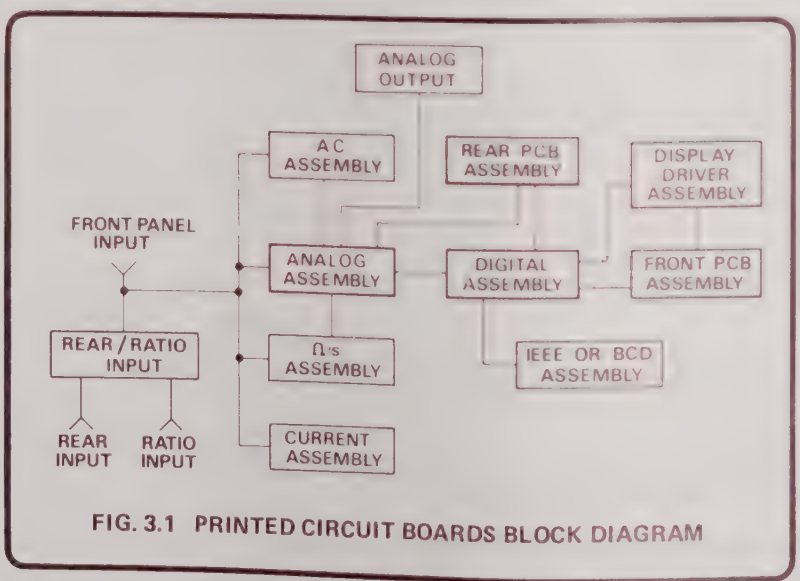


FIG. 3.1 PRINTED CIRCUIT BOARDS BLOCK DIAGRAM

Option checked	ID line low	Pin No. of M19 held low if Option incorporated
AC	ID 1	M19-3
Ω	ID 2	M19-11
I	ID 3	M19-4
RATIO	ID 4	M19-10

Fig. 3.2 POWER-ON OPTIONS FITTED TEST

high on M18-4, turning the opto-isolator M2-B on and thus COND. VAL (M2-8) is high, signalling to the Digital assembly that the AC option is fitted. Similarly, when the Ω, I or RATIO options are interrogated, the appropriate output of M19 is set low if the option is fitted causing the COND. VAL to be set high.

*Note: ID and IA lines
logic '1' ≡ +5 volts logic '0' ≡ 0 volts
AD lines
logic '1' ≡ 0 volts logic '0' ≡ --15 volts

The next step in the power-up sequence as far as the analog circuits are concerned, is to be placed into the DC, 1000V range (See Fig. 3.3 Flowchart). Firstly, all assemblies are deselected by placing logic '1's on all the ID lines, then setting the IAØ and IA1 lines low (see Fig 3.4), clocking the option selects latches (M20 Analog assembly, M5 AC assembly, M9 Ohms assembly, M1 Ratio assembly from M17-3. Both IA lines then return high.

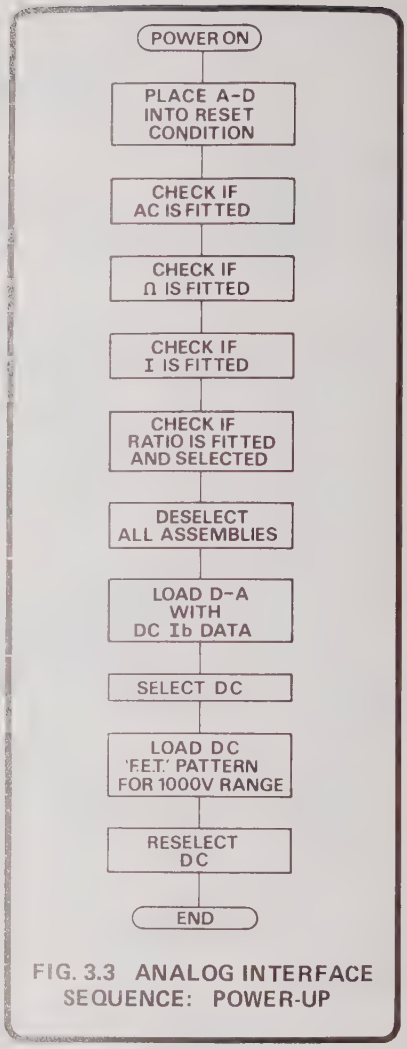

POWER ON
PLACE A-D INTO RESET CONDITION
CHECK IF AC IS FITTED
CHECK IF Ω IS FITTED
CHECK IF I IS FITTED
CHECK IF RATIO IS FITTED AND SELECTED
DESELECT ALL ASSEMBLIES
LOAD D-A WITH DC Ib DATA
SELECT DC
LOAD DC 'F.E.T.' PATTERN FOR 1000V RANGE
RESELECT DC
END

FIG. 3.3 ANALOG INTERFACE SEQUENCE: POWER-UP

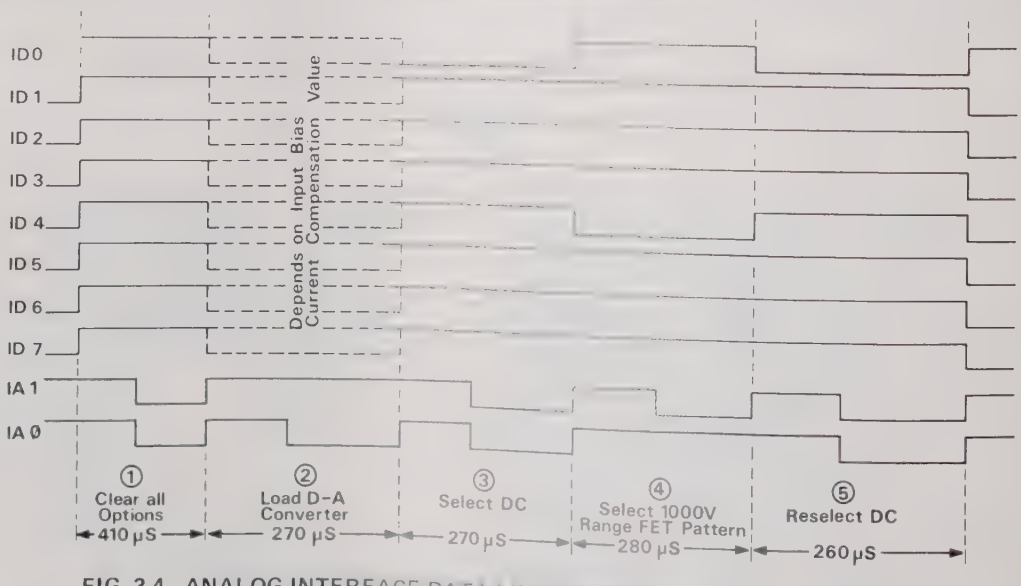


FIG. 3.4 ANALOG INTERFACE DATA LINE TIMING DIAGRAM (POWER-UP)

Secondly, the latches of the D - A converter (M13, M14) are set up with the input bias current (Ib) compensation data. The ID lines are set to the appropriate pattern and the information is clocked on to M13 and M14 by a delayed low to high edge from M17-4, originating from IAØ going low. The delay makes sure that the signal from M17-10 has disabled the "F.E.T." latch M21. Once again, the IAØ line returns to the resting state of logic '1'. Thirdly, the DC analog circuits are enabled by setting all the ID lines high except for ID0, then clocking M20 by a low to high edge from M16-6 caused by both IA lines going low. Once DC has been selected, the F.E.T. pattern latch is enabled from M12-1, and the penultimate step is to load this latch with 1000V range data from the ID lines (ID4 low, the rest high). This is executed by clocking the 'F.E.T.' latch from M17-4 once again, but this time being due to IA1 going low. The final step is to reselect DC as described above.

3.2.1.3 General Interface Update Sequence

Before the start of each reading, the analog interface undergoes a complete update. The series of events is the same as the power-up sequence for selection of function and range, as can be seen by comparing the two flowcharts (Figs. 3.3 and 3.5). When Ohms or Current is selected, the DC Isolator or AC assembly is also used in the measurement procedure as seen in the following table.

Type of Measurement	Circuits Selected	Use of D - A
DC Volts	Analog Assembly	Input Bias Current Compensation
AC Volts	AC Assembly	Frequency Compensation
AC + DC Volts	AC Assembly	Frequency Compensation
Resistance	Ohms Assembly and Analog Assembly	Input Bias Current Compensation
DC Current	Current Assembly and Analog Assembly	Input Bias Current Compensation
AC Current	Current Assembly and AC Assembly	Frequency Compensation
AC + DC Current	Current Assembly and AC Assembly	Frequency Compensation

The update sequence order is (i) Deselect all assemblies, (ii) Load D - A latches, (iii) Select AC assembly or DC Isolator, (iv) Load range pattern into DC or AC range latches, (v) Deselect DC or AC and select either the Ohms or Current assembly, (vi) Load range pattern into Ω's or I range latches, (vii) Reselect circuits selected in (iii) and (iv).

Note: Steps (v) and (vi) are used only when I or Ω is selected.

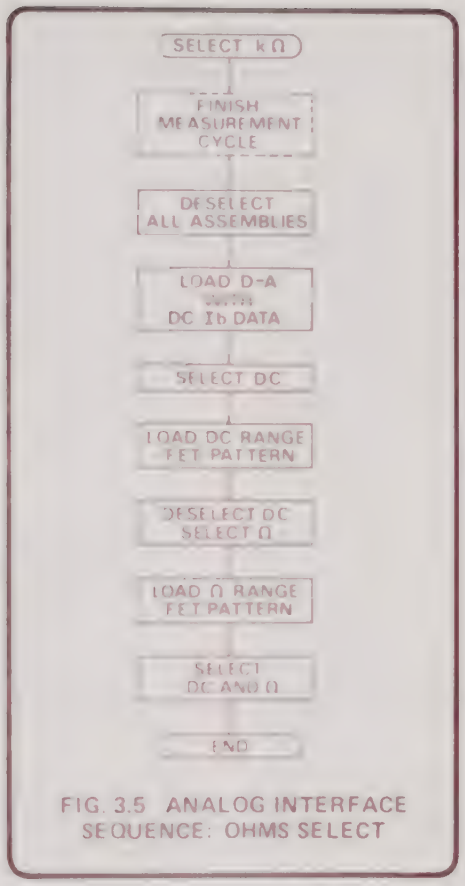


FIG. 3.5 ANALOG INTERFACE SEQUENCE: OHMS SELECT

Flowchart 3.5 gives the above sequence for an ohms update. The general form of the timing diagram for the above sequence is given in Fig. 3.6, the analog 'F.E.T.' patterns for each range of each function being given in Appendix 1

3.2.1.4 Test

When TEST is selected, a logic '0' is placed on ID7 at stages (iii), (v) and (vii) in Fig. 3.6, i.e. each time a function measurement circuit is selected. Appendix 1 lists the 'F.E.T.' patterns of each assembly for each test measurement cycle.

3.2.2 DC Isolator Section

3.2.2.1 Preamplifier Scaling (430328 sheet 1)

Figure 3.8 shows the essential features of the isolator scaling circuit. For the purpose of explanation the same symbols are used, regardless of whether the switching is accomplished electronically (F.E.T.) or by means of relay contacts. In Fig. 3.8 all switches are shown in the 1V RANGE position.

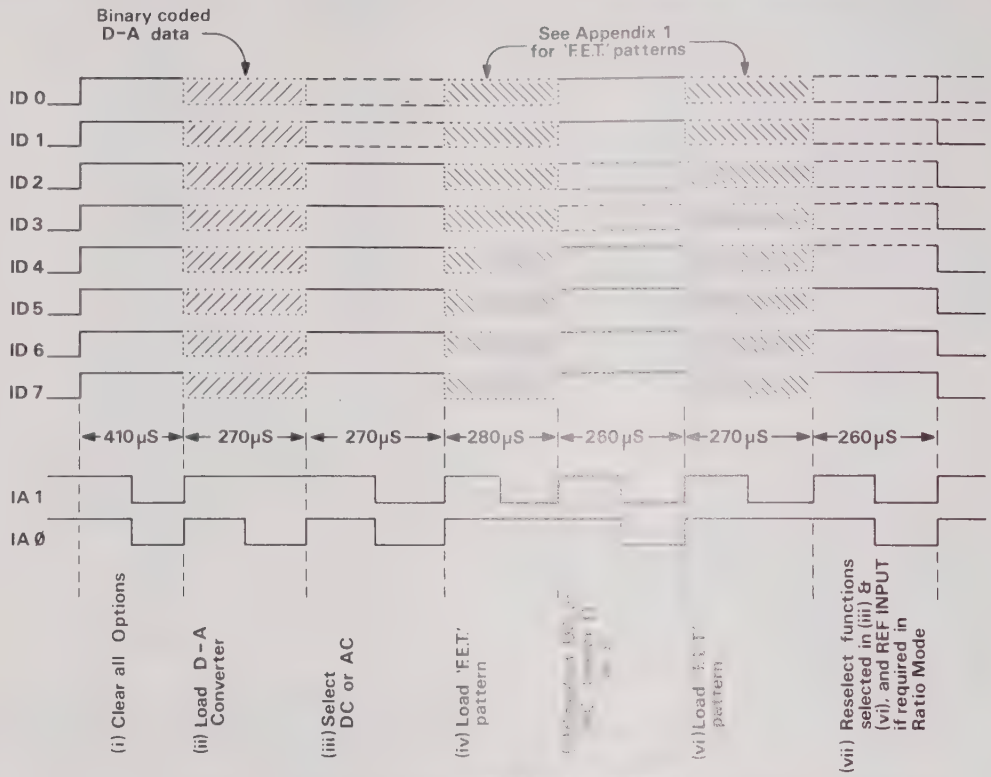


FIG. 3.6 GENERAL FORM OF ANALOG INTERFACE GATE TIMING DIAGRAM

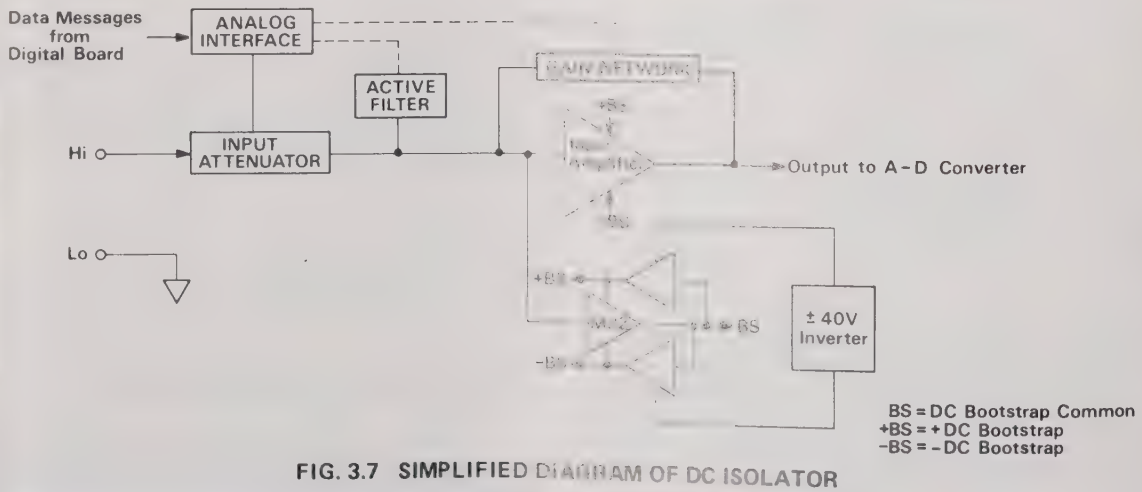


FIG. 3.7 SIMPLIFIED DIAGRAM OF DC ISOLATOR

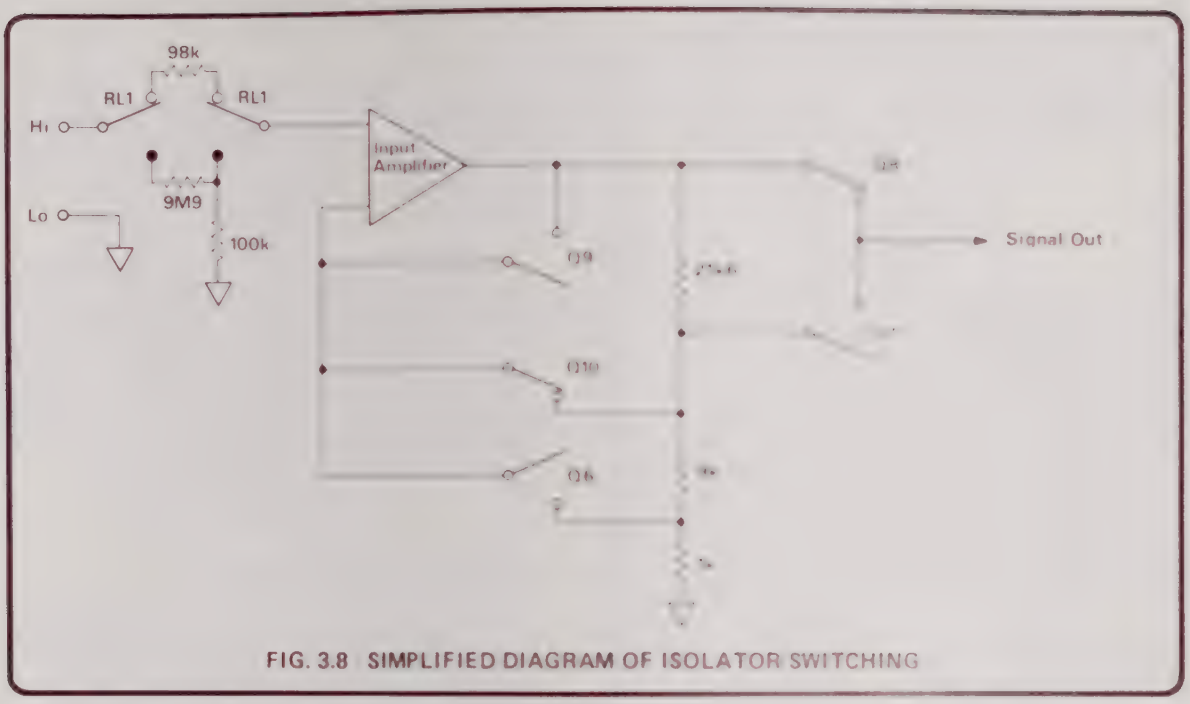


FIG. 3.8 SIMPLIFIED DIAGRAM OF ISOLATOR SWITCHING

The various switching combinations for the different ranges are as follows:–

Range	Gain	Q6	Q7	Q8	Q9	Q10	RL1
100mV	x31.6	ON	OFF	ON	OFF	OFF	ON
1V	x3.16	OFF	OFF	ON	OFF	ON	ON
10V	÷3.16	OFF	ON	OFF	ON	OFF	ON
100V	÷31.6	OFF	OFF	ON	OFF	ON	OFF
1000V	÷316	OFF	ON	OFF	ON	OFF	OFF
$\overline{DC}$		OFF	OFF	OFF	ON	OFF	OFF

The configuration of the circuit for each range is shown in Fig. 3.9.

Reference should be made to circuit diagram number 430328, sheet 1, for the complete circuit. Sheet 2 gives tables of the coding on the input control lines (from the Analog Interface).

When the 100V or 1kV range is selected, a ÷100, 10MΩ input attenuator (R143, R156, R149, R148) is incorporated into the circuit. This is a matched set of resistors for low temperature coefficient. The selection of a lower range energizes relay RL1 (via Q33), causing resistor chain R119-R122 to be in series with the Hi input. Should an overload signal then be applied, the resistor chain limits the current and the power dissipation is such that 1000V can be applied continuously.

The amplifier end of the resistors is clamped by zener diodes D22, D23 and Q18, Q19 to low, thus the amplifier input can never exceed approximately ± 24 volts.

The output from the DC Isolator, test point (TL8) is approximately 3.16 volts ($\approx \sqrt{10}$) for a full range (100,000) input by the following methods (See Fig. 3.9):–

100mV Range Q6 and Q8 are turned on, all other F.E.T.'s are turned off and RL1 energised. Thus the output of the amplifier is connected to its inverting input via R108, R109, R110, R111 and Q6, an attenuator chain of 31.6, giving the amplifier an overall gain of X 31.6 Q8 connects the preamplifier directly to the output.

1V Range Q10 and Q8 are turned on, all other F.E.T.'s are turned off and RL1 energised. The output of the amplifier is connected to its inverting input via R108, R109, R110, R111 and Q10, an attenuator chain of ÷3.16, giving the amplifier an overall gain of X 3.16. Q8 connects the preamplifier directly to the output.

10 V Range Q9 and Q7 are turned on; all other F.E.T.'s are turned off and RL1 energised. Q9 causes the amplifier output to be directly connected to its inverting input, giving a gain of unity. The output of the amplifier is attenuated by 3.16 (R114, R115) before being passed to the output via Q7 instead of Q8.

100V and 1000V Ranges These two ranges select the 1V and 10V ranges respectively but a ÷100 attenuator (R149, R156, R143, R148) is inserted between Hi and the preamplifier input when RL1 is de-energised.

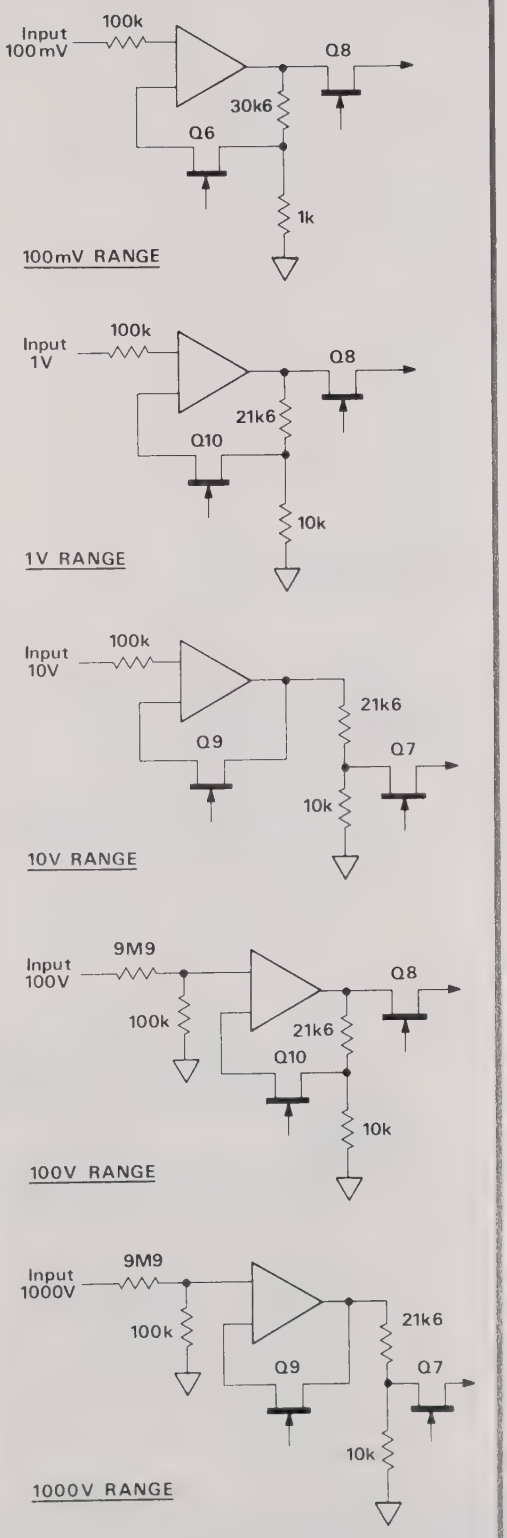


FIG. 3.9 PREAMPLIFIER GAIN CIRCUITS

3.2.2.2 Preamplifier (430328 sheet 1)

The preamplifier is designed to present an input impedance of greater than 10,000MΩ for signals up to ± 20 volts. It is also bootstrapped (tracking of both ground lines and supply voltages with input signal) being essential for correct operation of input bias compensation, temperature compensation and common mode rejection.

Q12 is a well matched monolithic NPN transistor pair exhibiting minimal voltage drift and low noise characteristics, the output being buffered by M31. To compensate for the current gain drift of Q12 (approx. $-1\%/^{o}C$), the change in the base-emitter voltage of one half of Q12 is sensed by M30. The drift compensation is linearised to 1% by thermistor R218. Thus the input bias current is kept constant with temperature.

3.2.2.3 D.C. Bootstrap (430328 sheet 2)

Bootstrapped supplies are generated which track the input signal directly (BS), track the input signal with a positive offset of +12V(+BS) and track the input signal with a negative offset of −12(−BS).

M32 is the high inpedance buffer which tracks the inverting input of the preamplifier. The offset of M32 is adjusted so that its input is within 100μV of the input of the preamplifier. M32 thus functions as the low impedance rail (BS) following the input signal.

Selection of DC(M20-3) enables the capacitive inverter driven from M33 to provide an unregulated +42V(TL4) and −42V(TL5) supply from the ±15V supply.

The positive bootstrap supply (+BS) is generated as a current source comprising Q26 and the shunt regulator, Q27, referenced to D50. When the output voltage of the regulator is approximately 1.2 volts above D50 cathode, Q27 supplies current into R175. Since the current in R175 is controlled to be constant by Q30, referenced to D50, the current flowing through R174 is reduced. Hence the base current, "mirrored" in R173, is reduced and the output voltage controlled.

The negative bootstrap supply (−BS) is generated in a similar manner. Thus bootstrapped supplies of approximately ±12 volts are produced, tracking the input signal within 1%.

3.2.2.4 Filtering (430328 sheet 1)

Selection of filter causes an active filter to be switched in by relay, RL2, (via Q32). The filter gives an attenuation of −34 dB at 50Hz. The essential components of the filter are shown in Fig. 3.10.

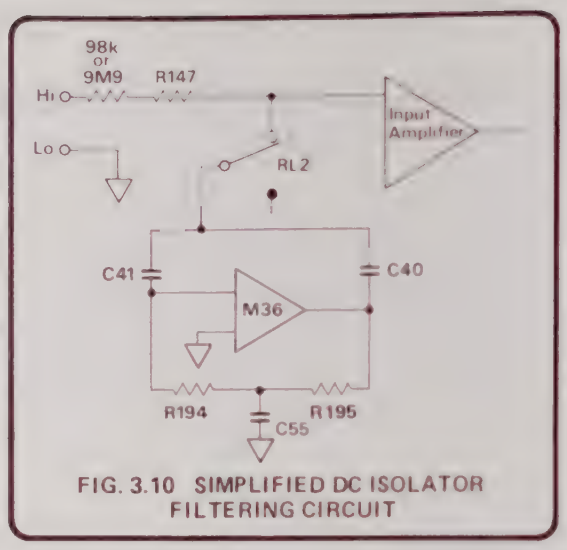


FIG. 3.10 SIMPLIFIED DC ISOLATOR FILTERING CIRCUIT

3.2.2.5 Input Current (Ib) Compensation (430328 sheets 1 and 5)

During the calibration cycle, the microprocessor notes and stores the zero error due to the bias current (measured in a known source resistor). When DC is selected, this information is recalled by the microprocessor, transferred across the isolation barrier and latched into M13 and M14, see Fig. 3.11.

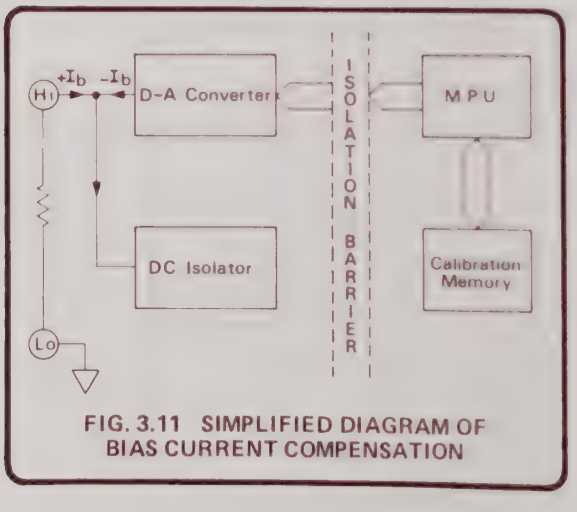


FIG. 3.11 SIMPLIFIED DIAGRAM OF BIAS CURRENT COMPENSATION

The output from the latches is applied to the binary resistor ladder network, AN2, providing a 255 step digital to analog conversion. The analog signal is applied to the inverting input of M3 so that the output drives current, through the diode, to control the current in the corresponding transistor of the opto-isolator, M23. The transistor of the opto-isolator sinks current to the −15V supply until the voltage across R198 is equal to the voltage applied to the inverting input of M3.

The other half of the opto-isolator acts as a current mirror, referenced to the bootstrap (BS) supply. Thus the input current correction is floated on the bootstrap supply, tracking the input signal is divided by R84 to R128 and R129 to null the bias current of the preamplifier.

3.2.2.6 Test (430328 sheets 1 and 5)

During the self test routine, (actuated from the front panel or remotely programmed) the DC isolator is checked for correct operation. The circuitry is placed into the 0.1V range, as described in 3.2.1.3, except that relay RL1 is not energized, (i.e. the ÷ 100 attenuator is across the input amplifier). Filter is selected and F.E.T. Q5 'closed' via M20.5 causing a small signal to be injected into the feedback path of the input amplifier. Thus a signal of −3.125 volts is output from the DC Isolator (TL8). This signal is then measured and compared with a stored value. If the measured signal is within ±6% of the stored value, the test continues with a 1V range check and a 10V range check.

Range	Output signal from DC Isolator (TP13)
0.1V	−3.125 volts
1V	−0.2193 volts
10V	−0.06932 volts

DC Isolator Output Test Voltages

3.2.3 Analog to Digital Conversion (Analog Section) (430328 sheets 3 and 4)

3.2.3.1 General Principles

Section 1 and Fig. 1.2 of the User's Handbook gives a very basic description of the principles of the integration involved. The technique used in the Autocal Voltmeter is a quadruple slope, the two extra slopes being towards the end of the signal and reference integration periods respectively.

Fig. 3.12 is a simplified diagram showing the essentials of the analog section of the A - D conversion and should be used with timing diagram Fig. 3.13 for full appreciation of the circuit operation.

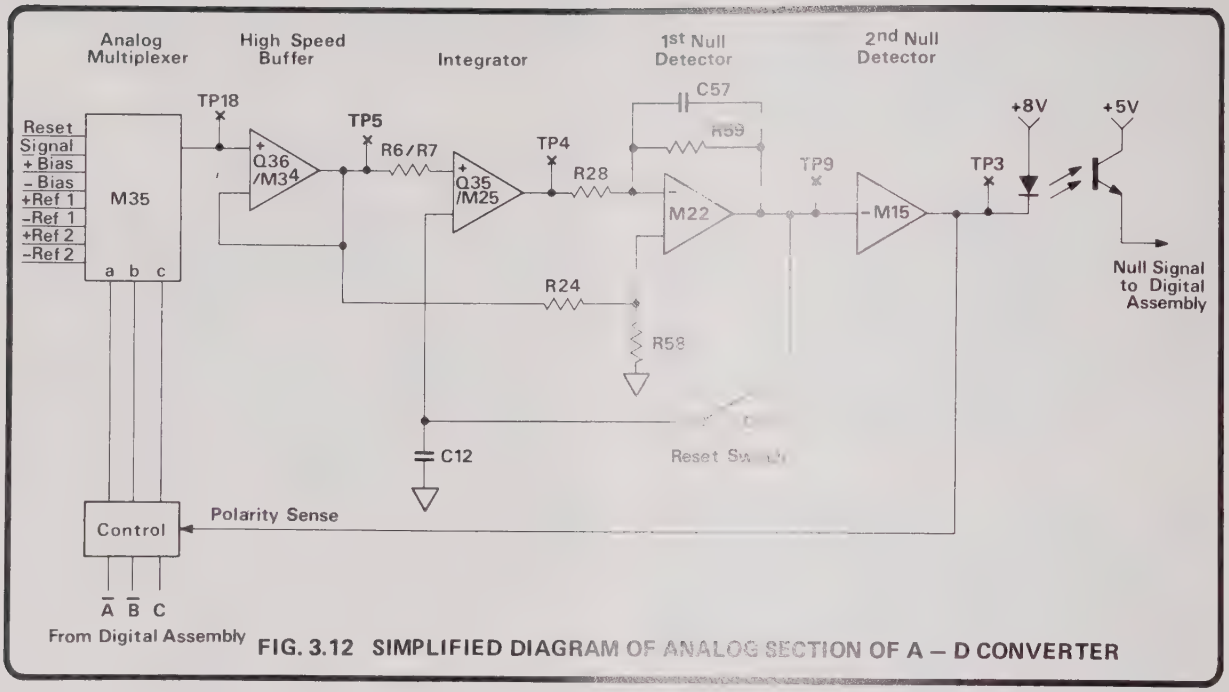


FIG. 3.12 SIMPLIFIED DIAGRAM OF ANALOG SECTION OF A – D CONVERTER

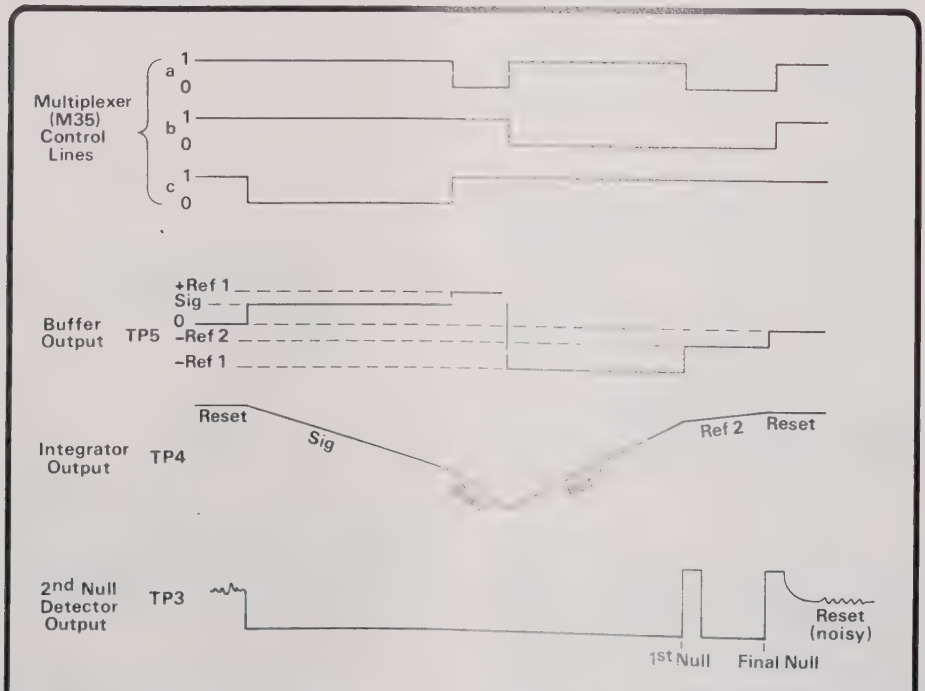


FIG. 3.13 TIMING DIAGRAM FOR ANALOG SECTION OF A – D CONVERTER
(Positive Signal)

3.2.3.2 A - D Input Control

The analog signal from the DC Isolator is applied to the analog multiplexer (M35) and fed to the input of the buffer (Q36/M34). This in turn feeds the signal to the integrator comprising of Q35, M25 and C9.

Control of the multiplexer is derived from the Digital assembly via opto-isolators M4, M5 and M6. These signals control the sequence of events, allowing first the signal, then a bias voltage of the same polarity as the signal, followed by opposite polarity reference and reference ÷16 signals to the buffer and integrator. The multiplexer is then placed in a reset condition ready for the next measurement cycle. Fig. 3.14 gives the multiplexer control line sequence for both positive and negative signals.

STATE	a	b	c	STATE	a	b	c
RESET	1	1	1	RESET	1	1	1
SIG	1	1	0	SIG	1	1	0
+ BIAS	0	1	1	–BIAS	0	1	0
–REF 1	1	0	1	+REF 1	1	0	0
–REF 2	0	0	1	+REF 2	0	0	0
RESET	1	1	1	RESET	1	1	1
	Positive signal				Negative signal		

Logic levels : (0 ≡ –8V, 1 ≡ +8V)

Fig 3.14 MULTIPLEXER CONTROL LINE SIGNALS

3.2.3.3 Reference Voltages and Control Logic Power Supply

REF 1 : The two halves of M39 in conjunction with zener diodes D60 and D59 form the positive and negative reference voltages respectively, D65 and D64 being 'start-up' diodes (see Fig. 3.15). The outputs of M39 (+11 and –11 volts) supply the defined current for the reference zeners via R212 and R38 respectively. R19 and R18 are selected by Datron so that each zener has zero voltage/temperature coefficient.

The resistor chains R42-R43 and R89-R90 are binary weighted values allowing the set up of the exact nominal REF 1 voltages, of ±6.42V, by cutting the appropriate links.

REF 2 The second reference is 1/16th of REF 1. The positive and negative REF 1 voltages are divided by R71, R68 and R214, R70 respectively.

The power supplies for the logic circuits M35, M29, M27, M28 and opto-isolators M1, M4, M5 and M6 are also derived from M39 via zener diodes D61 and D62, giving supply voltages of ± 8 volts.

FIG. 3.15 POSITIVE REFERENCE CIRCUITRY

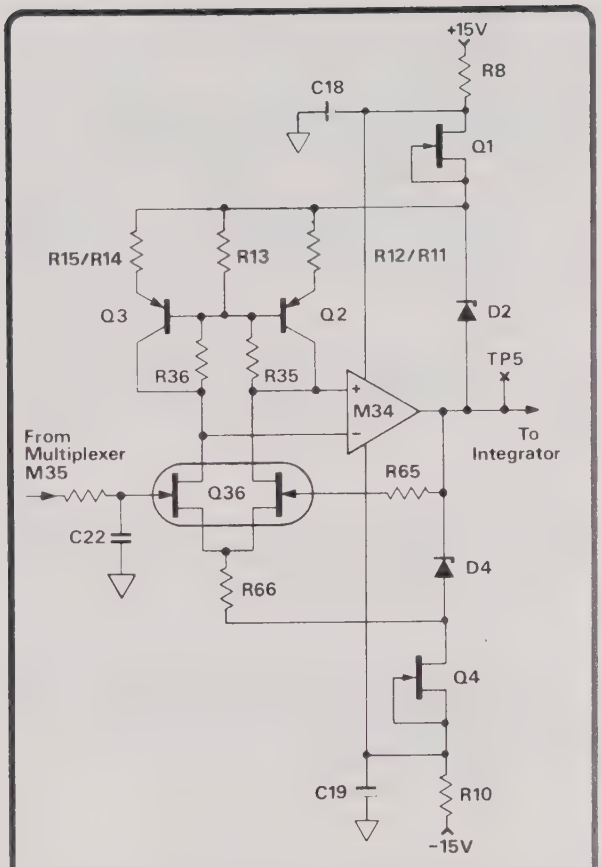


Fig. 3.16 HIGH SPEED BUFFER CIRCUITRY

3.2.3.4 High Speed Buffer

C22 slows the switching edges from the multiplexer M35 so that the buffer cannot slew-limit and thus lose the charge. The signals are fed to Q36, M34 which comprise a high speed buffer with high common mode rejection ratio (See Fig. 3.16). The common mode rejection is dependent on the power supplies of Q36 (from R66 and R11-R15) being bootstrapped to the output of the buffer, via D2 and D4. Thus the difference between input signal and power supply around the input stage is maintained constant whatever the input signal.

3.2.3.5 Integrator

The basic Integrator comprises R6, R7 and C9, with hybrid amplifier Q35 and M25. (See Fig. 3.17). Low-noise FET-pair Q35 also has low gate leakage, which maintains the effectiveness of 'sample-and-hold' components R34 and C12.

An inverted and attenuated version of the integrator output voltage is developed across R5. This is applied via R4 and C10 to compensate for the small amount of dielectric absorption in C9. The value of R5 is factory-selected to equalize readings of the same input, taken at different read-rates (including 'one-shot' measurements).

R31 and R27 provide shorter term compensation, R23 being set to correct linearity at 10% of full range.

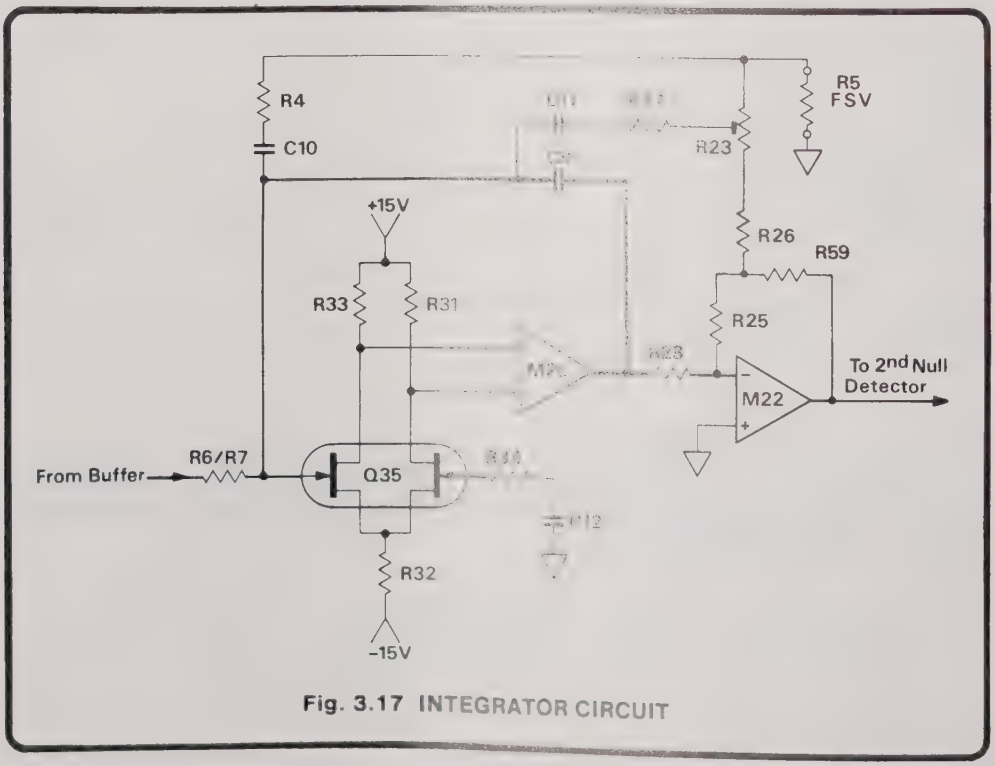


Fig. 3.17 INTEGRATOR CIRCUIT

3.2.3.6 1st Null Detector

The 1st null detector comprises a low noise amplifier, M22, an inverting configuration, where the dc gain is controlled by the ratio of R59 to R28 for small inputs. For larger inputs from the integrator the clamp diodes, D1 and D3, prevent the amplifier from saturating.

During REF 1 the non-inverting input is offset by approximately 10mV to determine the point at which REF 2 is applied (after counting is synchronised). In REF 2 the offset reduces by a factor of 16 giving the null reference point.

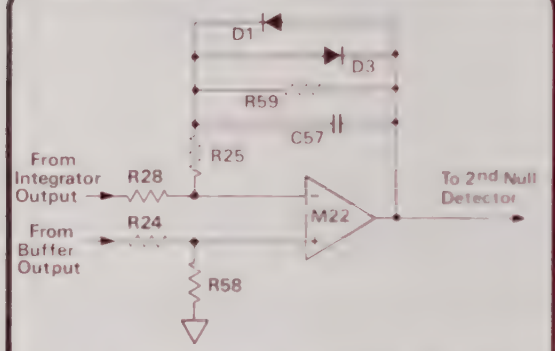


FIG. 3.18 1st NULL DETECTOR CIRCUITRY

3.2.3.7 2nd Null Detector

The signal from the 1st null detector is applied to M15 which boosts the voltage gain. The output provides a logic drive signal via opto-isolator M1, signalling the digital circuitry whenever a null condition changes, Fig. 3.19.

When in an averaging mode (Input Zero or CAL Zero selected; or for 1061A only, with 'Input Filter' and DC, AC Option 12, or Ohms selected) the second null detector is offset a small amount in a cycle of 16 steps (See Fig. 3.20). This offset is produced from the digital to analog converter M28, which is enabled by the level-shifted $\overline{AVE}$ signal from M20-5, and clocked from M6, the C control opto-isolator.

3.2.3.8 Reset Period

At the end of a measurement cycle or in hold, the circuitry is placed into a reset condition. The control lines of the multiplexer M35 allows the 0 volts reference input, at pin 4, to be connected to its output. (See Fig. 3.21). At the same time the reset line (M27-3) is taken high turning on M26. This reset signal, applied to pins 5 and 12 of M26, allows the output of the 1st null detector to be fed back via R60 to a sample and hold capacitor C12 on the integrator.

Thus, with the input to the A - D converter at zero volts, the charge stored on C12 is the sum of all the offsets from the multiplexer, buffer, integrator and 1st null detector, allowing the 1st null detector to indicate the true zero crossing (null) point.

The reset signal applied to M26 pins 6 and 13 merely allows a lower impedance path between the buffer and the integrator to speed up the settling time as C9 is discharged to zero.

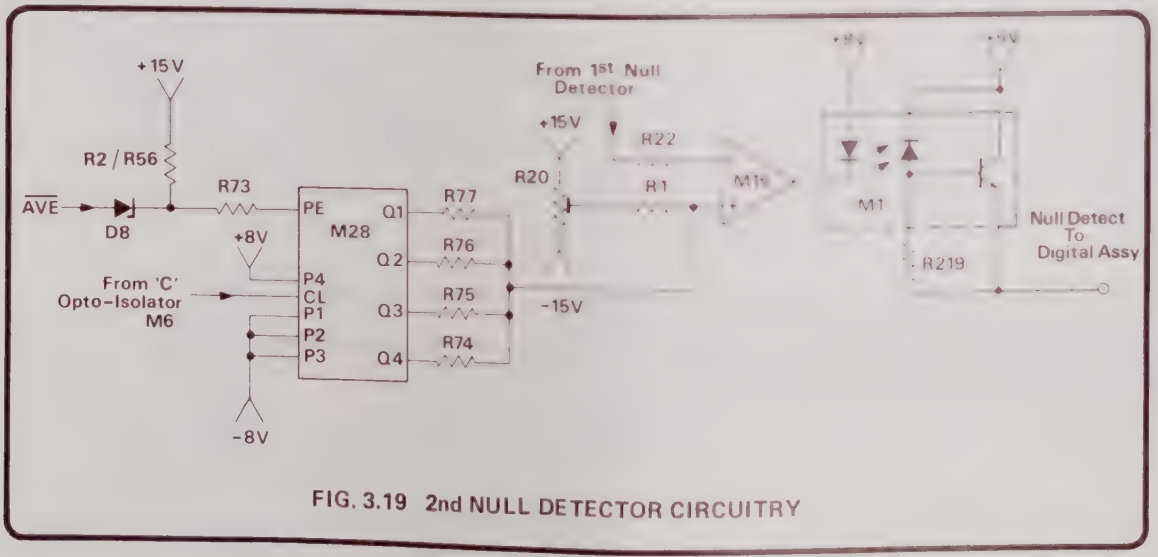


FIG. 3.19 2nd NULL DETECTOR CIRCUITRY

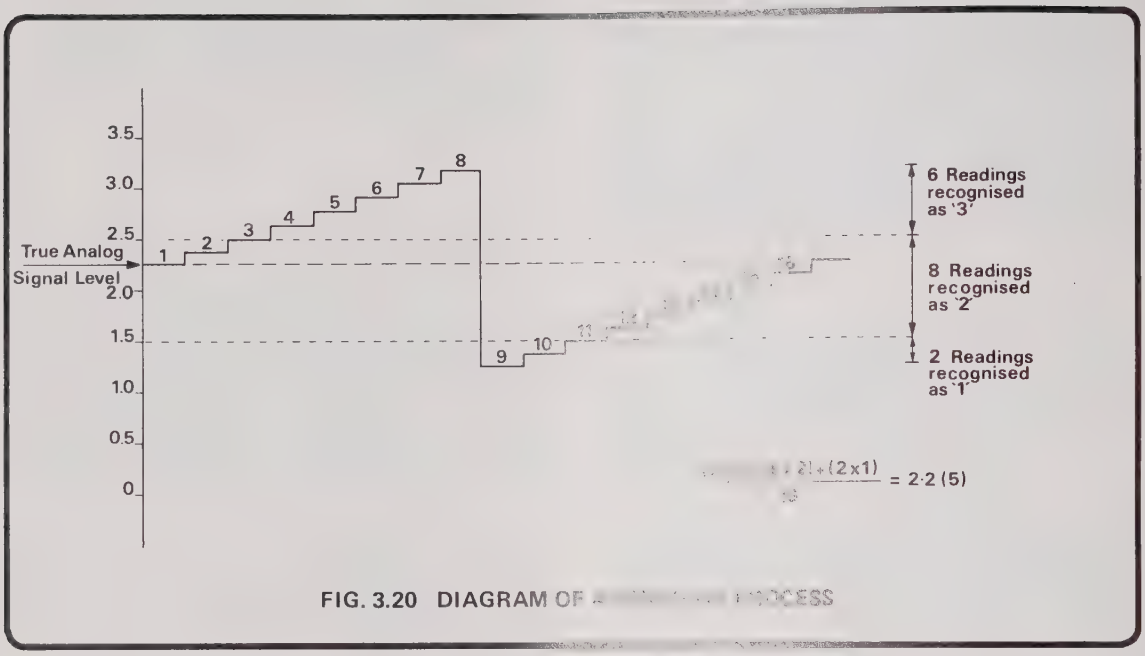


FIG. 3.20 DIAGRAM OF A [illegible] PROCESS

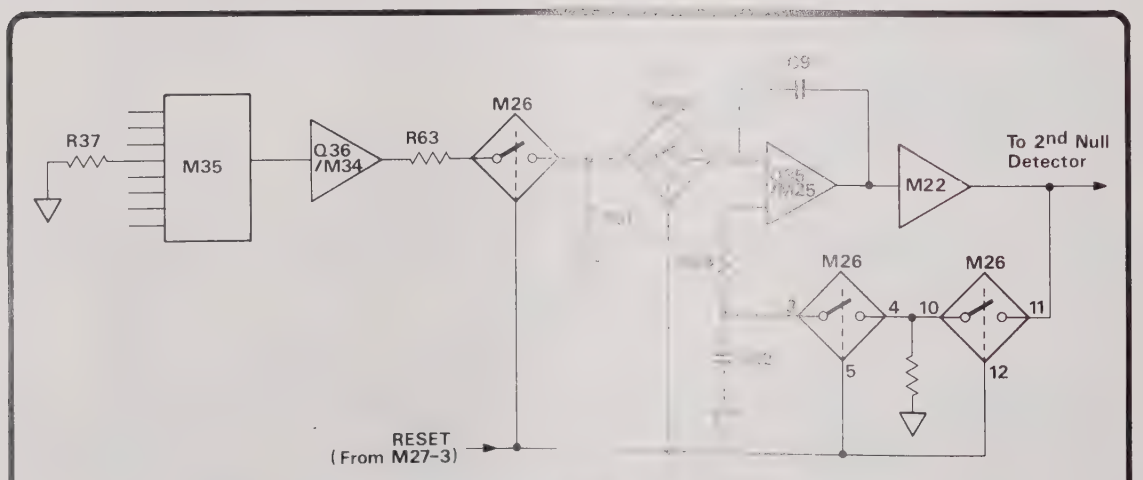


FIG. 3.21 SIMPLIFIED DIAGRAM OF A - D DURING RESET PERIOD

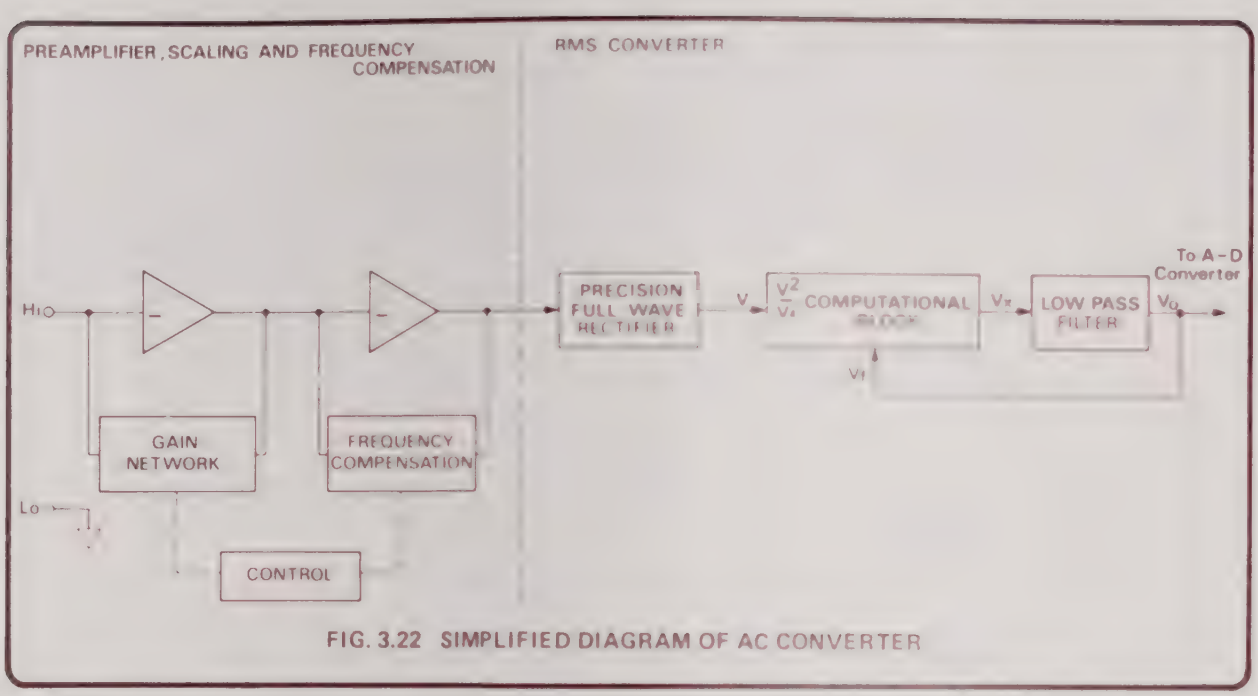


FIG. 3.22 SIMPLIFIED DIAGRAM OF AC CONVERTER

3.3 OPTION 10 AC ASSEMBLY (Circuit Drawing No. 430402)

(For OPTION 12 see page A27)

3.3.1 General Principles

The preamplifier buffers and ranges the signal in order to present 0.9 volts full range to the AC to DC converter section.

Once converted to an equivalent DC signal, it is applied to the analog to digital converter on the main analog assembly.

The conversion technique is electronic true RMS sensing as shown in the simplified block diagram Fig. 3.22. The Datron RMS module can be best considered as a functional block consisting of circuitry which accepts two inputs, V and V_f, computes V^2/V_f and has an output of V_x which is then filtered so that all the AC components are removed. The output of the block is fed back to V_f, thus closing the loop around the whole circuitry.

Mathematically: $\overline{V_x} = V_o$

but $V_x = V^2/V_f$

$\overline{V^2/V_f} = V_o$, but $V_o = V_f$

$\overline{V^2} = V_o^2$

i.e. $\underline{V_o = \sqrt{\overline{V^2}}}$

3.3.2 Preamplifier and Scaling (430402 sheet 1)

Relay RL2 is energised on selection of AC, directly connecting the Hi terminal to the input of the AC assembly. If DC and AC are selected together, the AC assembly becomes DC coupled by energising RL3, causing C57, the AC coupling capacitor, to be by-passed.

The signal is then fed to the switched gain inverting preamplifier whose full range output is 0.9 volts r.m.s. A simplified diagram of this arrangement is shown in Fig. 3.23. The frequency response is held flat, to within ±1%, by controlling the gain defining component time constants, to a similar order of accuracy. Residual errors are removed by the frequency compensation stage. (See section 3.3.4).

The preamplifier has a stable DC path provided by a dual transistor pair Q33 and a fast AC path by dual F.E.T.'s Q32 and Q34. Further gain is provided by the following long-tail pair cascade of Q20, Q21, Q22 and Q23, which is loaded by a current mirror, Q24. Q15 and Q16 with bias components Q17 and Q18 form a conventional class AB output stage. R121 compensates for the bias current of Q33, whileR112 trims the offset voltage to zero.

The unity gain frequency compensation amplifier consists of a stable DC path, provide by M11, and a fast AC path provided by Q25 to Q29. The bootstrap circuit of Q19 presents the varicap diode, D11, with a high impedance, thus ensuring that the varicap is not shunted to ground.

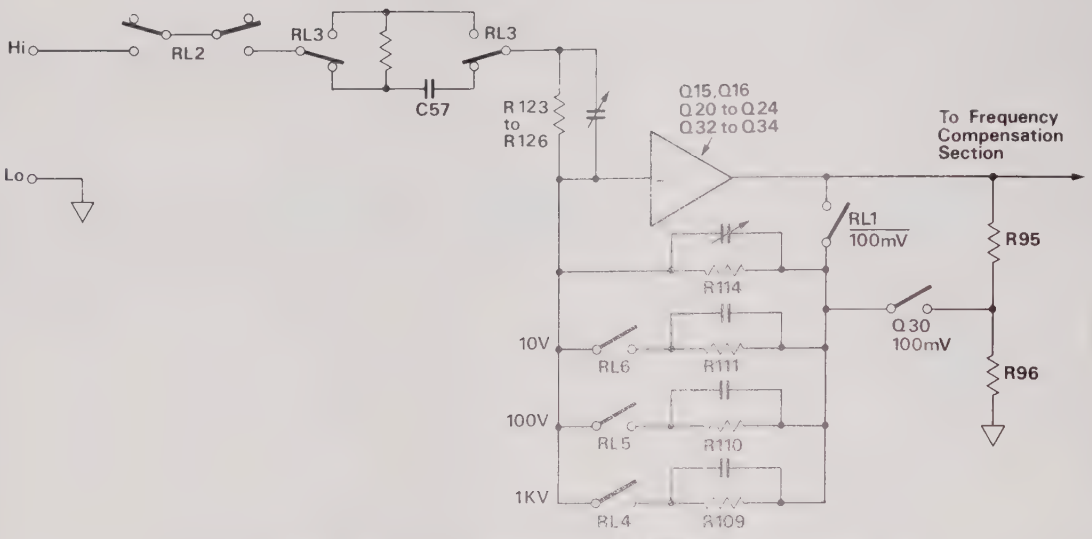


FIG. 3.23 SIMPLIFIED DIAGRAM OF THE AC PREAMPLIFIER SCALING

3.3.3 RMS Converter (430402 sheet 2)

The RMS converter takes the scaled AC signal from the preamplifier and converts it to an equivalent DC signal suitable for Analog-to-Digital conversion. The conversion technique is electronic true RMS sensing as shown in the simplified block diagram Fig. 3.25.

M8 and M9 form a summing type, full wave rectifier. The output of M8, a precision half-wave rectifier inverter, is summed with the non-inverted signal with a weighting of 2 : 1 at the input of M9. This forces a full-wave rectified current to flow in RMS module M6. Potentiometer R50 balances the rectifier to provide the same output for non-inverted or inverted asymmetric waveforms.

The output current from the RMS module passes into filter buffer M1 and is converted to a nominal 5 volts for a full range signal. Q1 and Q2 switch in additional capacitors when FILTER is selected, to operate down to 45Hz. M6 is a voltage to current converter providing a feedback current to the RMS module proportional to the output voltage. R90 is the zero adjustment for the half wave rectifier M8 and R35 is the high crest factor gain adjustment. R75 is adjusted for optimum linearity.

The output of M1 (TP2) is fed to a resistor chain R1 - R7, to provide an output of 3.14 volts by the selection of resistors R2 - R5. Q3 is turned on when AC is selected and switches the output of the AC converter into the Analog-to-Digital Converter (Drawing No. 430328 sheets 3 and 4)

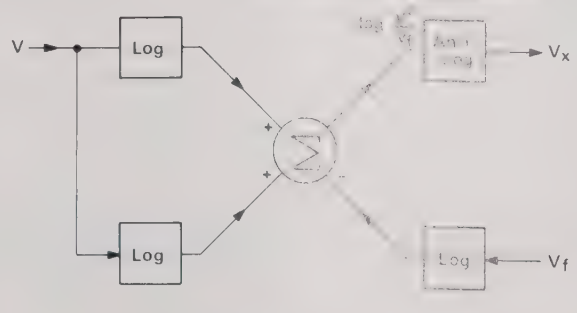


FIG. 3.25 BLOCK DIAGRAM OF RMS CONVERSION TECHNIQUE

3.3.4 High Frequency Compensation

During the calibration cycle, the microprocessor notes and stores the high frequency (HF) error of each range. When AC volts is selected the compensation information for a particular range is recalled by the microprocessor, transferred across the isolation barrier and latched on to M13, M14 (Drawing No. 430328 sheet 5), see Fig. 3.26. As in the case of the Input Current Compensation (section 3.2.2.5), the output from the latches is applied to a digital-to-analog converter, AN2. The voltage produced is fed to the AC converter via connector J1 pin 11 and applied to varicap D11. The varicap is thus adjusted to give the amplifier chain a flat frequency response.

The calibration is carried out at one H.F. frequency but since it flattens the AC amplifier response, the correction is valid for all specified frequencies. It should be noted that the calibration routine is iterative since the varicap is non linear.

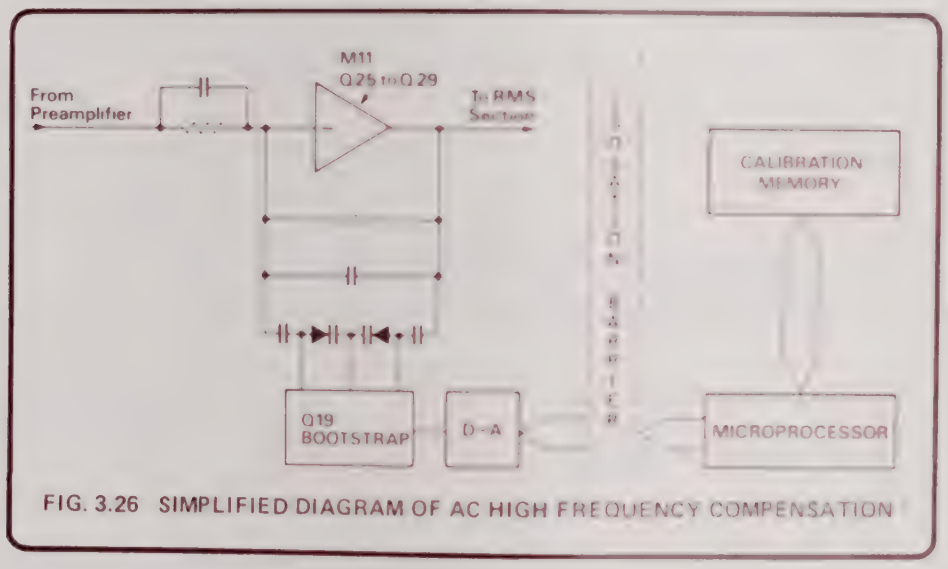


FIG. 3.26 SIMPLIFIED DIAGRAM OF AC HIGH FREQUENCY COMPENSATION

3.3.5 Frequency Detection (430402 sheet 2)

The signal frequency is monitored by M10 which is set so that a signal frequency greater than 5kHz causes a logic '1', (0 volts) on M10 - 4. This signal indicates to the Digital Board via M18, M2 (Drawing No. 430328 sheet 5) which one of the two sets of specifications should be used for calculating the measurement uncertainty when the Spec key is depressed.

3.3.6 Test

During the self-test routine (actuated from the front panel or remotely programmed) the AC assembly is checked for correct operation. The circuitry is placed into the .1V range as described in Section 3.2.1.3. Filter is selected and F.E.T. Q31 is 'closed' from M5 - 13 causing a signal of 0.08 volts DC to be injected into the preamplifier. Thus a signal of approximately 3.14 volts is output from the RMS section and applied to the A - D converter situated on the Analog assembly. This signal is then measured and compared with a stored value. If the measured signal is within ±6% of the stored value, the test continues with a 1V range check.

Range	Output from RMS section
.1	+3.14 volts
1	+0.314 volts

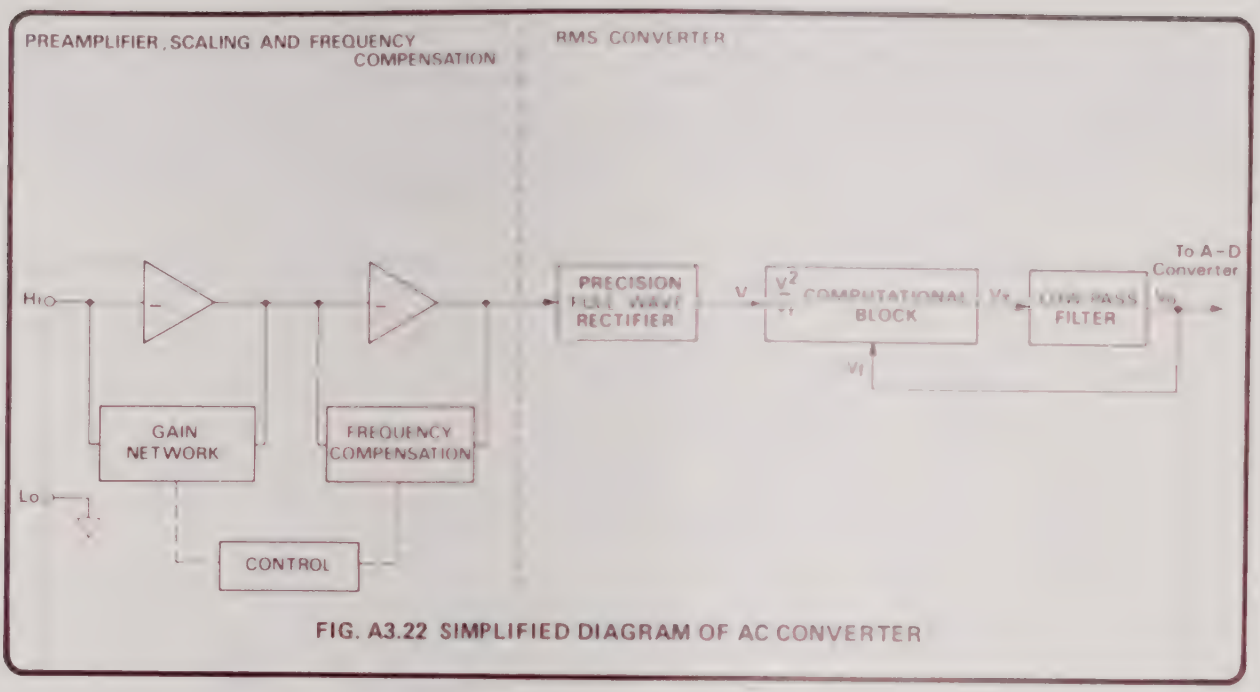


FIG. A3.22 SIMPLIFIED DIAGRAM OF AC CONVERTER

A3.3 OPTION 12 AC ASSEMBLY
(Circuit Drawing No. 430552)

(For OPTION 10 see page 27)

A3.3.1 General Principles

The preamplifier buffers and ranges the signal in order to present 0.9 volts full range to the AC to DC converter section.

Once converted to an equivalent DC signal, it is applied to the analog to digital converter on the main analog assembly.

The conversion technique is electronic true RMS sensing as shown in the simplified block diagram Fig. A3.22. The Datron RMS module can be best considered as functional block consisting of circuitry which accepts two inputs, V and V_f, computes V^2/V_f and has an output of V_x which is then filtered so that all the AC components are removed. The output of the block is fed back to V_f, thus closing the loop around the whole circuitry.

Mathematically: $\overline{V_x} = V_o$

but $V_x = V^2/V_f$

$\overline{V^2}/V_f = V_o$, but $V_o = V_f$

$\overline{V^2} = V_o^2$

i.e. $\underline{V_o = \sqrt{\overline{V^2}}}$

A3.3.2 Preamplifier and Scaling (430552 sheets 1 & 2)

When the AC option is selected, the AC preamplifier is connected in parallel with the 1000 Volt range of the DC isolator. The resultant impedance presented at the input terminals is a resistance of 1MΩ, shunted by 150pF.

Relay RL2 is energized on selection of AC, directly connecting the Hi terminal to the input of the AC assembly. If DC and AC are selected together, the AC assembly becomes DC coupled by energizing RL3, causing C77, the AC coupling capacitor, to be by-passed.

The signal is then fed to the switched gain inverting preamplifier whose full range output is 0.9 volts r.m.s. A simplified diagram of this arrangement is shown in Fig. A3.23. The frequency response is held flat, to within ±1%, by controlling the gain defining component time constants, to a similar order of accuracy. Residual errors are removed by the frequency compensation stage. (See section 3.3.4).

The main amplifier M22 responds to signals from DC to above 1MHz. Its input buffer Q36 reduces bias current errors. A chopper-stabilized amplifier M21 nulls the offset of Q36. Filter components R123 and C90 eliminate the effects of current 'kickback' from M21 to the main signal path. M22 output (Test link TLK) is fed directly to the unity gain frequency compensation stage.

C88 and C89 decouple R160 and R162 except on the 100mV range, when Q33 and Q34 are switched off to provide greater open loop gain. To ensure stability at the higher feedback levels required for the 10V, 100V, and 1000V ranges; C73 is switched in by Q32 to decouple M22 non-inverting input, further reducing the open loop gain.

The unity gain frequency-compensation amplifier includes a stable DC path M20, and a fast AC path Q28 and Q29. The capacitance of varicap diode D14 is determined by the bias voltage at J1-11. The bootstrap circuit of Q17/Q21 ensures that both halves of the varicap are subjected to the same AC signal, removing the non-linearity of the voltage-capacitance characteristic.

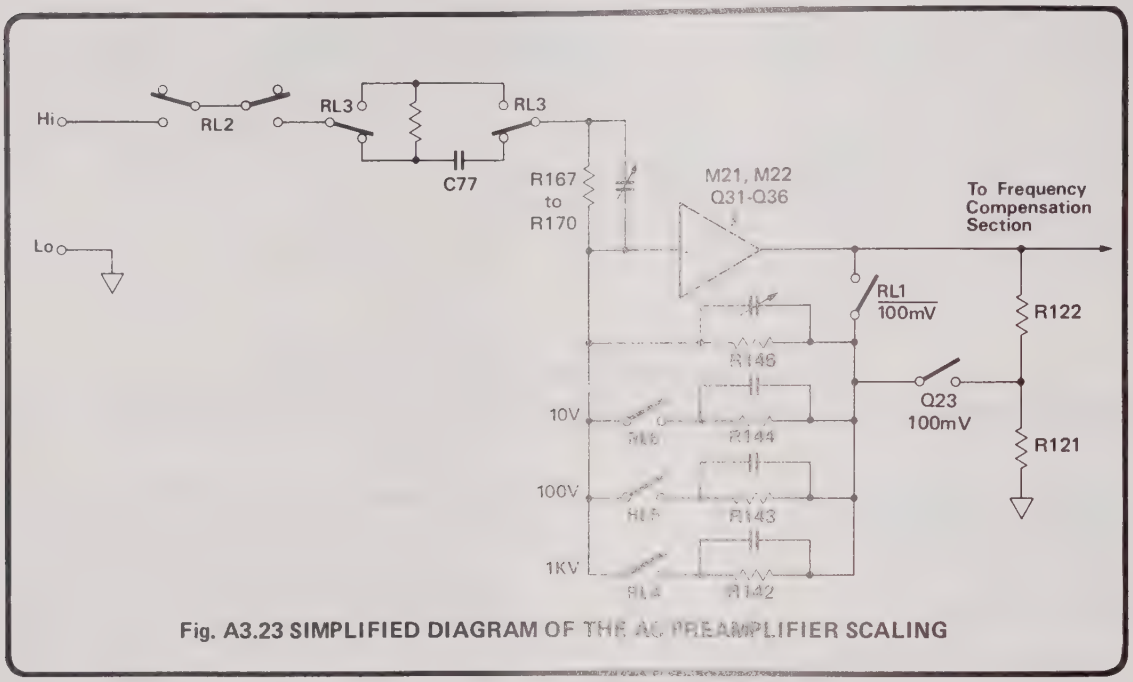


Fig. A3.23 SIMPLIFIED DIAGRAM OF THE AC PREAMPLIFIER SCALING

A3.3.3 RMS Converter (430552 Sheets 2 & 3)

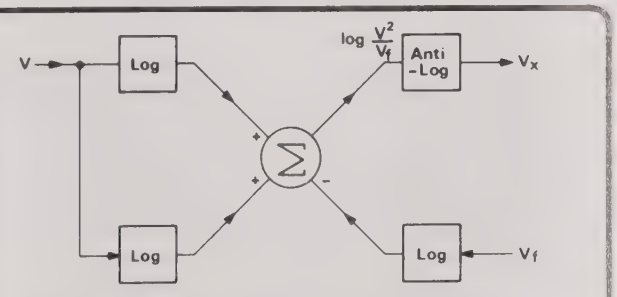


Fig. A3.25 BLOCK DIAGRAM OF RMS CONVERSION TECHNIQUE

The RMS converter takes the scaled AC signal and converts it to an equivalent DC signal suitable for Analog-to-Digital conversion. The technique used is Electronic True RMS Sensing as shown in the simplified block diagram Fig. A3.25.

M13 and M14 form a summing full-wave rectifier. The output of precision half-wave rectifier M13 is summed with the non-inverted signal at the input of M14, with a weighting of 2:1. This forces an accurately rectified full-wave current to flow in RMS module M11. Potentiometer R02 adjusts the rectifier symmetry to provide the same output for signals of either polarity.

The output current from the RMS module drives the low pass current-to-voltage converter M10/M13, which generates a nominal 0.5 Volts for a full range signal. (Note that M10, M9 and M4 are chopper-stabilized amplifiers to handle the low signal voltages).

M16 is the active element of a switched 3-pole Bessel filter. M15 and M17 switch the time constants, extending the overall low frequency response down to 10Hz (See Fig. A3.24), when 'Filter' is selected.

The high impedance output from the 3-pole filter is buffered by M9/M2, and the other half of M2 provides a bootstrap for M9 input. D26 and D16 prevent the voltage on TL A from exceeding the +5V power rail, providing overload protection.

The buffer output voltage (3.12V full range) is developed across R52-R56 and R70, referred to Output Common at M4 input. Log-feedback stage M4/M3 closes the 'Square-Root' loop, providing feedback current for the RMS computation in M11.

When the AC, or DC-coupled AC option is selected, Q3 connects the buffer output to the Analog-to-Digital converter. Test links TLC, D, E and F are selectively removed at manufacture to set the correct output level.

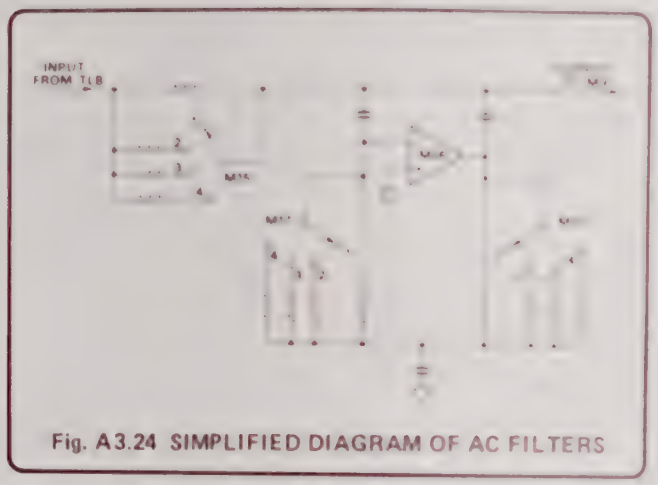


Fig. A3.24 SIMPLIFIED DIAGRAM OF AC FILTERS

A3.3.4 High Frequency Compensation

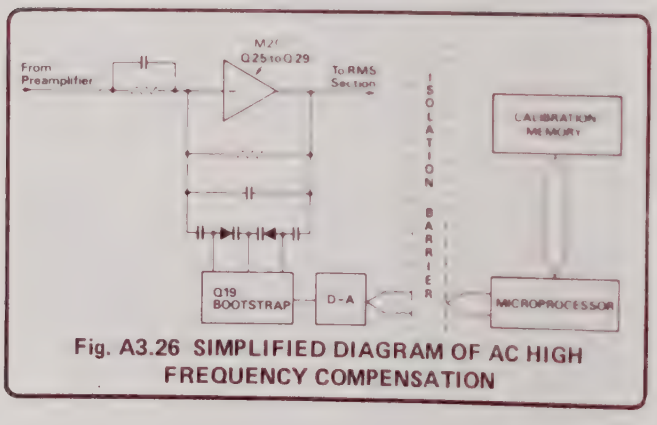


Fig. A3.26 SIMPLIFIED DIAGRAM OF AC HIGH FREQUENCY COMPENSATION

During the calibration cycle, the microprocessor notes and stores the high frequency (HF) error of each range. When AC volts is selected the compensation information for a particular range is recalled by the microprocessor, transferred across the isolation barrier and latched on to M13, M14 (Drawing No. 430328 sheet 5), see Fig. 3.26.

The output from the latches is applied to a digital-to-analog converter, AN2. The voltage produced is fed to the AC converter via connector J1 pin 11 and applied to varicap D14. The varicap is thus adjusted to give the amplifier chain a flat frequency response.

The calibration is carried out at one H.F. frequency but since it flattens the AC amplifier response, the correction is valid for all specified frequencies. It should be noted that the calibration routine is iterative since the varicap is non-linear.

A3.3.5 Frequency Detection (430552 sheet 2)

The signal frequency is monitored by M10 which is set so that a signal frequency greater than 2kHz causes a 'logic '1', (0 volts) on M19-4. This signal indicates to the Digital Board via M18, M2 (Drawing No. 430328 sheet 5) which one of the two sets of specifications should be used for calculating the measurement uncertainty when the Spec key is depressed.

A3.3.6 Test

During the self-test routine (actuated from the front panel or remotely programmed) the AC assembly is checked for correct operation. The circuitry is placed into the .1V range as described in Section 3.2.1.3. F.E.T. Q31 is 'closed' from M7-13 causing a signal of 0.08 volts DC to be injected into the preamplifier. Thus a signal of approximately 3.14 volts is output from the RMS section and applied to the A - D converter situated on the Analog assembly. This signal is then measured and compared with a stored value. If the measured signal is within ±6% of the stored value, the test continues with a 1V range check.

Range	Output from RMS section
.1	+3.14 volts
1	+0.314 volts

3.4 OHMS ASSEMBLY (Circuit Drawing No. 430331)

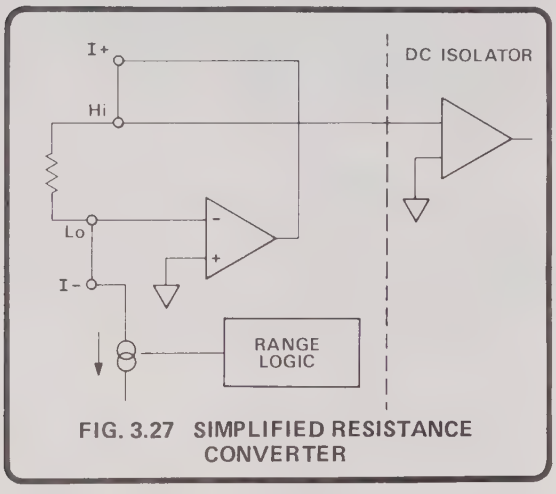


FIG. 3.27 SIMPLIFIED RESISTANCE CONVERTER

The instrument functions by measuring the voltage across an unknown resistance with a known constant current flowing in it. The converter can be split into two parts: a low drift voltage follower and a constant current source covering 6 decades from 100nA to 10mA (see Fig. 3.27).

It should be noted that when the Ohms assembly is fitted the DC Isolator Lo is no longer directly connnected to the front/rear panel Lo terminal, but goes via RL1 on the Ohms assembly (connector link removed on side panel). Lo becomes an active terminal in resistance measurements.

3.4.1 Low Drift Voltage Follower

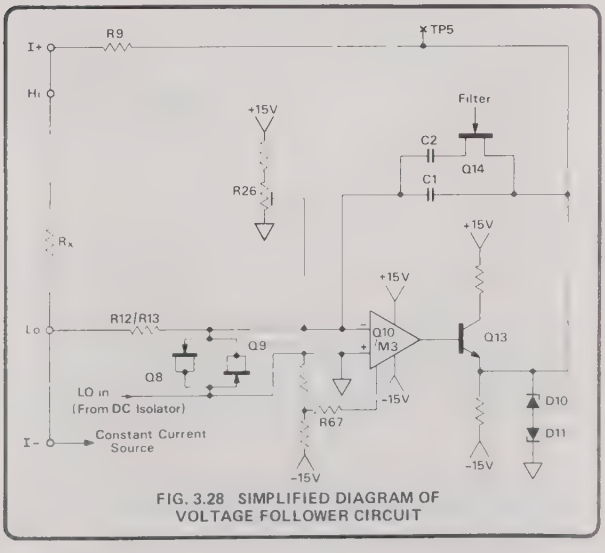


FIG. 3.28 SIMPLIFIED DIAGRAM OF VOLTAGE FOLLOWER CIRCUIT

When OHMS is selected, the front panel Lo terminal is connected to the –ve input of amplifier Q10/M3, the +ve input being referred to DC isolator Lo (this remains reference common). Q10/M3 together with output follower Q13, will thus apply a voltage at the I+ terminal via RL1 such that the voltage at front panel Lo is at reference common plus any offset due to Q10/M3. This voltage offset drift is kept small for changes of temperature by compensating the input bias current of Q10 with the current in R67, which changes with temperature due to the voltage drift at Q10 emitters. Q10 input bias current is initially nulled by R26.

Thus if we consider 2-wire measurement, I+ is linked to Hi, I– is linked to Lo and the unknown resistance linked between Hi and Lo, with a constant current flowing from I+/Hi, through the unknown resistance (R_X) to Lo/I–. The Lo terminal is maintained at 0V. Therefore the Hi terminal (DC Isolator Input) is at $I_{constant} \times R_X$ volts above Lo. As long as the error is small referred to reference 0, the DVM will read the correct resistance.

Input protection is provided as follows:–

Voltage/Current applied to input terminals:

I+	R9, D10, D11
I–	R2, D1, D2, Q25, R23
Lo	R12, R13, Q8, Q9

Open circuit voltage limit protection:

I+	R15, R16, Q6, Q7
I–	R6, D7, D8, Q2, Q22

3.4.2 Constant Current Source

Seven decades of ohms ranges are provided by 6 ranges of current and 2 ranges of DC Isolator voltage gain (100mV range for 10Ω, 1V range otherwise). See Fig. 3.29.

When kΩ's is selected, Q17 (sheet 2) is turned on enabling astable M6 to produce a 200Hz signal to switch M5. Thus when gates B and C of M5 are open, C9 is charged up from the negative reference (originating from the analog section of the A - D converter). These gates then close and A and D open, sharing the charge with C8, the voltage across C8 equals the reference voltage (sheet 1).

Range	Current	F.E.T.'s/Switches turned on	
		Current Selector	Leakage path
10Ω	10mA	Q11, M2(A)	
100Ω	10mA	Q11, M2(A)	
1kΩ	1mA	M1(A)	
10kΩ	100µA	M1(B)	
100kΩ	10µA	Q4	M2(B)
1MΩ	1µA	Q1, M1(D)	Q3, M2(C)
10MΩ	100nA	Q1, M1(C)	Q3, M2(C)

FIG. 3.29 OHMS CURRENT RANGE SWITCHING

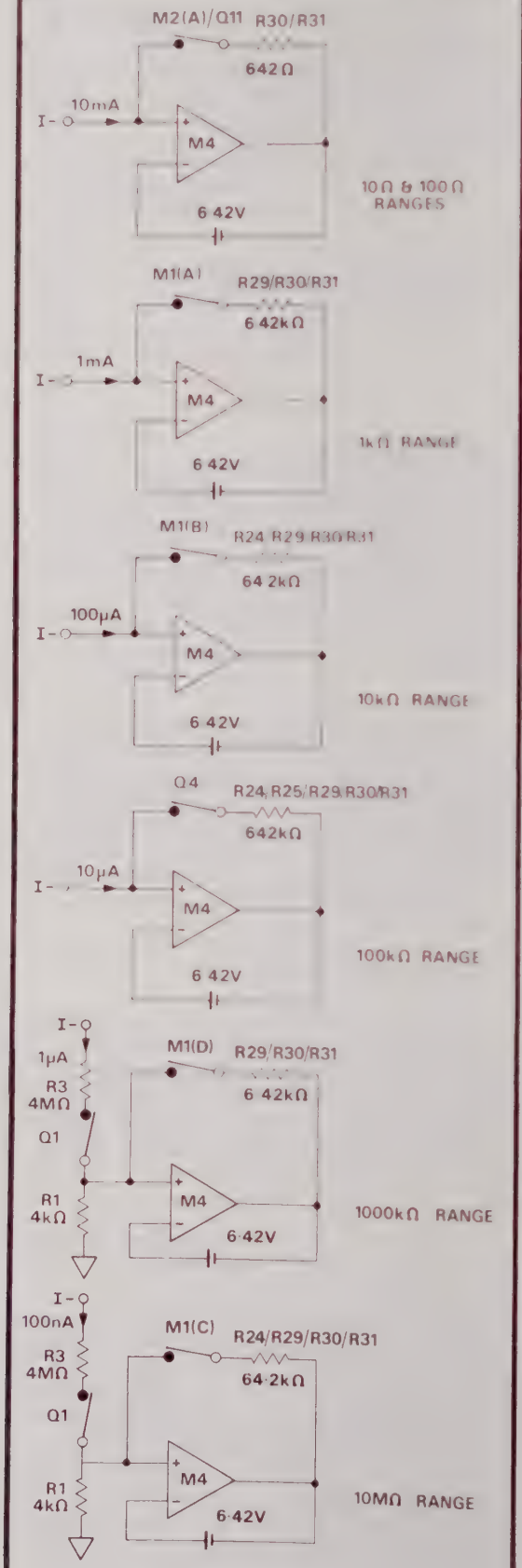


FIG. 3.30 SIMPLIFIED Ω CURRENT SWITCHING

The voltage developed across C8 causes M4 to sink current through resistor chain R24, R25, R29, R30, R31 until the voltage developed across the chain balances that across C8. Thus the current required for a particular range is selected by the value of the resistor chain switched by M1, M2 and Q4. Simplified diagram Fig. 3.30 shows the resistor chain and switching for each range. On the high resistance ranges leakage paths are provided by Q3, M2(B) and M2(C).

To produce good common mode rejection, M4 supplies are bootstrapped, the supply span being defined by a 12 volt zener, D17. The filtered bootstrap supplies (+ΩBS and −ΩBS) power the astable (M6) and bilateral switch.

The use of ohms guard permits in circuit measurement of resistors, provided shunt paths are greater than 250Ω and a suitable tapping point is available. Consider Fig. 3.31. Guard is reference 0, Lo is actively maintained within microvolts of reference 0 (as previously explained). Thus there is no voltage across Rz and consequently no current in Rz. Voltage follower Q10 M3 will simply pass more current into Ry from the I+ terminal until the selected current for the particular range flows through Rx.

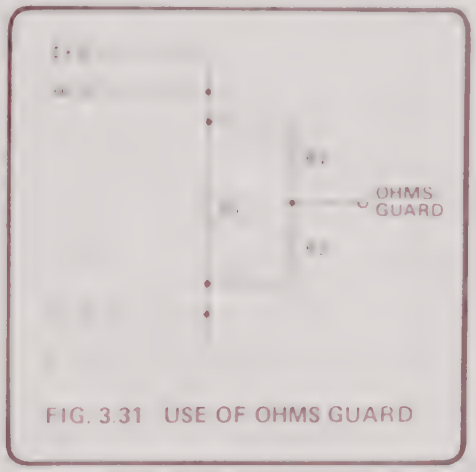


FIG. 3.31 USE OF OHMS GUARD

3.4.3 Test

During the self test routine (actuated from the front panel or remotely programmed), the Ohms Converter is checked for correct operation. The circuitry is placed into the 10kΩ range as described in Section 3.2.1.3. Filter is selected and F.E.T. Q5 'closed' from M9-1 causing R8 (9.76kΩ) to be placed between I+ and I−. Thus with I+ and Hi, I− and Lo connected (2-wire if front panel input selected), the DC Isolator (which is also in the TEST mode) measures the voltage developed across the resistor (approx 1 volt). The resulting voltage output from the DC Isolator is applied to the A-D converter, measured and compared to the stored value. If the measured signal is within ± 6% of the stored value, the test is complete.

3.5 CURRENT ASSEMBLY (Drawing No. 430304)

The Current assembly contains a set of selectable precision current shunts, the voltage developed across the shunt(s) being sampled by the DC or AC voltage measurement circuits.

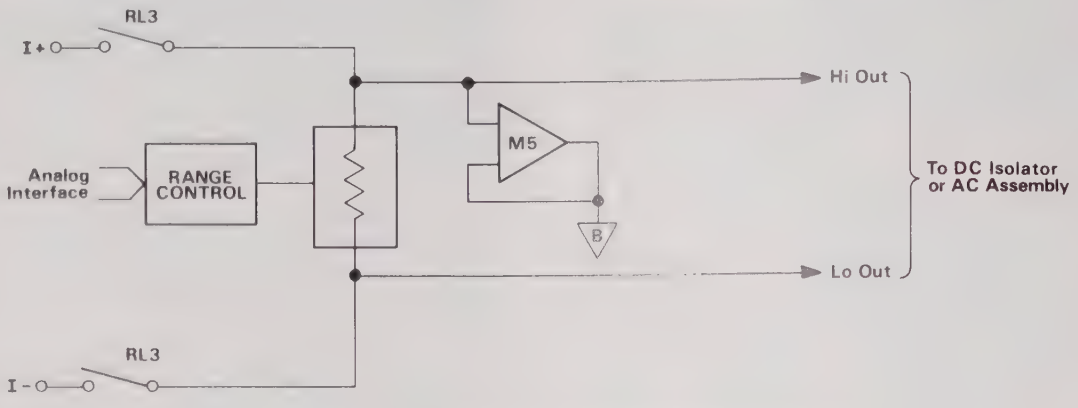


FIG. 3.32 SIMPLIFIED DIAGRAM OF CURRENT ASSEMBLY

3.5.1 Current Measurements

Precision current shunts of 0.1Ω, 1Ω, 9Ω, 90Ω and 900Ω connected in series provide an output of 100mV for a full range signal. To eliminate errors in measurement due to lead or contact resistance, all current shunts are 4-wire sensed i.e. a pair of current leads and a pair of voltage leads to the shunt(s) switched separately. The voltage developed across the shunt(s) is fed to the DC Isolator in DCI and the AC assembly in ACI or DCI + ACI. The latter, DC coupled mode, computing the RMS value of the DC and AC component of the input current. These circuits are placed in the '.1V range' amplifying the signal by 3.16. The output of buffer M5 is used to guard leakage paths on the current board.

Overload protection up to 2 amps is provided by diodes D13 – D16. An input greater than 2 amps causes the current fuse, located on the rear panel, to blow.

3.5.2 Test

During the self test routine, the Current assembly is checked for correct operation. The circuitry is placed into the .1mA DC current range as described in Section 3.2.1.3 with the DC Isolator in the 100mV range. Filter is selected and F.E.T. Q9 closed from M4 – 10 allowing current to flow through R18 to the 100μA range shunts, from the +15V supply. Thus a voltage of approximately 0.3 volts is developed across the shunts and fed to the DC Isolator. This voltage combined with the effect of the voltage injected due to the DC Isolator being in Test (Section 3.2.2.6) causes the output of the DC Isolator to be approximately 5.75 volts. After measurement by the A-D converter, the value is compared to the stored value. If the measured signal is within 6% of the stored value, the test is complete.

3.6 REAR INPUT/RATIO INPUT (Circuit Drawing No. [illegible]0307).

3.6.1 General

The Rear Input/Ratio Input assembly contains the switching circuitry to enable one of the three analog signal sources to be connected to the measurement circuits of the DVM. When Rear Input is selected either remotely or on the rear panel of the instrument and the RATIO key is depressed, the switching circuitry, under microprocessor control, selects the ratio (reference) input then the rear (signal) input, taking one valid reading at each stage.

3.6.2 Front Panel/Rear Panel Input

When Front Input is selected, either remotely or on the [illegible], this causes the base of Q1 to be connected to 0 volts, turning on the transistor. Thus relays RL1 and RL2 are energised, causing the front signal input terminals to be connected to the measurement circuits. Should Rear Input be selected, relays RL1 and RL2 are de-[illegible], connecting the rear input to the measurement [illegible].

3.6.3 Ratio

During the last part of the analog interface update sequence (see Fig. 3.6) M1-5 is taken high causing the flip-flop (M1) to be clocked high (0 volts) on pin 1. The signal is applied to Q2 energising the ratio mode input selector relays, RL3 and RL4. Thus the inputs to the 'Ratio Input' on the rear panel are connected to the measurement circuits. Once a valid reading has taken place, the 'Rear Input' lines are connected to the measurement circuits by leaving M1-5 low. This de-energises the relays as Q2 is turned off. Another reading is then taken and the ratio calculated.

3.6.4 Test

When TEST is selected, the ratio option is checked to see if it is fitted, by interrogating the AD4 line to see if it is held high.

3.7 ANALOG OUTPUT (Circuit Drawing No. 430308)

3.7.1 General

The Analog Output Board accepts the DC Isolator or AC Converter Output and converts it to a ±1 volt DC full range output. This signal can then be used, for example, to drive X-Y plotters or strip chart recorders.

3.7.2 Description

The 3.16V full range signal from the DC Isolator or AC Converter is buffered by unity gain amplifier M2. The output is potentially divided by R7 and R8 so that 1 volt full range is presented to M1, another unity gain amplifier. Potentiometer R5 is adjusted to remove any offset caused by M1 and M2. Positive temperature coefficient thermistors R3, R4 and diodes D1, D2, protect the Analog Output circuitry from accidental input applied to the Analog Output external connector.

3.8 DIGITAL ASSEMBLY (Circuit Drawing No. 430329)

The Digital assembly contains the circuitry providing the general management of the instrument and the digital section of the A-D converter. Fig. 3.33 outlines the main portions and signal highways of this board.

3.8.1 Processor and Memory (430329 sheet 1)

A 6800 microprocessor (MPU) together with 16k bytes of memory controls the communication between the front panel, digital interface, display drivers, Digital and analog assemblies. The memory can be split into five main areas

(1) Program Memory - needed to operate the whole instrument system

(2) Constant Data Memory - e.g. Self Test limits, Error read-out specifications and other fixed factors.

(3) Non-volatile Memory - used to store all the calibration errors used for each reading and determined during the 'Auto-cal' cycle.

(4) Operational Memory - used for scratch pad operations and storing.

(5) Volatile Display Memory - volatile data such as Max-Min stores, Limit stores and computation stores

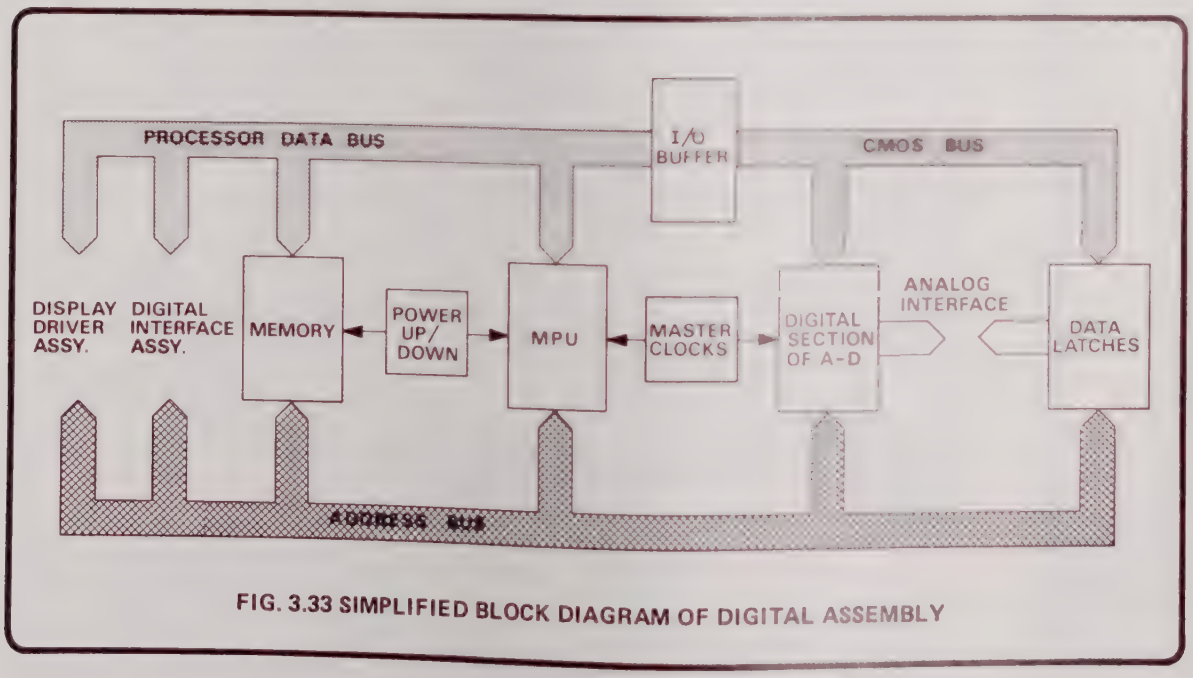


FIG. 3.33 SIMPLIFIED BLOCK DIAGRAM OF DIGITAL ASSEMBLY

3.8.1.1 Software Overview

The system uses the technique of a looping prioritised job scheduler (see Fig. 3.34). Each job driven from the scheduler is controlled by a flag in the system workspace which is set when the job is required to be run and cleared when completed. Priority of activation is ensured by making each job exit on completion, to the top of the schedule.

Program Modules: The program memory is split into a series of functional modules, each module corresponding fairly closely to a major functional area and hence to one of the jobs activated by the job scheduler, the larger ones being subdivided, see Drawing No. 890043.

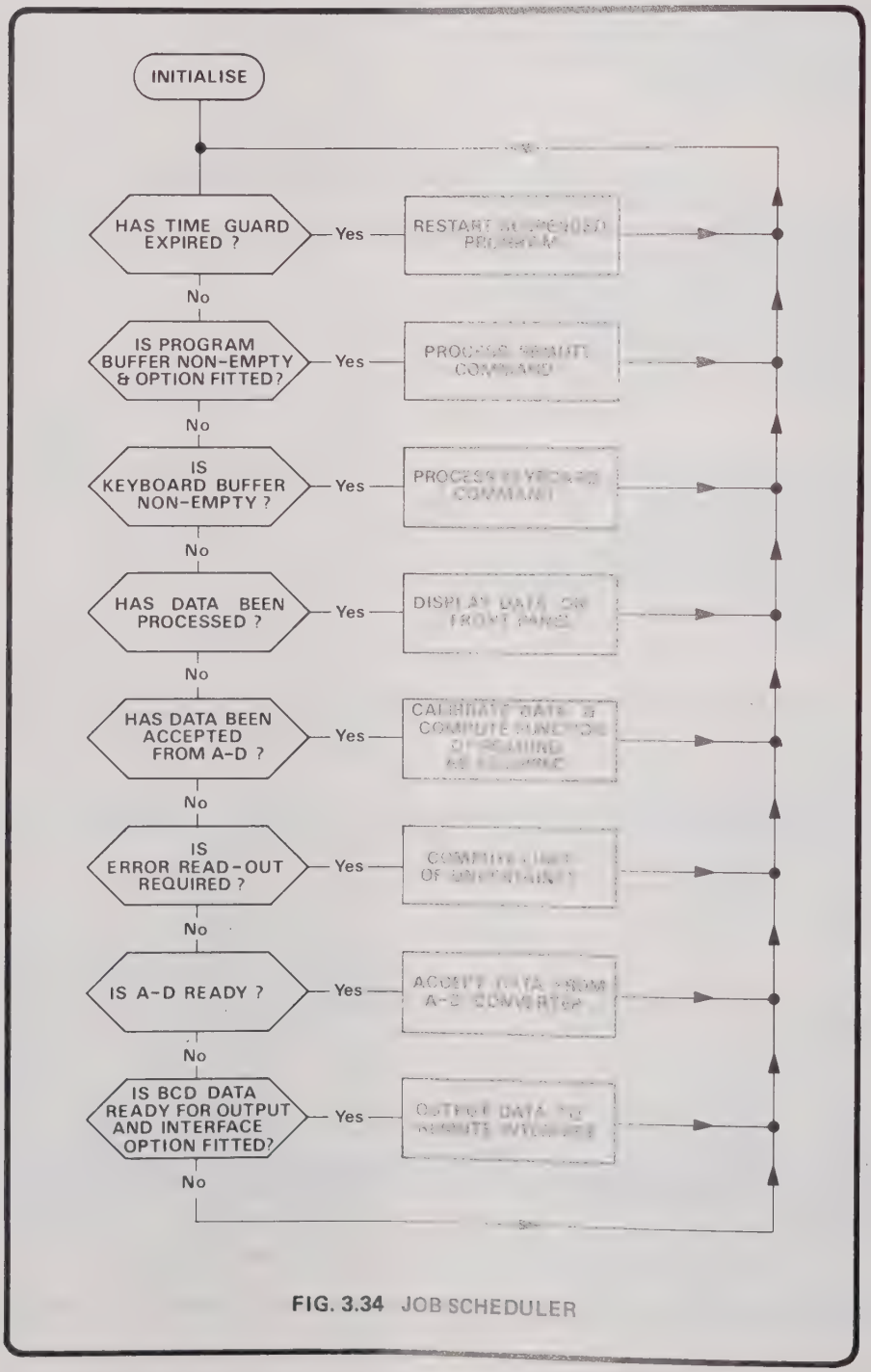
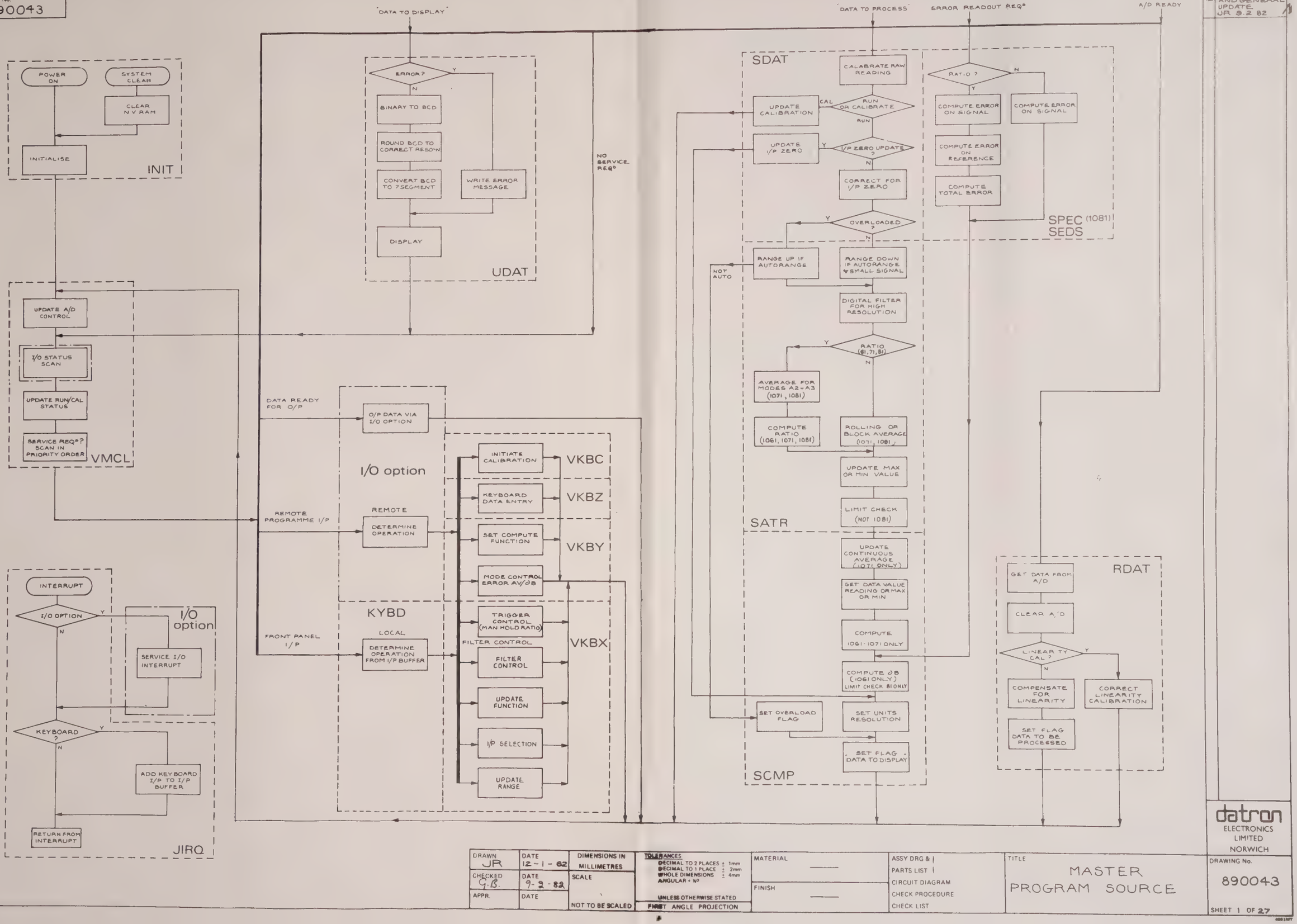


FIG. 3.34 JOB SCHEDULER

DRAWING No. 890043

ISS	CHANGES
12	FCO 1267 6½ DIGIT MOD AND GENERAL UPDATE. JR 9.2.82



DRAWN JR	DATE 12-1-82	DIMENSIONS IN MILLIMETRES	TOLERANCES: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; ANGULAR ± ½° — UNLESS OTHERWISE STATED — FIRST ANGLE PROJECTION
CHECKED G.B.	DATE 9-2-82	SCALE — NOT TO BE SCALED	MATERIAL —— FINISH ——
APPR.	DATE		ASSY DRG & | PARTS LIST | CIRCUIT DIAGRAM CHECK PROCEDURE CHECK LIST

TITLE: MASTER PROGRAM SOURCE

datron ELECTRONICS LIMITED NORWICH

DRAWING No. 890043

SHEET 1 OF 27

Data Control. Data handled by the system consists of a stream of measurement information on which a number of operations are carried out. A second stream, asynchronous with the first, consists of commands derived from the front panel or digital interface, controlling both the measurement circuits and computation programs. Operations on the measurement stream basically consist of acquiring the raw data from the A-D converter, calibrating this data and carrying out any other computations, and converting and formatting the data for output. Note that a job consuming data is given higher priority than the one producing data for it, allowing a producer to place data into an empty buffer. The consumer is activated by a flag, set by the producer to indicate data ready in the buffer.

Process Control. Control of the instrument by the processor, initiated from the front panel or digital interface, is arranged by using a 'pipeline control' of the major system state and a 'first in/first out' buffer between the interrupt level routine receiving the control command and the main program implementing it. The major system state consists of the range, function, resolution, filter, ratio, autorange, etc., flags and the computation mode (reading, A-B, ÷C, etc.). The pipeline comprises three levels. The top, level 1, reflects the state being programmed, the second, level 2, the state of the measurement circuits and the third, level 3, the measurement being processed. When a command is input, level 1 is updated (e.g. a new range is selected) and as soon as the measuring circuits are not converting an input signal, the state in level 1 is moved to level 2 causing the measurement circuits to update to the new state. When an A-D conversion is complete, data is read from the A-D and the state transferred from level 2 to 3, providing information for the processing routines. Additionally, at this time, the level 1 to level 2 transfer is repeated and the measurement circuits again updated to allow for commands received while the conversion is in progress.

A second control mechanism used is to input all the commands via a 'first in/first out' buffer between the interrupt level routine receiving the command and the main program implementing it. Thus the processor under remote control is able to 'simultaneously' set up the requirements for the next reading, convert the current reading and process the last one.

3.8.1.2 The Two-Phase Clock

The 6800 requires a non-overlapping positive two-phase (Ø1, Ø2) clock. This is derived from the line-locked master clock signal (1.6MHz for 50Hz line, 1.9MHz for 60Hz – see sheet 4). The first half of M57 divides the master clock by two to 800kHz, producing antiphase squarewaves at pins 14 and 15. If data is not being transferred via the CMOS data bus; M57 (CMOS I/O) is at logic 0, M57 11 is at logic 1, so M56-8 follows M57-15. The circuit utilizes the propagation delays inherent in M54 and M55 (approx. 10ns per gate), to ensure that the positive-going segments of Phase 1 and Phase 2 clock waveforms do not overlap (as illustrated in Fig. 3-35). Q6 and Q7 drive the clock output at voltage levels demanded by the processor (0V and +5V)

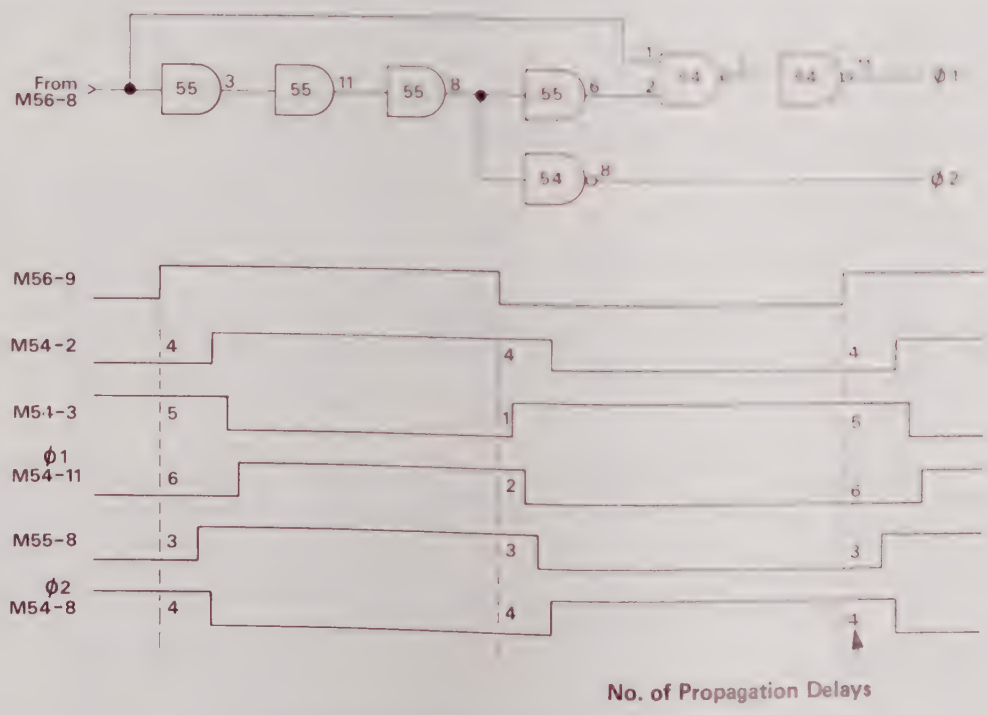


FIG. 3.35 TWO-PHASE CLOCK GENERATION

To account for slower data transfer in CMOS devices, the clock frequency is again divided by two to 400kHz when the CMOS data bus is active. The decoded address 'CMOS I/O' at M57-7 is set to logic 1 during these transfers, so a 400kHz square wave appears at M57-11. The combined effect of this and the 800kHz output from M57-15 is to 'stretch' the waveforms of the Phase 1 and 2 clock outputs (illustrated in Fig. 3.36). Thus Phase 2 remains at logic 1 for one cycle of the normal 800kHz operation, allowing more time for CMOS transfers.

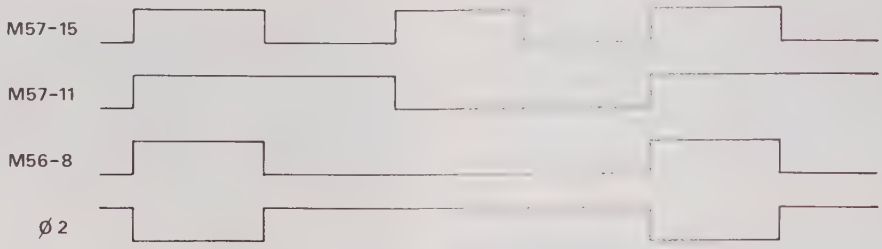


FIG. 3.36 TIMING DIAGRAM OF STRETCHED TWO PHASE CLOCK

3.8.1.3 RAM/ROM Circuit

The 6800 uses 3 Read-Only Memory chips (ROMs) which contain the program necessary to run the instrument. Each ROM is able to store up to 4096, 8-bit 'bytes' of program information; grouped in program modules. The MPU accesses a byte by placing its address on the 16-bit Address Bus and driving the Valid Memory Address (VMA) line true (logic-1). The information held in that particular location is then sent back to the MPU via the Processor Data Bus.

The chip-select inputs for the RAM and ROM are decoded from a selection of high-order address bits. This selection determines the positions of the RAM and ROM in the memory map. For example: M30 is fed from A15.A13.A12 so that it covers the memory locations from #F000 to #FFFF (Note that since A14 is not decoded M30 also appears at #B000 to #BFFF).

The processor employs 1024 bytes of 8-bit wide Random Access Memory (RAM) made up from two 1024 x 4-bit RAMs (M31/M36). M31 and M36 are employed as operating memory for scratch pad operations and storing volatile data (e.g. Max, Min). The principal location of the RAM is from #0000 to #00FF. Since A8 and A9 are not decoded there are images starting at #0100, #0200, #0300.

A further 256 bytes of 8-bit wide RAM are made up from two 256 x 4-bit RAMs (M19/M20). M19 and M20 are backed up by a battery to privide the non-volatile 'Calibration' and 'Zero' memory. Three address bits A12, A14 and $\overline{A15}$ are decoded by M33 (pin 8) to enable M19/M20, but M29 (pin 6) permits the memory contents to be changed only if CAL is selected, or if the ZERO section of the memory is addressed (A7 and A6 both at logic-1).

The read/write control line R/$\overline{W}$ from the 6800 is [illegible] Clock ÷ 2' signal to provide correct address decodes include gating with VMAØ2.

An instrument power up is detected by M60/M62 [illegible] initialization $\overline{RESET}$ signal to be fed to the [illegible] (See Fig. 3.38).

[illegible] power-up or power-down (+5V supply line [illegible] signal from the supply-level detectors prevents [illegible] M19 and M20 from being overwritten by holding the [illegible] chip select) lines low (<0.2 volts) via Q14 for a period [illegible] 75mS determined by R55/C32.

[illegible] Decode and Input/Output [illegible] M30329 sheet 2)

[illegible] is transferred to and from CMOS devices [illegible] Data Bus during periods when the signal [illegible] logic-1 (M33-6). CMOS I/O is addressed [illegible] A14.A11 is true. This occurs when memory [illegible] at #4100 (and its images) are selected. [illegible] data between the Processor Data Bus and [illegible] Bus takes place at 400kHz, the Read/ [illegible] ting the direction of the information [illegible] state buffers M4, M5 and M6.

[illegible] to address the various CMOS input/output [illegible] address lines must be further decoded. M32 [illegible] oder, providing 5 addressable drives; M16 [illegible] 1-of-4 decoder addressing the front panel circuitry [illegible] the digital elements of the A-D converter. A summary [illegible] ed CMOS address signals is given in Fig. 3.39.

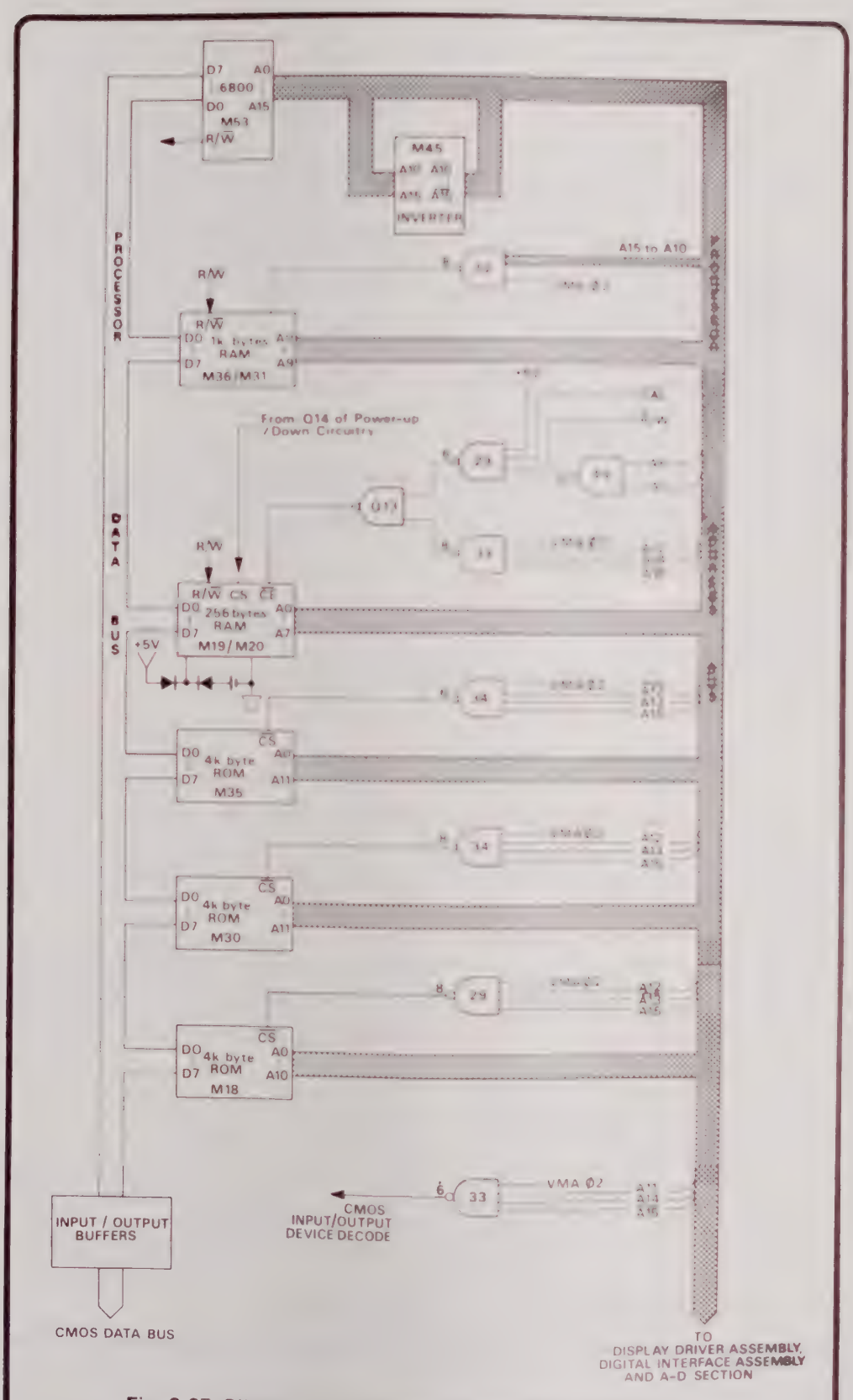


Fig. 3.37 SIMPLIFIED DIAGRAM OF MEMORY CIRCUITS

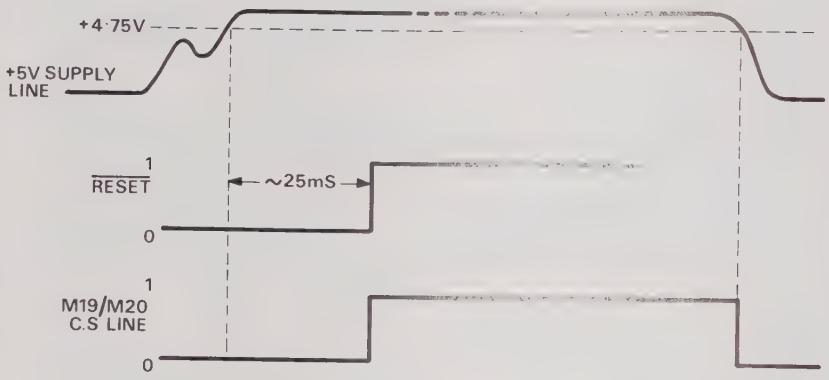


FIG. 3.38 START UP AND NON-VOLATILE RAM PROTECTION

A6	A5	A4	A2	A1	A0	SIGNAL	M32/M33 Pin No.	Comments
0	0	0	1	X	X	$\overline{\text{XKYBRD}}$	M[illegible]	[illegible]d read/write
0	0	1	X	X	X		(M32-4)	[illegible]s a MPU 'power up' sequence
1	0	0	X	X	X		(M32-11)	Triggers processor time guard (M43)
0	1	0	1	X	X	$\overline{\text{XADDT}}$	M32-1	[illegible]ain counter output enable
0	1	1	X	X	X		(M32-13)	Analog interface address latch input
0	0	0	X	0	0	$\overline{\text{XKDSP0}}$	M16-[illegible]	
0	0	0	X	0	1	$\overline{\text{XKDSP1}}$	M16-[illegible]	Addresses keyboard
0	0	0	X	1	0	$\overline{\text{XKDSP2}}$	M16-5	[illegible]e.d. latches
0	0	0	X	1	1	$\overline{\text{XKDSP3}}$	M16-4	
0	1	0	X	0	0	$\overline{\text{XADSTA}}$	M16-[illegible]	[illegible]ing interrupt status output enable
0	1	0	X	0	1		M16-[illegible]	[illegible] INTERVAL switch output enable
0	1	0	X	1	0	$\overline{\text{XADCTL}}$	M16-[illegible]	[illegible]D control latches, input enable
0	1	0	X	1	1	$\overline{\text{XADDLY}}$	M16-[illegible]	[illegible]D delay counter input enable

FIG. 3.39 CMOS ADDRESS DECODING

3.8.3 Analog to Digital Conversion (Digital Section)

3.8.3.1 General Principle

Block diagram Fig. 3.40 outlines the essentials of the digital section and should be used with flowchart Fig. 3.41 in order to follow the operation of this section.

The function of this section of the circuitry is to generate the sequence that when transferred to the analog section, controls the sequence from RESET through the integration cycle and back to RESET. The circuitry controls the length of SIG and BIAS and counts during REF 1 and REF 2, the accumulated count being proportional to the length of the reference periods, which in turn is proportional to the measured input signal. At the end of each reading cycle the count is read by the MPU, processed and displayed.

The sequence is controlled by stepping M47 through Q0 to Q7. Each 'Q' output from M47 goes to logic-1 to activate its stage of the sequence; completion of one stage generates the 'Enable' for the next, via M46 switches, as a logic-0 state at M47-13. Timing is synchronized by Master-Clock/2 positive-going edges at M47-14, when M47 is enabled.

3.8.3.2 Preset Procedure

As part of the initialisation routine (at switch on), M47 (used as the sequence controller), is reset from M37-11, causing M47-2 to be logic '1'. Thus the control lines A, $\overline{B}$ and $\overline{C}$ put the analog section of the A-D into $\overline{\text{RESET}}$ (See Fig. 3.42). The Address Bus decoded signal $\overline{\text{XADDLY}}$ is taken low, enabling the presetting of the delay counters M13 and M14 from the CMOS Data Bus, the amount of delay being determined by the selected range, function and filter state, see Fig. 3.43. The A-D control latches, M11 and M12 are then enabled by $\overline{\text{XADCTL}}$ to (i) reset the command latch M1 (from M11-4), (ii) set the resolution of the main counter (M11-5 and 6), (iii) select trigger gate (M12-3, 4 or 5) and (iv) reset the data ready latch (M12-6).

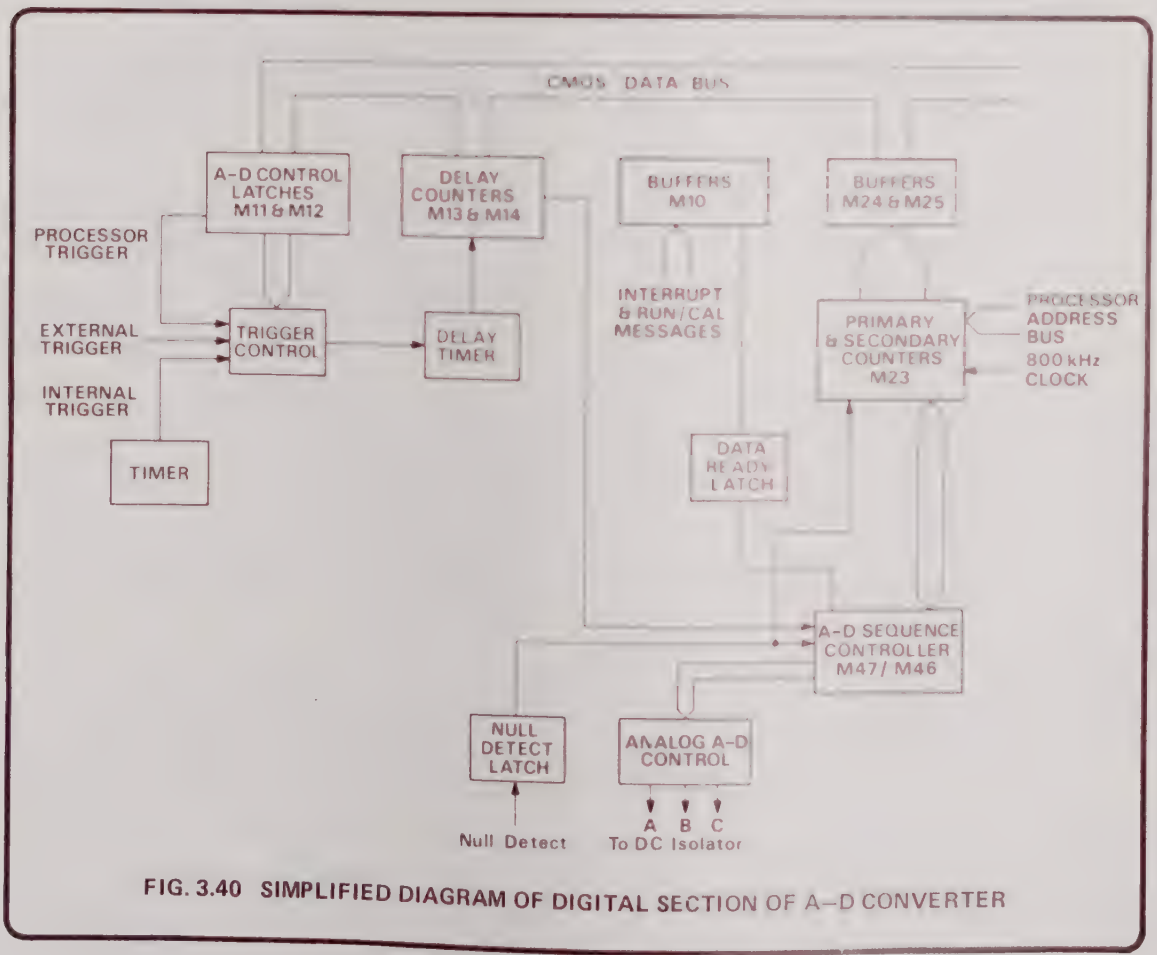


FIG. 3.40 SIMPLIFIED DIAGRAM OF DIGITAL SECTION OF A–D CONVERTER

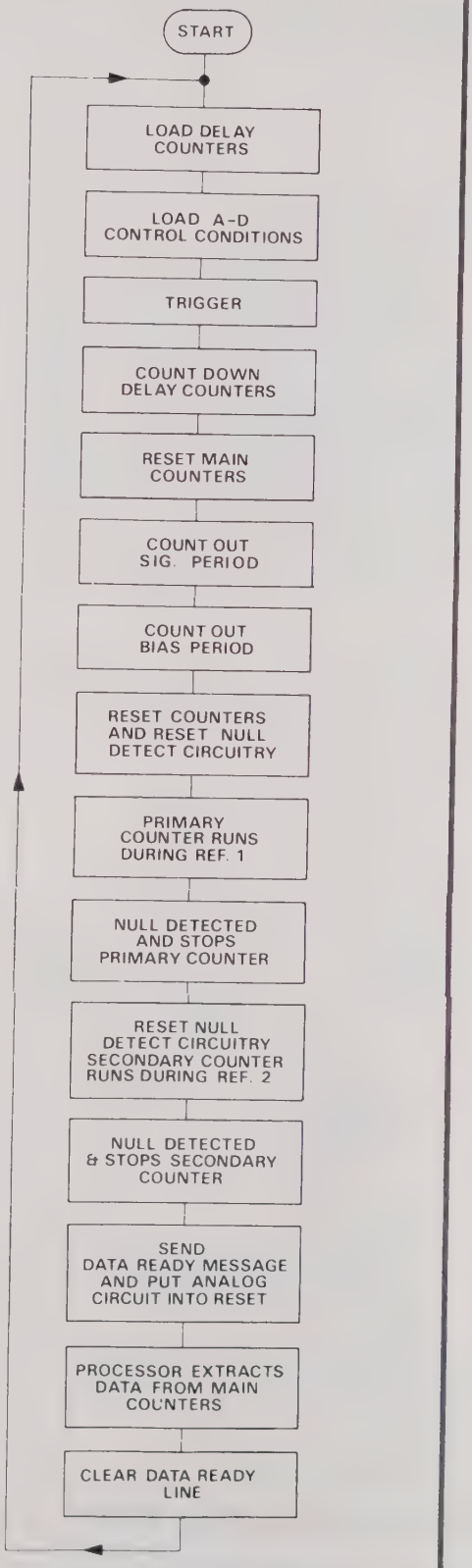

START
LOAD DELAY COUNTERS
LOAD A-D CONTROL CONDITIONS
TRIGGER
COUNT DOWN DELAY COUNTERS
RESET MAIN COUNTERS
COUNT OUT SIG. PERIOD
COUNT OUT BIAS PERIOD
RESET COUNTERS AND RESET NULL DETECT CIRCUITRY
PRIMARY COUNTER RUNS DURING REF. 1
NULL DETECTED AND STOPS PRIMARY COUNTER
RESET NULL DETECT CIRCUITRY SECONDARY COUNTER RUNS DURING REF. 2
NULL DETECTED & STOPS SECONDARY COUNTER
SEND DATA READY MESSAGE AND PUT ANALOG CIRCUIT INTO RESET
PROCESSOR EXTRACTS DATA FROM MAIN COUNTERS
CLEAR DATA READY LINE

FIG. 3.41 FLOWCHART OF A–D DIGITAL SECTION

SIGNAL	$\overline{A}$	$\overline{B}$	C
RESET	1	1	0
SYNC	1	1	0
BIAS	1	1	1
[illegible]	0	1	1
[illegible]	0	1	1
[illegible]	1	0	1
[illegible]	0	0	1
[illegible]	1	1	1

FIG. 3.42 A-D ANALOG SEQUENCE CONTROL SIGNALS

[illegible]		M13/M14 COUNT	
FUNCTION	RANGE	$\overline{\text{FILTER}}$	FILTER
DCV	All Ranges	2	101
Ohms / ACV / DCV	All Ranges	61	251
Option 30 ACV / DCV / ACI / DCI	All Ranges	46	151
DCI	100μA-1mA	2	101
	10mA	3	
	100mA	5	
	1A	6	
KOhms	10Ω-100kΩ	2	101
	1MΩ	4	121
	10MΩ	31	251

FIG. 3.43 COMMAND DELAYS

3.8.3.3 A-D Measurement Sequence

Trigger. The trigger, required to initiate the measurement sequence, is generated from one of three possible sources:

1. Internally generated 3/second trigger, from timer M61-7.

2. Externally generated trigger, from EXT TRIG on rear panel via M24-13.

3. A MPU derived trigger from M11-3 generated when auto-ranging, pressing MANUAL when HOLD selected, during calibration, an INPUT ZERO sequence or via the digital interface.

The trigger source is selected by the latched data on M12, enabling one of the three gates of M2.

Delay. The trigger pulse clocks the 'command latch' M1-11 causing the timer, M15, to output clock pulses (200Hz) to the delay counters (M13 and M14) after a delay of approx. 1.5mS set by C5, R8, R9, R11. The delay counters proceed to count down to zero, at which time the delay latch (M26) is clocked. Thus M26-14 becomes a logic '0', enabling the sequencer M47 (an octal counter) to proceed on to the next step via M46-2.

SYNC. The SYNC phase from the sequencer resets the counters of M23 and places the analog section of the A-D into SIG. The pulse is fed back to M47 via M46-3 to step on the sequencer.

SIG. During the time that the SIG line is at logic-1 (M47-3), the primary counter in M23 is enabled and counts out the signal period (20ms in normal mode, or 2.5ms superfast). When the counter times out, M23-23 goes to logic-0, enabling M47-13 via M23-14. The next Master-Clock/2 at M47-14 steps the sequence on to BIAS (M47-7 to logic-1, M47-3 reverts to logic-0).

BIAS. The BIAS signal (M47-7) is transferred to the analog section of the A-D by changing the state of the $\overline{A}$ line (M38-9 to a logic '0'). BIAS also enables the secondary counter of M23 to count out the BIAS period (20μS). The signal indicating the end of this period is passed via M46-9 causing the sequencer to carry on to the next step. The BIAS signal also resets the 'delay latch' (M26) ready for the next measurement cycle, and the 'null detector' latch (M22A).

WAIT. The WAIT pulse resets the counter of M23 via M39-10, keeps the $\overline{A}$ line to the analog section low, clocks the polarity null detect latch M22(B) causing a logic '1' on pin 1 if the signal applied to the analog section of the A-D converter was positive (logic '0' if negative) and is fed back to enable the sequencer via M46-3.

REF 1. The high to low edge of WAIT causes the $\overline{A}$ to change state and going into REF 1 makes $\overline{B}$ a logic '0'. The analog side is then in the condition to start 'ramping down'. While REF 1 is high the primary counter of M23 is enabled (pin 3) and counts the period of REF 1.

REF 1 is ended when a null detector pulse is detected and latched on to M22. This causes the sequencer to step on once more from M46-3, the low to high edge from pin 4 disabling the primary counter.

REF 2. The REF 2 signal changes the state of the $\overline{A}$ line (causing the analog section to ramp down at a slower rate), resets the 'null detect' latch and enables the secondary counter of M23 (Pin 13) to count the period of REF 2. If the secondary counter overflows, the primary counter is incremented from M23-16.

As in REF 1, a null detector pulse causes the counting period to end (M22-12) and increments the sequencer via M46-3 causing the $\overline{A}$ and $\overline{B}$ lines to change state.

END. The low to high edge from M47-10 is fed back to M47, via M48-6 giving a master reset. Thus the sequencer is placed into RESET.

RESET. The sequence pulse from M47-2 clocks the 'data ready' latch M1-3 placing a signal on to the CMOS Data Bus via tri-state buffer M10 indicating to the MPU that a reading is ready to be taken from the main counter M23. Data is extracted from the counters in three bytes (controlled by the A1 and A0 lines of the processor address bus) with the counter output buffers, M24 and M25 being enabled by $\overline{XADDT}$, a decoded processor address.

The RESET signal is also passed to the analog section of the A-D by changing the state of the C line.

Once the data has been extracted from the main counter the set up procedure is then repeated to await a further trigger.

3.8.3.4 Master Clock and Line Locking (430329 sheet 4)

To give improved rejection of line frequency related noise, the 1061 is linelocked. The line frequency is sampled and compared to the internal master clock. Synchronisation is achieved by adjusting the master clock frequency.

A sinusoidal line frequency signal from the 5V mains tap is converted to a square-wave (M25-13) and ÷2(M26-1) before being fed to the comparator section of the ULA M23 (sheet 3). The MASTER CLOCK ÷2 signal is fed to ripple counter M27 which outputs a signal of twice the estimated line frequency, for line related periods, controlled by the ULA (M23-18). This signal is fed to M23-19 (via inverter M39) and after a further ÷2, is compared with the actual line frequency (see Fig 3.44).

The ULA determines whether the master clock is running too slow or too fast, producing a signal on pin 20 whose pulse-width is proportional to the difference. The output of pin 21 is a 25Hz square-wave which is fed to the up/down input of counters M41/50. Thus depending on the position and down period of the pulse, the count held is increased or decreased.

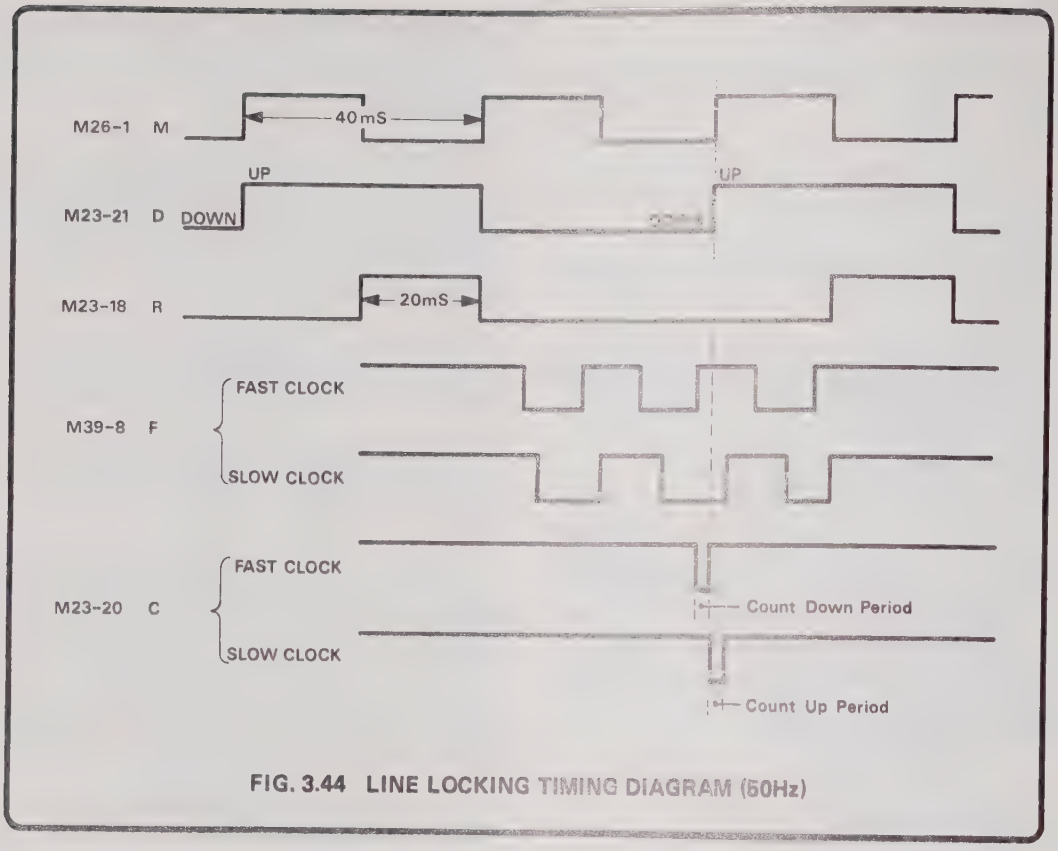


FIG. 3.44 LINE LOCKING TIMING DIAGRAM (50Hz)

Latches M42/51 are updated with this new count during the A-D RESET period and applies the count to resistor network AN4 which forms a D-A converter. Changing the voltage applied to varicap D9 alters its capacitance, thus adjusting the LC of the Colpitts oscillator. Therefore the frequency of the Master Clock is increased or decreased to be an exact multiple of the mains frequency.

3.9 FRONT PCB ASSEMBLY (Circuit Drawing No. 430294)

The Front pcb assembly accepts the measurement signals, digitally displays the value, provides manual control of the measurement circuits and data conditioning, and gives a visual status indication of the selectable instrument states.

3.9.1 Analog Input Signals (430294 sheet 2)

The Front pcb connects the terminals to the 2/4-wire Ohms and Local-Remote Guard switches. Thus in '2-wire': Hi is connected to I+, and Lo to I−, through thermistors R1 and R2. In 'Local': Guard is not directly linked to the front panel Lo terminal, as this becomes active in 4-wire Ohms. Instead, 'Local' links Guard to Ohms Guard, which is permanently connected to DC Isolator Lo.

Signals applied to the six front panel terminals are routed through to the Rear pcb (to the Rear Input/Ratio pcb or Rear Input pcb if Option 40 or 41 is fitted) via the Signal Cable assembly. Each of the terminal leads passes through its own HF choke, all six inductors being wound in the same direction on the same core. This 'Common Mode' choke presents high impedance to transient common mode currents, but low impedance to normal mode differential input currents.

Two screened cables are used to transfer the signals to the rear: I+ and Hi are carried in one; I−, Lo and Ohms Guard in the other. Guard is carried via the cable screens, thus guarding the signals during transfer.

3.9.2 Display Signals (430294 sheet 1)

The front panel assembly routes the display signals from the Display Driver board to the gas discharge display.

3.9.3 Keyboard Data Encode (430294 sheet 1)

Selection of a front panel keyswitch causes one of the two 16-key encoders (M7 or M10) to send a data available message to M2 (a data latch) and to remember which key was pressed. The output of M2, (pin 1 or 13) signals the interrupt circuitry of the Digital Board (IRQK1 or IRQK2).

When the microprocessor accepts the interrupt and has located the source, the XKY BRD line to pin 13 of M7 and M10 is taken low, enabling the data outputs of the encoders to be placed on to the CMOS data bus (See Fig. 3.45 for the key select coding). This signal also resets M2 ready for the next key selection.

KEY	M7			
	14	15	16	17
	CD7	CD6	CD5	CD4
100	0	0	0	0
10	0	0	0	1
1000	0	0	1	0
10MΩ	0	0	1	1
1	0	1	0	0
.1	0	1	0	1
10Ω	0	1	1	0
AUTO	0	1	1	1
DC	1	0	0	0
kΩ	1	0	0	1
KEYBOARD	1	1	0	1
I	1	1	1	0
INPUT ZERO	1	1	1	1

KEY	M10			
	14	15	16	17
	CD3	CD2	CD1	CD0
HOLD	0	0	0	0
RATIO	0	0	0	1
TEST	0	0	1	0
SPEC	0	0	1	1
(A–B)	0	1	0	0
dB	0	1	0	1
÷C	0	1	1	0
MAX	0	1	1	1
MIN	1	0	0	0
RESET	1	0	0	1
MAN	1	0	1	0
INPUT FILTER	1	1	0	1
AC	1	1	1	1

FIG. 3.45 CMOS DATA BUS : KEY SELECT CODING

3.9.4 Keyboard L.E.D. Data Decode (430294 sheet 1)

The XKY BRD signal is inverted by Q1, R7, C1, R6 partially enabling the L.E.D. data latches M4, M5, M6, M8, M9, M11 and M12 while information is not being extracted from the keyboard encoders. The data latches are divided into four sets, M6 and M4, M8 and M5, M12 and M11, M9 being fully enabled from the XKD SP0 – XKD SP3 lines respectively.

On initialisation or after a change of the instrument's selectable states, the L.E.D. data latches are updated by placing data on the CMOS Data Bus (See Fig. 3.46), firstly to M8 and M5 (enabled from XKD SP1) and 'clocking' from the CMOS CLK line (J2-6), secondly to M12 and M11, then M9 and finally M6 and M4.

The output of the L.E.D. latches provide the signals to the bases of the L.E.D. drive transistors, switching them on or off as required.

CMOS DATA LINE	M12/M11	M8/M5	M6/M4	M9
CD0	÷C	DC	AUTO	
CD1	dB	kΩ	10Ω	
CD2	SPEC	INPUT ZERO	1	
CD3	TEST	INPUT FILTER	.1	
CD4	A–B	KEYBOARD	10	
CD5	MIN		100	MAN
CD6	MAX	I	1000	RATIO
CD7	RESET	AC	10MΩ	HOLD

FIG. 3.46 CMOS DATA BUS : LED-SELECT CODING

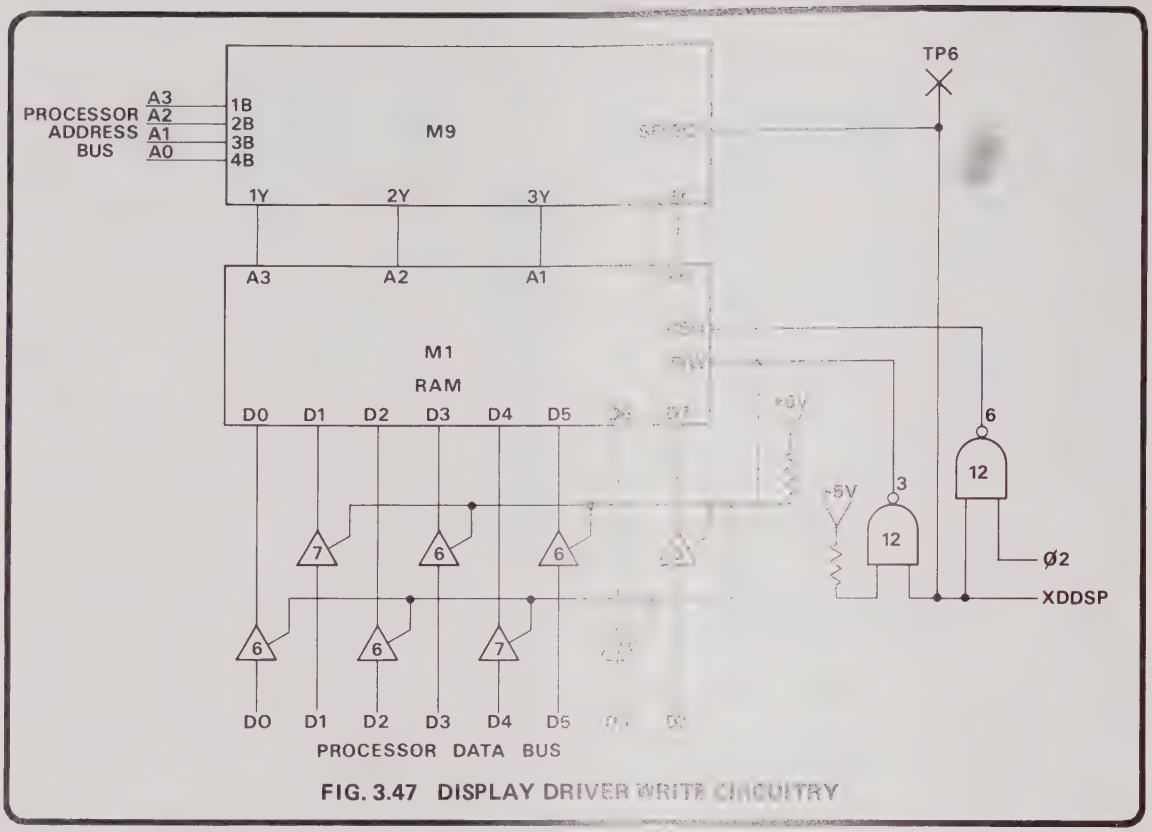


FIG. 3.47 DISPLAY DRIVER WRITE CIRCUITRY

COUNTER (M8)				RAM (M1)				Display block energised or operation implemented from M11
Q_3	Q_2	Q_1	Q_0	A_3	A_2	A_1	A_0	
0	0	0	0	0	0	0	0	1
0	0	0	1	0	0	0	1	3
0	0	1	0	0	1	0	0	5
0	0	1	1	0	1	0	1	7
0	1	0	0	1	0	0	0	9
0	1	0	1	1	0	0	1	11
0	1	1	0	1	1	0	0	
0	1	1	1	1	1	0	1	
1	0	0	0	0	0	1	0	2
1	0	0	1	0	0	1	1	4
1	0	1	0	0	1	1	0	6
1	0	1	1	0	1	1	1	8
1	1	0	0	1	0	0	1	10
1	1	0	1	1	0	1	1	Reset Counter

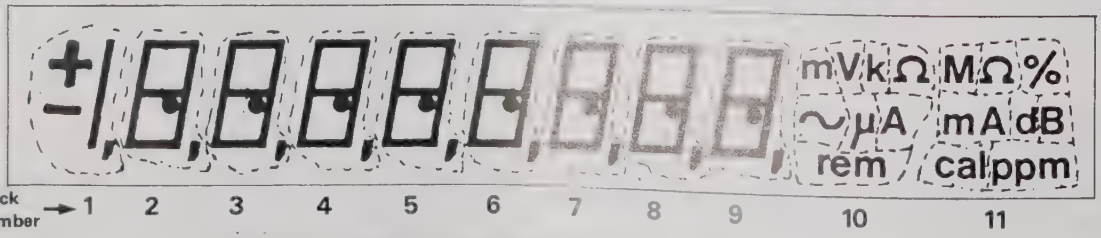


FIG. 3.48 DISPLAY DRIVER READ MODE ADDRESS STATES

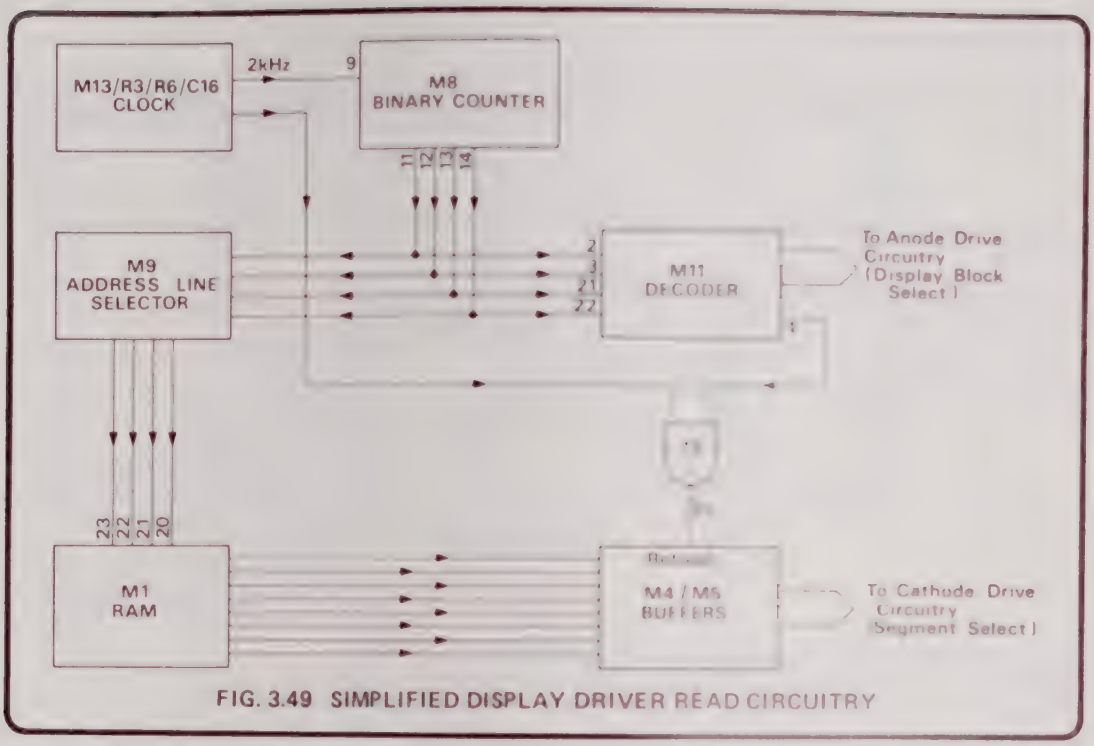


FIG. 3.49 SIMPLIFIED DISPLAY DRIVER READ CIRCUITRY

3.10 DISPLAY DRIVER ASSEMBLY (Circuit Drawing No. 430330).

Basically, the Display Driver assembly receives the display information from the microprocessor (running at 800kHz) and stores it in a Random Access Memory (RAM) digit by digit. This data is then read out at a slower frequency (2kHz), level shifted and output to the gas discharge display.

NOTE: In the following description, each bar, decimal point or legend is referred to as a display segment and each set of segments i.e. ±1, ⊟ or a legend block, is referred to as a display block.

3.10.1 Write Mode

On completion of a reading or when certain modes are selected, (e.g. ERROR, keyboard entry), the processor indicates to the Display Driver Board that data is ready to be transferred by the signal XDDSP (TP6). This causes the RAM (M1) to be placed into its write mode and the quadruple 2-line to 1-line data selector, M9, to select the 'B' inputs which are connected to the processor address bus.

The signal XDDSP also causes the tri-state buffers M6 and M7 to become enabled, causing the data input lines of the RAM to be connected to the processor data bus. Thus under MPU control, the display data (±1, ⊟ 's, decimal points and legends) is written into the RAM.

Once this transfer of data is complete the RAM becomes deselected, the buffers return to their third state inhibiting the data bus to the RAM and connects the 'A' inputs of M9 to the address lines of the RAM.

3.10.2 Read Mode

Discharge between adjacent display blocks is prevented by time multiplexing and sending information to alternate blocks. A particular display block is selected by driving its anode, and a particular segment by driving the segment cathode.

The free running clock M13, R3, R5, C16, produces a 2kHz signal (M13-9) to drive a 4-bit binary counter, M8, which provides the control of the address lines in the read mode (See Fig. 3.49). The display block selection is achieved by decoding these 4 lines into 16 bits using M11. The output lines of M11 are connected to the bases of transistors Q1-Q3, Q13-Q20 which act as anode switches. Note that when the address lines are in the state 0000 the output of M11 (pin 11) selects the anode to block 1, 0001 selects the anode to block 3 (M11-9), 0010 ... block 5, etc., thus the display blocks are selected alternately.

To select the appropriate segment data from the RAM to match the display block selection the address lines are given a left hand bit rotation, i.e. if the output of M8 is labelled DCBA, (2^3, 2^2, 2^1, 2^0), the address input of M1 would be CBAD. (Fig. 3.48 gives the state of the address lines for each display block). The particular display block segment data is recalled by the RAM, buffered by M4 and M5, level shifted –180 volts by R8-R15, C4-C11 causing Q5-Q12 to drive the cathodes, D1-D10 acting as restoration diodes. Between the transfer of each set of segment data, M13-13 is taken high, causing the outputs of M4 and M5 to be a logic '0'. This produces a refresh period for capacitors C4-C11 to discharge from the –180V supply through the restoration diodes.

3.11 IEEE 488 STANDARD DIGITAL INTERFACE (Circuit Diagram No. 430627)

The IEEE Digital Interface assembly contains the extra memory circuitry required for the execution and decoding of interface functions, and for data input and output transfers. Simplified diagram Fig. 3.50 shows its essential features.

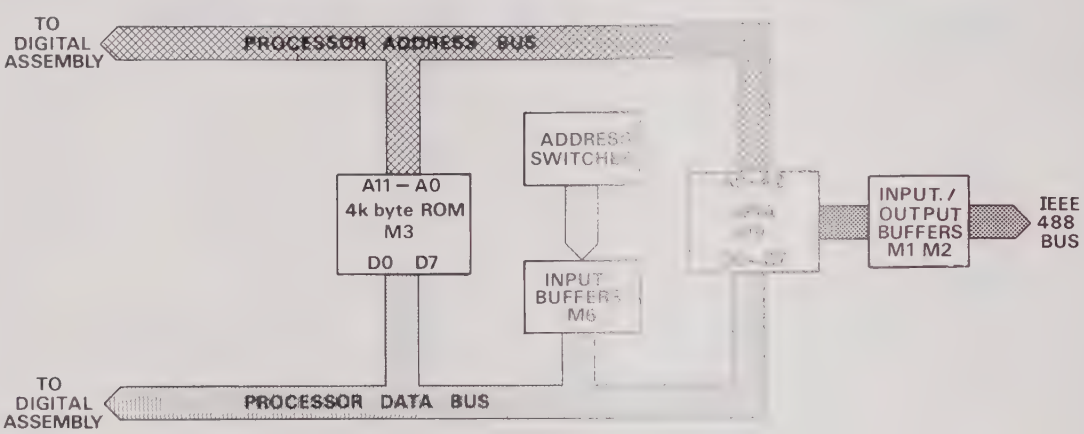


Fig. 3.50 SIMPLIFIED DIAGRAM OF IEEE ASSEMBLY

3.11.1 ROM Circuit

The IEEE Digital Interface assembly acts as an extension to the Digital assembly with connections to both the Processor Address and Data Buses. The board houses 4k bytes of program memory (M3) containing the sub-routines to control the instrument from the IEEE 488 Bus. The ROM receives the address information, with chip selection being made by decoding address lines A3-A11 with XIOBD and master clock Ø2.

3.11.2 Interface Circuit

The General Purpose Interface Adaptor (GPIA). M9, provides the interface between the IEEE 488 Standard Instrument Bus and the 68000 microprocessor. The MPU can receive, process and send messages to the interface through the GPIA.

The GPIA is able to automatically handle the following interface protocal[1] :-

Single address capability
Source and acceptor handshake
Talker and Listener states
Service Request
Device Clear

With MPU it is also capable of:-
Programmable Interrupts
Storing the instrument's address
Control of the interface input/output buffers.

The GPIA is selected by decoding address lines A3- with XIOBD. Address lines A0-A2 with the state of R/W line select one of the 8 read only or 7 write- in the GPIA, enabling the MPU to send or receive data over the interface.

The two signals T/$\overline{R}$1 and T/$\overline{R}$2 are used to control receivers (formed from M1, 2) which drive the interface bus.

[1] For further information refer to 'Getting aboard the 488 Bus' published by Motorola.

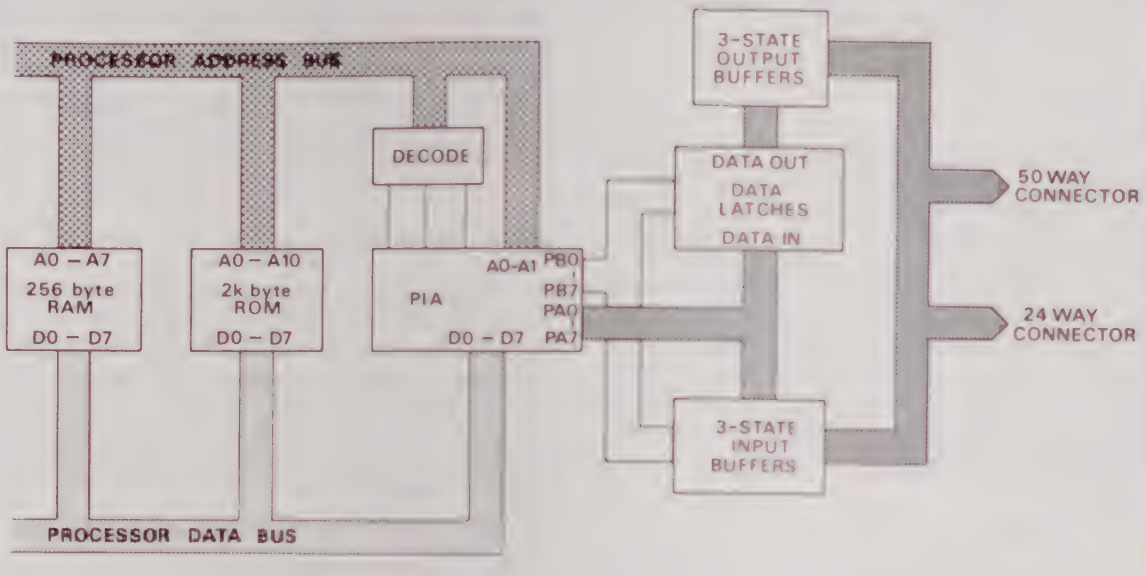


FIG. 3.51 SIMPLIFIED DIAGRAM OF BCD INTERFACE ASSEMBLY

3.12 BCD DIGITAL INTERFACE (Circuit Drawing No 430332)

The BCD Digital Interface assembly contains the extra memory and circuitry required for the execution and decoding of interface functions and to perform data output transfers. Simplified diagram, Fig 3.51 shows the essential features of this board.

3.12.1 RAM/ROM Circuit

The BCD Digital Interface assembly acts as an extension to the Digital assembly with connections to both the Processor Address and Data Buses. The board contains 2k bytes of program memory (M11) containing the sub-routines to control the instrument from the BCD Interface. Extra 'operating (scratch pad) memory' is provided by two 256x4 bit RAMS (M22, M23). Both the ROM and RAM's receive the address information, with chip selection being made by decoding address lines A8-A11 with XIOBD, R/$\overline{W}$ and Ø2.

3.12.2 Interface Circuit

The Peripheral Interface Adapter (PIA), M1, provides the means of interfacing the BCD input/output to the 6800 microprocessor. The PIA is selected by decoding address lines A9-A11 with XIOBD. Address lines A0 and A1 together with MPU data messages configure the six internal registers controlling data flow and external control signals

Peripheral Data lines PB0-PB7 (M1 pins 10-17) are used as enable lines allowing data to be placed on the BCD Data Bus from the Remote Programming Input and control lines of the BCD Output (2 bytes) and allowing data to be placed into output latches M7-M10, M12-M16 (5 bytes).

The length of PRINT COMMAND is controlled by the timer M2, R1, C3.

3.13 REAR (POWER SUPPLY) PCB ASSEMBLY (Circuit Drawing No. 430295)

3.13.1 General

The line transformer and power supply components are situated at the rear right hand side of the instrument, when viewed from the front. Transformers T1 and T2 are of toroidal construction mounted one on top of the other and bolted to the rear panel. T1 has a split primary comprising two 115V windings, intended for either series or parallel connection depending on the line voltage. An earth screen is interposed between primary and secondary windings to minimise electrostatic coupling, and is grounded to line earth. The second transformer T2 is driven from T1. It also possesses an electrostatic screen, this time being connected to Guard.

3.13.2 180V Supply

The 180V supply is required for the gas discharge display. Bridge rectifier W1 and C6 convert the 200V AC from the secondary of T1, to DC. R6, D3, R4 and Q2 act as a constant current source being regulated by D4, R5 and Q1. The +5V line (TP2) is connected to the digital +5V line (TP3) on the Display Driver assembly.

3.13.3 5V Supply

All the logic circuitry to the right of the central printed circuit board is powered from the supply gener-

ated from [illegible] volt 750mA secondary windings on transformer T1. The centre tap (digital common) is linked directly to line ground via LK6. The output of rectifying diodes [illegible] is smoothed by C7 and C8 before being fed to regulator M1. This regulator is capable of 1 amp output and has foldback current limiting and thermal shut-down, [illegible] short circuit protection.

3.13.4 [illegible] Supply

[illegible] the third secondary winding of transformer T1 is input to the primary of T2. The two [illegible] connected in series, with the centre tap [illegible] common. The output of bridge rectifier [illegible] voltage regulators M2 and M3 (wired as [illegible] positive and negative 15 volt supplies [illegible] circuitry. These regulators also include [illegible] limiting and thermal shut-down, to [illegible] protection.

3.14 [illegible] TEST SEQUENCE

[illegible] the TEST key places the instrument [illegible] checking the display and basic measur- [illegible] wchart for the routine is given in Fig. [illegible] circuitry conditions for each test are [illegible] bsection of the circuit description for [illegible] and the range 'F.E.T.' patterns in

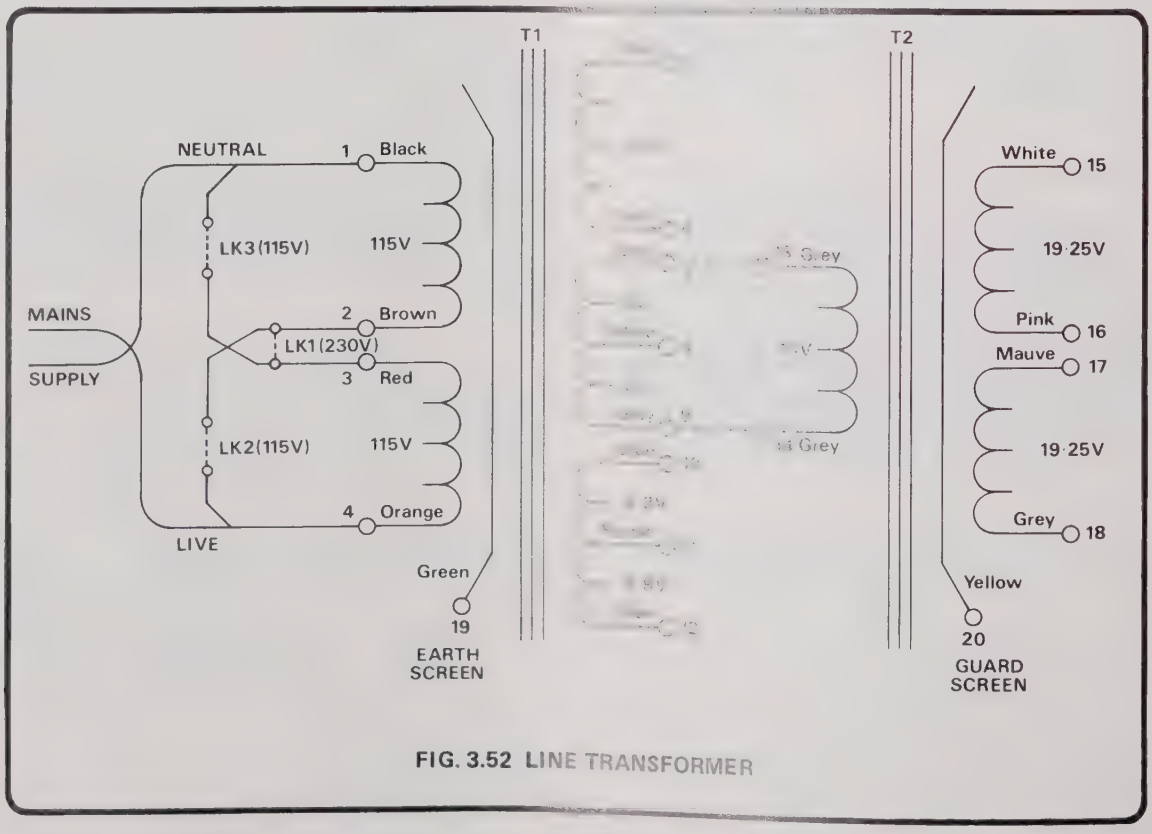


FIG. 3.52 LINE TRANSFORMER

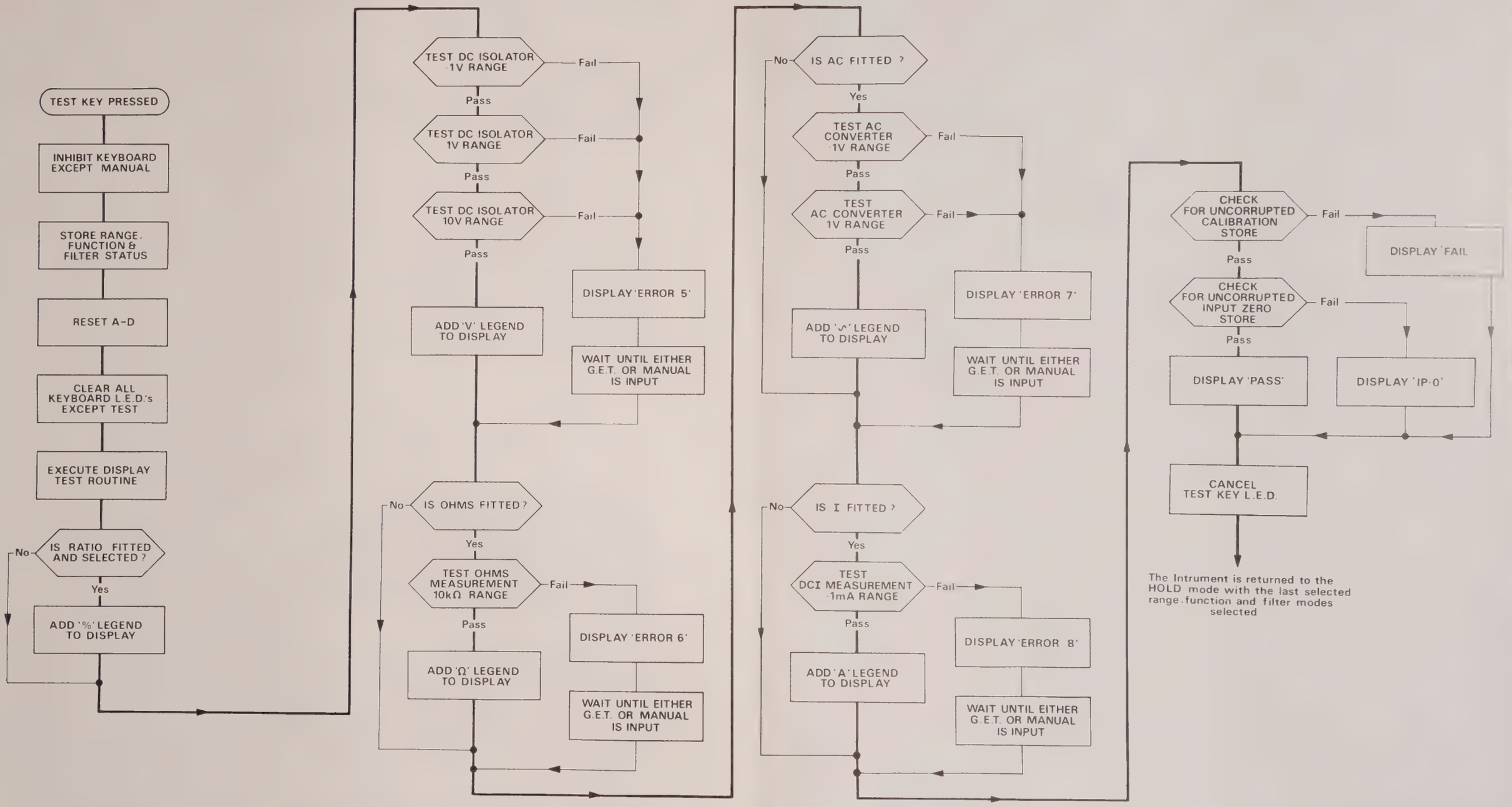


FIG. 3.53 FLOWCHART OF SELF-TEST ROUTINE

SECTION 4

INTERNAL ADJUSTMENT PROCEDURES

4.1 ALTERATION OF LINE VOLTAGE AND LINE FREQUENCY

The instrument is set to 50Hz, 205V to 255V supplies unless Option 80, 81 or 82 is specified. This information is carried on the instrument identification label located on the rear panel. Alteration to a different line voltage/line frequency may necessitate an instrument recalibration.

4.1.1 Changing Line Voltage

1. Disconnect power and all signal input/output leads.
2. Remove the lower cover.
3. Locate the link(s) connecting the split primary on the printed circuit board in front of the toroidal line transformer, Fig. 2.1 and Drawing No 400295.
4. 115V Operation:– Remove LK1 (link 1) and fit LK2 and LK3[1].
   230V Operation:– Remove links LK2 and LK3, and fit LK1[1].
5. Amend instrument identification label.
6. Replace lower cover.
7. Replace power fuses with 160mA anti-surge (230V) or 500mA anti-surge (115V).
8. Carry out the Specification Verification tests (Section 8, User's Handbook) and recalibrate if necessary.

4.1.2 Changing Line Frequency

1. Disconnect power and all signal input/output leads.
2. Remove the top cover.
3. 400Hz Operation:– Remove link LK5 and fit LK7[1] on the Digital assembly, (Drawing No. 400329).
   50/60Hz Operation:– remove link LK7 and fit LK5[1] on the Digital assembly (Drawing No. 400329).
4. Place instrument into HOLD. Adjust L2 (Digital assembly) so that TP7 is 1.05V ±0.03V with respect to Digital Common (TP28).

NOTE: This signal contains about 200mV peak-to-peak high frequency noise.

5. Amend instrument identification label.
6. Replace the top cover.
7. Carry out the Specification Verification tests (Section 8, User's Handbook) and recalibrate if necessary.

[1] Links should be 22 SWG TIN.Cu wire with silicone rubber sleeving.

4.2 BATTERY REPLACEMENT

The battery should be replaced on or before the date indicated on the rear panel instrument identification label. To retain the calibration memory, the instrument must be powered up during replacement. Therefore great care must be taken due to voltages up to 260 volts being present inside the instrument.

1. Remove top cover and locate battery on the Digital assembly (see Fig. 2.1)
2. Power up instrument
3. Desolder battery at end of tags and remove from clip.
4. Replace with new battery, (Datron Part No. 920049) positive terminal to resistor
5. Replace top cover
6. Amend instrument identification label (Current date + 5 years)
7. Carry out the Specification Verification tests (Section 8, User's Handbook) and recalibrate if necessary.

4.3 POST REPAIR PROCEDURES

Apart from the RMS Module (which is available only from Datron), all integrated circuits and semiconductor devices are standard manufacturers' products, and special selection is unnecessary. During manufacture certain resistors are selected in value (FSV = Factory Selected Value) to accommodate circuit component tolerances, or to bring the desired setting of a preset control to the middle of its adjustment range.

The thermal tracking of the DC Preamplifier is particularly important, to ensure a low order of zero drift with variations of temperature. This rather time consuming procedure is carried out initially during manufacture, and need only be repeated following replacement of Q12 or any component associated with the temperature compensation circuitry.

NOTE: A routine calibration as detailed in Section 1 should be carried out after completion of the following procedures.

CAUTION : Up to 260 volts is present inside the instrument. Personal contact with these points may result in injury.

4.3.1 Basic DC Instrument

Equipment Requirements:

4½ digit Digital Voltmeter e.g. Datron 1041
Variable 5V, 1 amp DC supply
5mV/division Oscilloscope e.g. Telequipment D83
1MΩ5% resistor in parallel with 10nF capacitor
10MΩ5% resistor in parallel with 10nF capacitor
DC Voltage Calibrator, e.g. Datron 4000/4000A Autocal Standard.

Procedure:

Power Supplies

1. Turn instrument on and allow 5 minutes warm-up period.

2. Connect DVM Hi to TP8 and Lo to TP28 on the Digital assembly. Adjust R2 on the Rear (Power Supply) pcb assembly to give +5.100V ±25mV.

3. Connect DVM Hi to TP1 and Lo to TP20 on the Analog assembly. Adjust R7 on the Rear (Power Supply) pcb assembly to give +15.000V ±15mV.

4. Connect DVM Hi to TP2 and Lo to TP20 on the Analog assembly. Adjust R12 on the Rear (Power Supply) pcb assembly to give −15.000V ±15mV.

Digital Assembly

5. Switch the instrument off and disconnect the power lead.

6. Isolate the Digital Board by removing the connectors along the centre panel (J1-J5).

7. Connect variable 5V supply and DVM Hi's to TP8, Lo's to TP28. Reduce supply to 4.750V ±10mV.

8. Set R83 fully clockwise. Connect oscilloscope Lo to TP28 and monitor M53 pin 40. Turn R83 anti-clockwise until TP30 undergoes a high to low transition (or begins to pulse low).

9. Remove variable supply and reconnect items disconnected in steps 5 and 6. Disconnect the oscilloscope. Switch on the instrument.

10. Connect DVM Hi to battery positive terminal, Low to TP28. Check battery voltage is >2.5 volts.

11. Disconnect DVM and connect oscilloscope Hi to TP25, Lo to TP28. Adjust scope trigger until the trace is triggered by the first pulse of each reading burst. Adjust R11 so that the pulses occur every 5ms ± 0.5ms.

12. Place instrument into HOLD. Connect oscilloscope Hi to TP7. Adjust L2 to give a stable +1.05 ±0.03V. NOTE: This signal contains about 200mV peak to peak high frequency noise.

13. Insert calibration key into keyswitch on the back panel and turn, placing the instrument into CAL mode.
NOTE: The display CAL legend will be lit.

14. Short together pins 'D' and 'E' on Digital assembly. Clear the calibration store correction factors to zero.

15. Turn the calibration key back to RUN mode.

Assembly (DC Isolator Section)

Controller R150 and R160.

Select 0.1V range DC with FILTER out. Apply a 10MΩ resistor between instrument Hi and Lo. Connect DVM Hi to TL8, Lo to TP20.
Adjust P/V R152 with a metal film resistor (50ppm/°C) for a reading of < 10mV, using R159 for fine adjustments.
Solder in R152.

Apply a short circuit across the input terminals and adjust R150 for a reading of < 50μV at TP13.

Connect DVM Hi to TP33 and adjust R160 for a reading of <20μV.

Repeat steps 17 to 19 until readings are within limits.

The following step is only required after the replacement of Q12 or any component associated with the temperature compensation circuitry.

(i) Re-apply 10MΩ resistor across the input terminals. Note the reading on the front panel display (=A).
(ii) Note the ambient temperature (=X°C).
(iii) Place the instrument in a temperature controlled oven at approx 50°C without top cover and with power 'on'.
(iv) Leave the instrument for at least 1 hour then note the reading on the display (=B) and the temperature of the oven (=Y°C).
(v) Compute (B−A)/(Y−X) = Drift/°C.
(vi) Remove instrument from oven and allow to stabilize, with power 'on' to ambient for one hour.
(vii) If the drift was < 10 digits/°C proceed to (x).
(viii) For drifts in excess of 10 digits/°C R151 must be adjusted. If the drift was positive turn R151 clockwise, if negative turn R151 anti-clockwise.
(ix) Repeat from (i).
(x) Lock R151 with a clean soldering iron.
(xi) Repeat steps 17 to 20.

22. Solder in R152, with instrument turned off.

Analog Assembly (A-D Converter)

23. Select 100V range and apply short circuit between Hi and Lo. Connect DVM Hi to TP7, Lo to TP20. If reading is +6.42V ±0.03V proceed to step 25.

24. Switch off instrument and make positive reference links B & C, if cut i.e. the links alongside TP7. Switch on instrument and measure voltages on TP7 once again. Consult Fig. 4.1 and cut links as indicated. Repeat step 23.

Voltage on TP7 or TP8	TL'B'	TL'C'
6.42	–	–
6.47	–	✓
6.525	✓	–
6.58	✓	✓

Select voltage closest to measured value and cut links

FIG 4.1 REFERENCE SELECTION VOLTAGES

25. Connect DVM Hi to TP8. If reading is –6.42V ± 0.03V proceed to step 27.

26. Switch off instrument and make negative reference links A to C, if cut i.e. the links alongside TP8. Switch on instrument and measure voltage on TP8 once again. Consult Fig 4.1 and cut links as indicated. Repeat Step 25.

27. Select HOLD. Connect DVM Hi to TP9. Select correct resistance value for FSV R11 or R15 to give a reading of 0V ±1mV. Solder in resistor.

28. Deselect HOLD and disconnect DVM. Select 1000V range and apply +19mV. Connect oscilloscope Lo to TP21, Hi to TP5. Adjust R20 for noisy waveform at zero point.

29. Remove oscilloscope. Replace covers but do not replace screws. Select 1V, DC, filter out and apply 1MΩ across input terminals. Turn rear panel keyswitch to CAL mode and select LIN.

30. Select .1V range and apply 10MΩ across input terminals. Select Ib. Repeat until display reads less than 50 digits.

31. Select 10V range, FILTER and apply short copper link across input terminals. Select ZERO.

32. Apply +10 volts and select GAIN. Repeat until display reads +10.0000 ± ½ digit.

33 Apply +19 volts. If the display reads within the limits +18.9999 to +19.0001, proceed to step 35.

34 Calculate E = (19 - displayed reading)/2.
Re-apply +10 volts and adjust R23 for a displayed reading of 10 - E.
Repeat steps 32-34 until both readings are within the limits indicated.

35 Turn rear panel keyswitch to RUN mode.

The basic DC only instrument set-up procedure is complete.

4.3.2 Ohms Assembly

Equipment Required
5½ digit DVM, e.g. Datron 1065, or 1061.
10 Megohm 5% resistor in parallel with a 10nF capacitor, e.g. Datron Part No. 400392
Copper shorting links, and a short wire link.

Procedure.

1. Select 10 kilohm range, 4-wire. Connect I– to Ohms Guard, I+ to Hi, and 10 Megohm resistor between Hi and Lo.

2 Connect DVM Hi to TP4, Lo to TP1 and adjust R26 (bias current) for a reading of Zero ± 300μV.

3 Disconnect the 10 Megohm resistor, and connect a copper shorting link in its place between Hi and Lo.

4 Transfer DVM Hi from TP4 to TP14, and adjust R27 (Q10 offset) for Zero ± 2μV.
N.B. Ensure that the DVM used for measurement is correctly zeroed!

5. Repeat steps 1-4 until the readings are within the specified limits.

6. Connect Lo to Ohms Guard. Connect a shorting link between TP1 and TP3.

7. Transfer DVM Hi from TP14 to TP13, and check that the reading is zero ± 50μV.
If reading > +50μV, reselect FSV resistor R40 to bring the reading within limits.
If reading < –50μV, reselect FSV resistor R39 to bring the reading within limits.
NB R39 and R40 must each be at least 100 kilohms.

8. Remove the link from TP1 and TP3. Disconnect the DVM leads, and the connections from the front panel.

The basic Ohms set-up procedure is now complete.

4.3.3 OPTION 10 AC Assembly

Equipment Required:

5mV/Div oscilloscope. e.g. Telequipment D83.
5½ digit DVM with Ohms. e.g. Datron 1065, 1061.
DC calibrator. e.g. Datron 4000 or 4000A.
AC calibrator. e.g. Datron 4200.
Asymmetric signal, 1V RMS, Crest Factor 5:1 ±0.02%, reversible polarity.

Procedure

1. Select AC 1000V range and HOLD. Short Hi to Lo. Connect DVM Hi to TL7, Lo to TP8 and note reading. Select 1V range and adjust R121 (bias current) to give same reading ±10μV.

2. Select 100mV range, AC + DC and adjust R112 (offset adjust) for an indication of zero ±50μV on the DVM.

3. Repeat steps 1. and 2. until readings are within the specified limits.

4. Select 10V range and HOLD. Connect oscilloscope Hi to TP5, Lo to TP8 and adjust R90 (rectifier zero) for maximum noise about zero. Remove the oscilloscope.

5. Connect DVM Hi to TP2, Lo to TP8 and adjust R75 (linearity) for an indication on the DVM of 1.8mV ±10%.

6. Select AC, 1V range, FILTER and apply 1V 500Hz. Connect DVM Hi to TL5, Lo to TP8. If reading is +3.157V ±0.01V proceed to step 8.

7. Disconnect input signal and switch off instrument, Make links TL1 to TL4 if cut. Switch on instrument, reselect AC, 1V range, FILTER and reapply 1V, 500Hz. Measure voltage on TL5. Consult Fig. 4.2 and cut links as indicated. Check voltage on TL5 is 3.157V ±0.01V. Remove the DVM.

8. Deselect HOLD and short circuit instrument Hi and Lo. Turn rear panel key switch to CAL mode and select ZERO. Repeat for all ranges.

9. Select 1V range. Apply 1 volt (d.c.) and note reading on display. Apply −1 volt (d.c.) and adjust R50 (d.c. turnover) for same display indication ±10 digits.

10. This part of the procedure must be performed with the high frequency compensation voltage, at J1-11/ R57, at −5V ±0.2V.

    a. Select AC 100V range, FILTER and apply 100V, 500Hz. Select GAIN. Apply 100V, 50kHz and adjust C62 for a display of 100.000V ±20 digits.

[illegible]0V, 100kHz note error and adjust C61 [illegible]le the displayed error in the same direct-

[illegible] b. until 50kHz and 100kHz displays [illegible] ±20 digits.

[illegible] range and apply 1 volt, 500 Hz. Select [illegible] 1V 50kHz and adjust C63 for a [illegible].00000V ±20 digits.

[illegible] 5:1 crest factor signal. Adjust R35 [illegible] for a display of 1.00000V ±30 digits.

[illegible]nput. Turn rear panel key switch to [illegible]TEST and check for a display of PASS. [illegible]l key switch to CAL.

[illegible] range and apply 10V, 50kHz. Check [illegible]0.0000V ±1200 digits. Check that the [illegible] calibrated to 10.0000 ±20 digits [illegible] 5 presses of the AC HF key.

[illegible] range and apply 100mV 50kHz. [illegible] 100.000mV ±500 digits. Check that [illegible] calibrated to 100.000mV ±20 digits [illegible] presses of the AC HF key.

[illegible]ange and apply 1000V, 500Hz. Select [illegible]

[illegible]', 25kHz and check display is 1000.00V [illegible] Check that display can be calibrated [illegible]20 digits by less than 5 presses of the [illegible] Remove 1000V and turn rear panel key [illegible]

[illegible] procedure is complete.

[illegible]	TL1	TL2	TL3	TL4
[illegible]	–	–	–	–
[illegible]	–	–	–	✓
[illegible]	–	–	✓	–
[illegible]	–	–	✓	✓
[illegible]	–	✓	–	–
[illegible]	–	✓	–	✓
[illegible]	–	✓	✓	–
[illegible]	–	✓	✓	✓
[illegible]	✓	–	–	–
[illegible]	✓	–	–	✓
[illegible]	✓	–	✓	–
[illegible]	✓	–	✓	✓
[illegible]	✓	✓	–	–
[illegible]	✓	✓	–	✓
[illegible]	✓	✓	✓	–
[illegible]	✓	✓	✓	✓

[illegible]ge closest to measured value and cut links

FIG 4.2 OPTION 10 AC ASSEMBLY OUTPUT SELECTION VOLTAGES

4.3.4 OPTION 12 AC Assembly

Equipment Required:

5mV/Div oscilloscope. e.g. Telequipment D83.
5½ digit DVM with Ohms. e.g. Datron 1065, 1061.
DC calibrator. e.g. Datron 4000 or 4000A.
AC calibrator. e.g. Datron 4200.
Asymmetric signal, 1V RMS, Crest Factor 5:1 ±0.02%, reversible polarity.

CAUTION
The following procedures should commence with the HF Autocal voltage close to the center of its span. To check this, select the 100V AC range and measure the DC voltage at J1-11 with respect to TP8. If it is between +4V and +6V, it is NOT necessary to clear the calibration stores. If outside these limits, the cal stores should be cleared as described in para 4.3.1 operations (13), (14) and (15).

CLEARING THE CAL STORES ENTAILS A FULL 'AUTOCAL' OF THE INSTRUMENT!

Before proceeding; ensure that at least the Analog Assembly LIN and I_b Autocalibrations have been carried out.

AC Preamplifier Zero

1. Read and comply with the CAUTION above.
2. Apply short circuit input. Select AC + DC, 100mV range and HOLD.
3. Connect DVM Lo to TP8, Hi to Test link K (TLK). Adjust R148 (bias current) for a reading of zero, ±140μV.
4. Select 100mV range AC, and check that the reading is zero, ±140μV. It may be necessary to re-adjust R148 to obtain this value. If so, recheck operation 3.
5. Select each range in turn, and check that the DVM reading is within ±70μV of zero (except 100mV range: ±140μV).

Set up RMS Converter

6. Select 10V range. Adjust R119 (Rectifier zero) for the most negative (or least positive) reading on the display.
7. Connect DVM to TLH. Adjust R101 (linearity) for a reading of +1.1mV±10%.
8. Select 100mV range. Check that the DVM reading is between 0.8mV and 1.8mV.
9. Select 1V range and apply 1V, 500Hz, with the DVM still connected to TLH. Remake links TLC-TLF. Then after recording the TLH voltage, refer to Fig. 4.2 and cut the links (✓) as appropriate to give a DVM reading of 3.157V ±0.010V.

TLH Voltage	Cut Test Links C	D	E	F	Gain*
2.887 - 2.904	✓	✓	✓	✓	1.090
2.904 - 2.920	✓	✓	✓	x	1.084
2.920 - 2.937	✓	✓	x	✓	1.078
2.937 - 2.954	✓	✓	x	x	1.072
2.954 - 2.973	✓	x	✓	✓	1.065
2.973 - 2.988	✓	x	✓	x	1.059
2.988 - 3.004	✓	x	x	✓	1.054
3.004 - 3.021	✓	x	x	x	1.048
3.021 - 3.038	x	✓	✓	✓	1.042
3.038 - 3.055	x	✓	✓	x	1.036
3.055 - 3.071	x	✓	x	✓	1.031
3.071 - 3.090	x	✓	x	x	1.025
3.090 - 3.110	x	x	✓	✓	1.018
3.110 - 3.128	x	x	✓	x	1.012
3.128 - 3.147	x	x	x	✓	1.006
3.147 - 3.167	x	x	x	x	1.000

*Increase in TLH voltage when links are cut.

FIG. 4.3 OPTION 12 AC ASSEMBLY OUTPUT SELECTION VOLTAGES

Check Spec Readout Frequency Flags

10. Select HOLD. Connect DVM to TP6. Adjust the applied frequency and note that TP6 changes logic state at a frequency between 1.8kHz and 2.2kHz. Disconnect the DVM.

Set Range 'Zeros'

11. Deselect HOLD, and apply 500Hz at 0.1% FR input to each range in turn. Perform ZERO autocal on each range, using the instrument display to check that each range calibrates to 100 digits ±3 digits. Disconnect the input.
12. Apply a short circuit to the input, short Guard to Lo and select each range in turn. Check that the reading on each range is zero ±10 digits on the display (except 100mV range ±30 digits). Remove the shorts.

Set up DC-DC Turnover

13. Select 1V range, AC + DC. Apply 1V 500Hz and perform GAIN autocal.

14. Apply +1V DC and note the displayed reading.

15. Apply −1V DC and adjust R62 (DC turnover) for the same reading as in operation (14). (±3 digits).

16. Repeat (13) to (15) until all readings are the same to within ±20 digits.

Set up Coarse Frequency Response

17. Select 100V range, AC; apply 100V, 500Hz and perform GAIN autocal. Apply 100V, 50kHz and adjust C82 for a display reading of 100.000V ±20 digits. (If necessary change C81 to a value which permits this adjustment).

18. Apply 100V, 100kHz and note the reading error. Adjust C79 to give 5 times the error in the same direction.

19. Repeat (17) and (18) until the 50kHz and 100kHz readings are separated by less than 20 digits.

20. Select 1V range, AC; apply 1V, 500Hz and perform GAIN autocal. Apply 1V, 50kHz and adjust C84 for a display reading of 1.00000V ±20 digits. (If necessary change C85 to a value which permits this adjustment).

Set up Crest Factor

21. Apply 1VRMS, +ve 5:1 Crest Factor signal. Adjust R61 (crest factor) for a display reading of 1.00000V ±30 digits.

22. Apply 1VRMS, −ve 5:1 Crest Factor signal. Check that display reading is 1.00000V ±30 digits.

23. Apply 1V, 500Hz, and perform GAIN Autocal. Repeat (21), (22) and (23) until crest factor readings are within limits.

Linearity Checks

24. Select 1V range, AC + DC. Apply 1V DC and perform GAIN Autocal.

25. Apply 1.9V DC and adjust R27 value (Factory Selected Value - FSV) for a display reading of 1.90000V ±2 digits (reducing R27 increases reading).

26. Repeat (24) and (25) until both correct.

27. Select 1V range AC. Apply in turn 1V, 100mV, 10mV, at 500Hz and check that display reading is to within ±10 digits of the input voltage.

28. Apply open circuit input, set CAL/RUN switch to RUN; press 'Test' and check for a display of 'PASS'.

Output buffer check

29. Select 1V range, AC + DC, no filter. Apply 1V DC and set CAL switch to RUN. Use the 'A-B' computation mode to null out the reading: press STORE, B, then (A-B)

30. Select filter, and leave to settle for at least 30 seconds. Check that the displayed reading is within ±50 digits of zero.

31. Repeat (24).

The AC set-up procedure is now complete.

APPENDIX 1

ANALOG DATA LINE 'F.E.T.' PATTERNS

DC Voltage

Range		DC Isolator							
		AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1	100mV	0	0	0	0	0	1	1	X
2	100mV	0	0	0	0	0	1	1	X
3	1V	0	0	0	0	1	1	1	X
4	10V	0	0	0	0	1	0	1	X
5	100V	0	0	0	0	1	1	0	X
6	1000V	0	0	0	0	1	0	0	X
7	1000V	0	0	0	0	1	0	0	X

AC Voltage

Range		AC assembly							
		AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1	100mV	0	0	1	0	0	0	1	X
2	100mV	0	0	1	0	0	0	1	X
3	1V	0	0	1	0	0	0	0	X
4	10V	0	0	0	1	0	0	0	X
5	100V	0	0	0	0	1	0	0	X
6	1000V	0	0	0	0	0	1	0	X
7	1000V	0	0	0	0	0	1	0	X

DC Coupled AC Voltage

Range		AC assembly							
		AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1	100mV	0	1	1	0	0	0	1	X
2	100mV	0	1	1	0	0	0	1	X
3	1V	0	1	1	0	0	0	0	X
4	10V	0	1	0	1	0	0	0	X
5	100V	0	1	0	0	1	0	0	X
6	1000V	0	1	0	0	0	1	0	X
7	1000V	0	1	0	0	0	1	0	X

Ohms

Range	DC Isolator								Ohms assembly							
	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1 10Ω	0	0	0	0	0	1	1	X	[illegible]		0	0	0	0	1	X
2 100Ω	0	0	0	0	1	1	1	X	[illegible]		0	0	0	0	1	X
3 1kΩ	0	0	0	0	1	1	1	X	[illegible]		0	0	0	1	0	X
4 10kΩ	0	0	0	0	1	1	1	X	[illegible]		0	0	0	0	0	X
5 100kΩ	0	0	0	0	1	1	1	X	[illegible]		0	0	1	0	0	X
6 1MΩ	0	0	0	0	1	1	1	X	[illegible]		1	1	0	0	0	X
7 10MΩ	0	0	0	0	1	1	1	X	[illegible]		0	1	0	0	0	X

DC Current

Range	DC Isolator								Current assembly							
	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7	D0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1 100μA	0	0	0	0	0	1	1	X	[illegible]		0	0	0	0	1	X
2 100μA	0	0	0	0	0	1	1	X	[illegible]		0	0	0	0	1	X
3 1mA	0	0	0	0	0	1	1	X	[illegible]		0	0	0	0	1	X
4 10mA	0	0	0	0	0	1	1	X	[illegible]		1	0	0	0	1	X
5 100mA	0	0	0	0	0	1	1	X	[illegible]		0	1	0	0	1	X
6 1A	0	0	0	0	0	1	1	X	[illegible]		0	0	1	0	1	X
7 1A	0	0	0	0	0	1	1	X	[illegible]		0	0	1	0	1	X

AC Current

Range	AC assembly								Current assembly							
	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1 100μA	0	0	1	0	0	0	1	X	[illegible]		0	0	0	1	0	X
2 100μA	0	0	1	0	0	0	1	X	[illegible]		0	0	0	1	0	X
3 1mA	0	0	1	0	0	0	1	X	[illegible]		0	0	0	1	0	X
4 10mA	0	0	1	0	0	0	1	X	[illegible]		1	0	0	1	0	X
5 100mA	0	0	1	0	0	0	1	[illegible]	[illegible]		0	1	0	1	0	X
6 1A	0	0	1	0	0	0	1	[illegible]	[illegible]		0	0	1	1	0	X
7 1A	0	0	1	0	0	0	1	[illegible]	[illegible]		0	0	1	1	0	X

DC Coupled AC Current

Range	AC assembly								Current assembly							
	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
1 100μA	0	1	1	0	0	0	1	X	[illegible]	1	0	0	0	1	0	X
2 100μA	0	1	1	0	0	0	1	X	[illegible]	1	0	0	0	1	0	X
3 1mA	0	1	1	0	0	0	1	X	[illegible]	0	0	0	0	1	0	X
4 10mA	0	1	1	0	0	0	1	X	1	1	1	0	0	1	0	X
5 100mA	0	1	1	0	0	0	1	[illegible]	[illegible]	1	0	1	0	1	0	X
6 1A	0	1	1	0	0	0	1	[illegible]	[illegible]	1	0	0	1	1	0	X
7 1A	0	1	1	0	0	0	[illegible]	[illegible]	1	1	0	0	1	1	0	X

TEST

Function Tested	Range Checked	DC Isolator								Option assembly							
		AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7	AD0	AD1	AD2	AD3	AD4	AD5	AD6	AD7
DC	.1	0	0	0	0	0	1	0	1								
	1	0	0	0	0	1	1	0	1								
	10	0	0	0	0	1	0	0	1								
kΩ	10	0	0	0	0	1	1	1	1	Ohms assembly 0	1	0	1	0	0	0	1
AC	1	Not used								AC assembly 0	1	1	0	0	0	1	0
	1									0	1	1	0	0	0	0	0
I	1	0	0	0	0	0	1	1	1	I assembly 0	1	0	0	0	0	1	1

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. OFF Per Assy.
R1	090001	P.T.C. THERMISTOR	MULLARD	VA8650	2
R2	090001	P.T.C. THERMISTOR	MULLARD	VA8650	—
R3	000151	150Ω 1/4W CARBON	MULLARD	CR25	8
R4	000151	150Ω "			—
R5	000151	150Ω "			—
R6	000102	1K 1/4W. CARBON	"	CR25	2
R7	000104	100K 1/4W. CARBON	MULLARD	CR25	1
R8	000151	150Ω 1/4W CARBON	MULLARD	CR25	—
R9	000151	150Ω "	"		—
R10	000151	150Ω "	"		—
R11	000151	150Ω "	"		—
R12	000151	150Ω "	"		—
R13	000102	1K "	"		—
R14	000472	4K7 "	"		1
AN1	090032	150Rx7 2% NETWORK	BECKMAN	764-1-R150	2
AN2	090032	" " "	"		—
C1	102101	100PF CER DISC	ERIE	801	1
C2	150002	10μF 20% 16V DIP TANT	UNION CARBIDE	K10E16	2
C3	150016	1·0 μF 20% 35V "	UNION CARBIDE	KIR0E35	2
C4	101103	0·01μF 250V CER DISC	ERIE	801	3

NOTES CIRCUIT DIAG 430294
CHECK PROC. 460294
CHECK LIST 470294
SEE SHEET 2 FOR LATEST ISSUE

ISS	C	D	1	2	3	4	5
ECO	–		RELEASED				
DATE		17-8	18.9				
CHKD							

ND ND ND ND ND ND ND

28-4-81

datron
TITLE 1061/1071/1081 FRONT P.C.B ASSY
DRAWING NUMBER 400294 SHEET 2 OF 6

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. OFF Per Assy.
C5	101103	0·01μF 250V CER DISC	ERIE	801	—
C6	150016	1·0 μF 20% 35V DIP TANT	UNION CARBIDE	KIR0E35	—
C7	101103	0·01μF 250V CER DISC	ERIE	801	—
C8	150002	10μF 20% 16V DIP TANT	UNION CARBIDE	K10E16	—
C9	104023	2n2F 20% 1KV CER DISC	ITT	HD16K102N2MS-SSIK0DSC	1
C10		NOT USED			
C11	150006	4μ7 20% 16V DIP TANT	UNION CARBIDE	K4R7E16	1
Q1	240001	Si NPN	NATIONAL	BC184K	6
Q2	240001	Si NPN	NATIONAL	BC184K	—
Q3	240001	" "	"	"	—
Q4	240001	" "	"	"	—
Q5	240001	" "	"	"	—
Q6	240001	" "	"	"	—
M1	290042	GP. HIGH CURRENT. TRANS ARRAY	R.C.A.	CA3081P	3
M2	280011	DUAL D FLIP-FLOP	MOTOROLA	MC14013 BCP	1
M3	290042	GP HIGH CURRENT TRANS ARRAY	R.C.A	CA3081P	—
M4	280015	QUAD LATCH	MOTOROLA	MC14076	7

NOTES.

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ISS												
ECO												
DATE												
CHKD												

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TITLE 1061/71/81 FRONT. P.C.B ASSY.
DRAWING NUMBER 400294 SHEET 3 OF 6

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
M5	280015	QUAD LATCH	MOTOROLA	MC14076	—
M6	280015	" "	"	"	—
M7	280067	16WAY KEYBOARD ENCODER	NATIONAL	MM74C922	2
M8	280015	QUAD LATCH	MOTOROLA	MC14076	—
M9	280015	QUAD LATCH	"	"	—
M10	280067	16 WAY. KEYBOARD ENCODER.	NATIONAL	MM74C922	—
M11	280015	QUAD LATCH	MOTOROLA	MC14076	—
M12	280015	" "	"	"	—
M13	290042	G.P. HIGH CURRENT. TRANS ARRAY	RCA	CA3081P	—
S1	700019	SLIDE SWITCH	SIEMENS	C-42315-A60-A1	2
S2	700019	" "	"	"	—
S3	700061	KEYBOARD SWITCH. RED. LED	SCHADOW	SRL-RED LED	24
S4	700061	" " " "	"	" " "	—
S5	700061	" " " "	"	" " "	—
S6	700061	" " " "	"	" " "	—
S7	700061	" " " "	"	" " "	—
S8	700061	" " " "	"	" " "	—
S9	700061	" " " "	"	" " "	—
S10	700062	KEYBOARD SWITCH. GREEN LED	SCHADOW	SRL-GREEN LED	2

NOTES.

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TITLE 1061/71/81 FRONT. P.C.B ASSY

DRAWING NUMBER 400294

SHEET 4 OF 6

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
S11	700061	KEYBOARD. SWITCH. RED. LED.	SCHADOW	SRL-RED LED	—
S12	700061	" " " "	"	" " "	—
S13	700061	" " " "	"	" " "	—
S14	700061	" " " "	"	" " "	—
S15	700061	" " " "	"	" " "	—
S16	700061	" " " "	"	" " "	—
S17	700061	" " " "	"	" " "	—
S18	700061	" " " "	"	" " "	—
S19	700061	" " " "	"	" " "	—
S20	700061	" " " "	"	" " "	—
S21	700061	" " " "	"	" " "	—
S22	700061	" " " "	"	" " "	—
S23	700061	" " " "	"	" " "	—
S24	700061	" " " "	"	" " "	—
S25	700061	" " " "	"	" " "	—
S26	700061	" " " "	"	" " "	—
S27	700061	" " " "	"	" " "	—
S28	700062	KEYBOARD SWITCH. GREEN. LED.	SCHADOW	SRL-GREEN LED	—

NOTES.

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TITLE 1061/71/81 FRONT. P.C.B. ASSY

DRAWING NUMBER 400294

SHEET 5 OF 6

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURERS PART No	MANUFACTURERS PART No	No. OFF Per Assy.
	400573	SIGNAL CABLE ASSY			1
	450451-2	RM10 INSULATOR			1
	410090-7	PRINTED CIRCUIT BOARD			1
	450179-1	TERMINAL PLATE			1
	605060	IC SOCKET 14 WAY	AUGAT	314-AG-39D	1
	605061	IC SOCKET 16 WAY	AUGAT	316-AG-39D	10
	605062	IC SOCKET 18 WAY	AUGAT	318-AG-39D	2
	630099	MASKING TAPE	3M		A/R
	630024	INSULATING BEADS			8
	800017	8½ DIGIT DISPLAY	DALE		1
	920015	LOW EMF TERM BLK/BLK	CLIFF	TPI - SPECIAL	1
	920041	" " BLK/RED	CLIFF	" "	1
	920042	NOT LOW EMF TERM BLK/WH	"	TPI	1
	920043	" " " BLK/BRN	"	"	1
	920044	" " " BLK/BLUE	"	"	1
	920045	" " " BLK/YELLOW	"	"	1
	630029	DOUBLE SIDED PRESSURE SENSITIVE TAPE	3M	TYPE 4932	290mm
	420080-	WARNING LABEL			1

NOTES.

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DATE													
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061/71/81

FRONT PCB ASSY.

400294

SHEET 6 OF 6

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R1		NOT USED			–
R2	066200	20R POT 3/8 SQ VERT. CERMET.	BECKMAN	72XW	1
R3	000221	220R. 5% 1/4W CARBON.	MULLARD	CR25	1
R4	014320	432R 1% M.F.	HOLCO	H.8	1
R5	000102	1K 5% 1/4W CARBON	MULLARD	CR25	2
R6	001184	180K 5% 1/2W CARBON	MULLARD	CR37	1
R7	066102	1K 3/8" RIGHT ANGLED CER. POT.	BECKMAN	72XW	1
R8	014021	4K02 1% 1/8W M.F.	HOLCO	H.8.	1
R9	019091	9K09 1% 1/8W M.F.	HOLCO	H.8.	1
R10	012001	2K 1% 1/8W M.F.	HOLCO	H.8.	1
R11	011302	13K 1% 1/8W M.F.	HOLCO	H.8.	1
R12	066501	500R 3/8" RIGHT ANGLED CER. POT	BECKMAN	72XW	1
R13	000102	1K 5% 1/4W CARBON.	MULLARD	CR25	–
L1	370001	10μH 0·85Ω R.F. CHOKE.	PLESSEY	5B/10/0011/10	3
L2	370001	10μH " "	"	"	–
L3	370001	10μH " "	"	"	–
C1	NOT USED				–
C2	NOT USED.				–
C3	NOT USED.				–

NOTES. CIRCUIT DIAG. 430295.
CHECK PROC. 460295.
CHECK LIST 470295.
SEE SHEET 2 FOR LATEST ISSUE

ISS.	C	D	1 [C]	2	3 [C]	4	5	6	7	8	9	10	11	12
ECO	–	–	RELEASED	ECO 783	ECO 816	8°16'82"00°	8°8'8"00°	10°8'5"00°	1441	1452	1470	1517	1529	1688
DATE	–	22-8-78	29-9-78	8-12-78	25 JAN 79	6 JUN 79	31-10-79	21.4.80	11-2-83	16.2.83	1.6.83	16-8-83	15-9-83	20.8.84
CHKD	–	NO		MD	MD	MD	MD	MD	MD	MD	MD	MD	MD	MD

DATE 2-5-78
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DATE

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TITLE 1061/1071/1081 REAR P.C.B. ASSY.
DRAWING NUMBER 400295
SHEET 2 OF 6

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
C4		NOT USED			
C5	101103	0·01μF 250V CER DISC	ERIE	801	3
C6	180026	10μF 350V ELECT	ITT	EN12/12 10/350	1
C7	180004	4700μF 16V AL ELECT	WIMA	PRINTILYT	1
C8	104026	47nF $^{+50}_{-20}$% 50V CER DISC	SIEMENS	B37449	1
C9	150003	47μF 20% 6V3 DIP TANT	UNION CARBIDE	K47E6V3	1
C10	150021	22μF 20% 25V DIP. TANT	UNION CARBIDE	K22E25	2
C11	150021	" " " "	" "	"	–
C12	101103	0·01μF 250V. CER. DISC.	ERIE	801	–
C13	180025	1000μF 35V ELECT.	WIMA	PRINTILYT	2
C14	101103	0·01μF 250V. CER DISC	ERIE	801	–
C15	180025	1000μF 35V ELECT	WIMA	PRINTILYT	–
C16	102102	1nF 10% 500V CER DISC	ITT	CD10.	1
D1	200022	Si RECTIFIER 3A 400V	MOTOROLA	BY252	2
D2	200022	" " "	" "	"	–
D3	210068	6V8 400mW ZENER	MULLARD	BZY88C6V8	1
D4	213004	180V 500mW ZENER	MOTOROLA	1N5279B	1

NOTES.

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ISS														
ECO														
DATE														
CHKD														

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TITLE 1061/71/81 REAR P.C.B ASSY.
DRAWING NUMBER 400295
SHEET 3 OF 6

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
Q1	240018	300V NPN TRANSISTOR	MOTOROLA	MJE 340	2
Q2	240018				—
M1	260068	5V 1½A REGULATOR	NATIONAL	LM309K/ALUM	1
M2	260024	POSITIVE VOLTAGE REGULATOR	FAIRCHILD	μA 78 MGUIC	1
M3	260023	NEGATIVE VOLTAGE REGULATOR	FAIRCHILD	μA 79 MGUIC	1
W1	209014	1A5 400V BRIDGE RECT	MICRO-ELECTRONICS	[illegible]	1
W2	209003	100V 1.5A BRIDGE RECT	MICRO-ELECTRONICS	[illegible]	1
J1					
J2	620003	SOLDER PCB TERMINAL LUG	[illegible]	M2105A	5
J3	604033	4 WAY FLAT GOLD WAFER PIN	MOLEX	22-27-2041/GOLD	7
J4	604033	"			—
J5	604033	"			—

NOTES

SEE SHEET 2 FOR LATEST ISSUE

ISS													
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DATE													
CHKD													

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TITLE 1061/71/81 REAR P.C.B. ASSY
DRAWING NUMBER 400295
SHEET 4 OF 6

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
J6	604033	4 WAY FLAT GOLD WAFER PIN	MOLEX	22-27-2041/GOLD	—
J7	604033	"	"	"	—
J8					
J9	604033	4 WAY FLAT GOLD WAFER PIN	MOLEX	22-27-2041/GOLD	—
J10	604033	"	"	"	—
J11	604033	"	"	"	—
J12	604033	"	"	"	—
J13	604033	"	"	"	—
J14					—
	410091-5A	PRINTED CIRCUIT BOARD			1
	450180-2	HEATSINK 5V	ADVANCE		1
	450183-1	HEATSINK 15V	ADVANCE		3
	540002	22SWG TIN CU WIRE			A/R
	512999	7/·2 PTFE WIRE WHITE			A/R
	611037	SCREW M3x8mm. NYLON HEX HD.	NYLON ALLOYS	BSG210 TYPE C	8
	613005	WASHER M3 INT/SHAKEPROOF ST.	GKN DISTRIBUTORS		4
	613017	WASHER M3 FLAT NYLON	NYLON ALLOYS		8
	615002	NUT. M3 FULL HEX STEEL	GKN	ZINC PLATED	4
	615008	NUT. M3 FULL HEX NYLON	GKN		2

NOTES.

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ISS													
E.C.O.													
DATE													
CHKD													

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TITLE 1061/71/81 REAR P.C.B. ASSY
DRAWING NUMBER 400295
SHEET 5 OF 6

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
	611016	SCREW M3x 8mm STEEL POZ-PAN ZINC PL.	GKN		6
	618007	MELINEX WASHERS 12·7x 16·5x 0·05mm.	JERMYN.	J26-5001	2
	620007	TEST POINT TERMINAL	MICROVAR	C30	9
	618001	INSULATING BUSH	JERMYN	A1218	2
	618009	INSULATING PAD SIL TO3	WARTH	3223-07FR-06	1
	590004	SLEEVE - PTFE	HELLERMANN ELECTRIC	FE10	A/R.
	900004	SILICONE RUBBER COMPOUND	R.S.	554-119.	A/R
	420080-1	WARNING LABEL			2
	613029	M3 CRINKLE WASHER SS			2
	613009	4BA SOLDER TAG BRASS	TIN PL.		2
	590001	SLEEVE MAX. CABLE ⌀3·0	HELLERMANN ELECTRIC	H15x20mm BLACK HELSYN	3
	590006	HEATSHRINK SLEEVE ⌀2·4 INT	RS OR HELLERMANN ELECTRIC	399-495 OR LVR24	20mm
	630024	STEATITE BEAD 16 SWG	PARK ROYAL PORCELAIN	N° 2	2

NOTES.

SEE SHEET 2 FOR LATEST ISSUE

ISS.												
ECO												
DATE												
CHKD												

DATE 2-5-78

DRAWN B.J.

CHECKED MHH

APPROVED

DATE

datron ELECTRONICS LTD

TITLE 1061/71/81 REAR P.C.B ASSY

DRAWING NUMBER 400295

SHEET 6 OF 6

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
J2	572115 /C	16 WAY RIBBON CABLE ASSY	DATRON		1
J3	604035	4CCT. RIGHT ANGLED WAFER. GOLD	MOLEX	22-12-2041	5
J4	604035	" " " "	"	"	—
J6	604035.	" " " "	"	"	—
	410092-5A	P.C.B.			1
J1 & J5	604036	STRIP OF 10 AMP PINS	AMP	163740-8	4
	630023	SCOTCHFLEX ADHESIVE CLIP	3M	CLIP 706	1
	630099	25mm MASKING TAPE	3M	SCOTCH N 230	A/R
	620007	TEST POINT TERMINAL	MICROVAR	C 30	2
R1	000473	47K 5% ¼W CARBON	MULLARD	CR25	2
R2	000473	47K " " "	"	"	—
D1	200002	Si RECTIFIER 1A 50V	FAIRCHILD	IN4001	2
D2	200002	" " " "	"	"	—

NOTES CIRCUIT DIAGRAM . 430296
CHECK PROC. . 460296.
CHECK LIST . 470296.
SEE SHEET 2 FOR LATEST ISSUE

ISS	C	D	1	2	3	4	5	6	7	8
ECO	-	-	RELEASED	ECO 784	ECO B-69	861	904	992	1000	1102
DATE	-	25-8-78	29-9-78	6-12-78	4-5-79	16-7-79	25-9-79	8-11-80	14-4-80	
CHKD	-	MD	MD	MD	MD	MD	MD	MD	MD	

2-5-78 BJ DG

datron ELECTRONICS LTD
1061/71/81
CENTRE P.C.B ASSY
400296 2-2

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
J2	604035	RIGHT ANGLED WAFER PCB GOLD	MOLEX	22-12-2041	12
J3	604035	" " " " "	"	"	—
J4	604035	" " " " "	"	"	—
J5	604035	" " " " "	"	"	—
J6	604035	" " " " "	"	"	—
	410093-4	PRINTED CIRCUIT BOARD			1
	510111	7/0.2 BROWN WIRE			120 mm
J1 & J7	604036	STRIP OF 10 AMP PINS	AMP	163740-8	2
	605053	12 WAY POLARISED SOCKET	MOLEX	22-01-2125	2
	605057	GOLD CRIMP PINS	MOLEX	4809-GL	7
	606004	PLASTIC POLARISING PEG	MOLEX	4161-1	4
	540002	22 S.W.G. TIN.CU WIRE			A/R
	590001	SLEEVE MAX CABLE Ø3.0	HELLERMANN ELECTRIC	H15 x 20mm BLK HELSYN	1

NOTES. CIRCUIT DIAGRAM . 430297.
CHECK PROC. . 460297.
CHECK LIST . 470297.
SEE SHEET 2 FOR LATEST ISSUE

ISS	C	D	1	2
ECO	-	-	RELEASED	867
DATE	-	24-8-78	29-9-78	11-6-79
CHKD	-	MD		MD

DATE 28-4-78
DRAWN BJ
CHECKED DG
APPROVED
DATE

datron ELECTRONICS LTD
TITLE 1061/71/81
L.H. PCB ASSEMBLY
DRAWING NUMBER 400297 2-2

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
R1	000334	330K 5% 1/4W CARBON	MULLARD	CR25	2
R2	000334	" " " "	"	"	-
	410094-4,A	P.C.B.			1
	540002	22SWG TIN. CU. WIRE.			A/R
J2	574270/C	24 WAY RIBBON CABLE ASSY	DATRON		1
J1 & J3	604036	STRIP OF 10 AMP PINS	AMP	163740-8	4
	590001	SLEEVE MAX CABLE Ø 3.0	HELLERMANN ELECTRIC	H15x20mm BLK. HELSYN	2
	630099	25mm MASKING TAPE.	3M	SCOTCH N.230	A/R

NOTES CIRCUIT DIAGRAM - 430298
CHECK PROC. - 460298
CHECK LIST - 470298
SEE SHEET 2 FOR LATEST ISSUE

ISS	C	D	1st	2	3	4	5	6
E.C.O.	-	-	RELEASED	ECO850	867/004	943	1217	1474
DATE	-	24-8-78	29-9-78	4-5-79	11-6-79	10.9.79	18.8.81	3.6.83
CHK.	-	MD		MD	MD	MD	MD	

DATE 28-4-78
DRAWN B.J.
CHECKED
APPD.

datron ELECTRONICS LTD
1061/71/81
R.H. P.C.B. ASSEMBLY
DRAWING NUMBER 400298
SHEET 2 OF 2

I.W. 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R1	000333	33K 5% ¼W CARBON	MULLARD	CR25	2
R2	000824	820K 5% ¼W CARBON	MULLARD	CR25	1
R3	000223	22K 5% ¼W CARBON	MULLARD	CR25	2
R4	000222	2K2 5% ¼W CARBON	MULLARD	CR25	3
R5	000104	100K 5% ¼W CARBON	MULLARD	CR25	3
R6	000104	100K 5% ¼W CARBON	MULLARD	CR25	–
R7	000104	100K 5% ¼W CARBON	MULLARD	CR25	–
R8	000102	1K 5% ¼W CARBON	MULLARD	CR25	2
R9	000105	1M 5% ¼W CARBON	MULLARD	CR25	3
R10	000105	1M 5% ¼W CARBON	MULLARD	CR25	–
R11	000105	1M 5% ¼W CARBON	MULLARD	CR25	–
R12	000103	10K 5% ¼W CARBON	MULLARD	CR25	5
R13	000103	10K 5% ¼W CARBON	MULLARD	CR25	–
R14	000103	10K 5% ¼W CARBON	MULLARD	CR25	–
R15	000222	2K2 5% ¼W CARBON	MULLARD	CR25	–
R16	000223	22K 5% ¼W CARBON	MULLARD	CR25	–
R17	000103	10K 5% ¼W CARBON	MULLARD	CR25	–
R18	015112	51K1 1% ½W M F	WELWYN	4033C	1
R19	070123	900R 0.1% 5 ppm/°c W W	MANN (KELVIN)	MX 125-B (EP-01)	1
R20	070124	90R 0.1% 5 ppm/°c W W	MANN	MX 125-B	1
R21	070125	9R 0.1% 5 ppm/°c W W	MANN	AX 175 BT	1
R22	070126	1R 0.1% 5 ppm/°c W W	MANN	LR 500 BU	1
R23	000222	2K2 5% ¼W CARBON	MULLARD	CR25	–

NOTES

SEE SHEET 2 FOR LATEST ISSUE

	9	10	11
	12/7	1257	1529
	18 8 81	24 11 81	6 5 83
	MD	MD	MD

	A	B	C	1	2	3	4	5	6	7	8
					972,987		1048	1081			
	17 JULY 79	21 AUG 79	24 9 79	24 9 79	25 10 79		16 1 80	14 4 80			
	MD	MD	MD	MD	MD	MD	MD	MD	MD	MD	

7 JULY 79 — W.G. SMITH

datron — CURRENT P.C.B

400304 — 2 — 7

DESIGNATOR		DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R24		000102	1K 5% ¼W CARBON	MULLARD	CR25	–
R25		070127	0R1 0.1% 5ppm/°c W W	MANN	LR 500 BU	1
R26		000333	33K 5% ¼W CARBON	MULLARD	CR25	–
R27	●	000221	220R 5% ¼W CARBON	MULLARD	CR25	2
R28	●	000221	220R 5% ¼W CARBON	MULLARD	CR25	–
R29		000103	10k 5% ¼W CARBON	MULLARD	CR25	–
C1		150020	10uF 25v TANT	UNION CARBIDE	KI0E25	2
C2		150020	10uF 25v TANT	UNION CARBIDE	KI0E25	–
C3		101103	10nf 250V CER DISC	ERIE	801	1
C4		150014	680nf 35V 20% TANT	UNION CARBIDE	KR6BE35	1
C5		150016	1uf 35V TANT	UNION CARBIDE	KIR0E35	2
C6		150016	1uf 35V TANT	UNION CARBIDE	KIR0E35	–
C7	#	102471	470pf 10% 500V CER DISC	ITT	CD10	1
	+	102331	330pf 10% 500V CER DISC	ITT	CD10	
C8		102101	100pF 10% 500v CER DISC	ITT	CD10	1
C9		110013	100nF 20% POLYESTER	MULLARD	C280AEP100K	1

NOTES. # VALUE CORRECT IF R19 MANUFACTURED BY MANN - TYPE MX 125-B
+ VALUE CORRECT IF R19 MANUFACTURED BY KELVIN - TYPE EP-01
● R27 + 28 NOT USED WITH RL1, RL2 ALTERNATIVE.
SEE SHEET 2 FOR LATEST ISSUE

IBS											
ECO											
DATE											
CHKD											

DATE 17 JULY 79 — datron ELECTRONICS LTD
DRAWN W.G. SMITH — CHECKED — APPROVED — DATE
TITLE CURRENT P.C.B.
DRAWING NUMBER 400304 — SHEET 3 OF 7

J.W. 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D1	200001	75mA. 75v G.P. Si DIODE.	FAIRCHILD.	IN4148.	5
D2	200008	200mA. 125v. L.L. Si DIODE.	FAIRCHILD.	IN458A.	3
D3	200008	200mA. 125V. L.L. Si DIODE.	FAIRCHILD.	IN458A.	–
D4	200008	200mA. 125V. L.L. Si DIODE.	FAIRCHILD.	IN458A.	–
D5	200001	75mA. 75v. G.P. Si DIODE.	FAIRCHILD.	IN4148	–
D6	200001	75mA. 75v. G.P. Si DIODE.	FAIRCHILD.	IN4148	–
D7	200001	75mA. 75V. G.P. Si DIODE.	FAIRCHILD.	IN4148	–
D8	200001	75mA. 75V. G.P. Si DIODE.	FAIRCHILD.	IN4148	–
D9		NOT USED			
D10		NOT USED			
D11	220020	FET DIODE 100pA I_R.	TELEDYNE.	PAD100	2
D12	220020	FET DIODE 100pA I_R.	TELEDYNE.	PAD100	–
D13	200022	3A. 400V. G.P. Si DIODE.	MOTOROLA	BY252	4
D14	200022	3A. 400V. G.P. Si DIODE.	MOTOROLA	BY252	–
D15	200022	3A. 400V. G.P. Si DIODE.	MOTOROLA	BY252	–
D16	200022	3A. 400V. G.P. Si DIODE.	MOTOROLA	BY252	–

NOTES

SEE SHEET 2 FOR LATEST ISSUE

ISS	ECO	DATE	CHKD

DATE 17 JULY 79
DRAWN W.G. SMITH
CHECKED
APPROVED
DATE

datron ELECTRONICS LTD

TITLE CURRENT P.C.B

DRAWING NUMBER 400304

SHEET 4 OF 7

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
Q1		NOT USED			
Q2	250001	Si P.N.P. TRANSISTOR.	NATIONAL.	BC214/TO18	2
Q3		NOT USED			
Q4	250001	Si P.N.P. TRANSISTOR.	NATIONAL.	BC214/TO18	–
Q5		NOT USED			
Q6		NOT USED			
Q7	230003	N-CHAN J FET	TELEDYNE.	U1899 JF	1
Q8	230035	N-CHAN J FET.	TELEDYNE.	U1897 JF	2
Q9	230002	N-CHAN J FET.	TELEDYNE.	U1994 JF	1
Q10	230035	N-CHAN J FET.	TELEDYNE.	U1897 JF	–
M1	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076 BCP	2
M2	280011	DUAL D FLIP FLOP	MOTOROLA	MC14013 BCP	1
M3	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076 BCP	–
M4	270059	7× DARLINGTON DRIVER	SPRAGUE/EXAR	ULN2004A/XR2204CP	1
M5	260027	714 OP AMP	FAIRCHILD	uA 714 HC.	1

NOTES

SEE SHEET 2 FOR LATEST ISSUE

DATE 17 JULY 79
DRAWN W.G. SMITH
CHECKED
APPROVED
DATE

datron ELECTRONICS LTD

TITLE CURRENT P.C.B

DRAWING NUMBER 400304

SHEET 5 OF 7

DESIGNATOR		DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURERS PART No	No. OFF Per Assy.
ANI			NOT USED			
RL1	*	330017	RELAY REED LOW THERMAL	OMRON	G2E 182PH	2
RL2	*	330017	RELAY REED LOW THERMAL	OMRON	G2E 182PH	-
RL3		330015	RELAY 4P2W 7v HOLD-IN	P/B	SEE DRAWING	1
RL4		330016	RELAY 2P2W 7v HOLD-IN	P/B	SEE DRAWING	1
		400379/1	MOLEX TERMINAL/WIRE ASSY	DATRON		1
		410104-5A	PRINTED CIRCUIT BOARD			1
		459112	RELAY BRACKET			2
		540008	7/2 PTFE INSULATED WHITE WIRE		TYPE C	807mm TOTAL
		620007	TEST POINT TERMINAL	MICROVAR	C30	5
		590001	SLEEVE BLACK	HELLERMAN ELECTRIC	HIS x 20mm HELYSYN	16
		605056	CRIMP TERMINAL	MOLEX	4809-TL	3
J1		571095/C	16 WAY AP/3M RIBBON CABLE	DATRON		1
		605060	14 WAY D.I.L. SOCKET	ASTRALUX OR JERMYN	ICL-143-SST	1
		605061	16 WAY D.I.L. SOCKET	ASTRALUX OR JERMYN	ICL-163-SGT	3
		605051	4 WAY POLARISED SOCKET	MOLEX	(22-01-2045) 6471-4-1	1
J3		605052	8 WAY POLARISED SOCKET	MOLEX	(22-01-2085) 6471-8-1	1
J2		605053	12 WAY POLARISED SOCKET	MOLEX	(22-0-2125) 6471-12-1	1
		605057	CRIMP TERMINAL	MOLEX	4809-GL	8

NOTES * ALTERNATIVE 330014 OR 330013

17 JULY 79 | datron ELECTRONICS LTD
W.G.SMITH | CURRENT P.C.B
DRAWING NUMBER 400304 | SHEET 6 OF 7

DESIGNATOR		DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURERS PART No	No. OFF Per Assy.
		611004	SCREW M3x6 POSI PAN			2
		613005	WASHER M3 INT/SHAKEPROOF			2
		615002	NUT M3 FULL HEX			2
		615005	NUT 3-48 UNC FULL HEX			2
		613014	WASHER M2.5 INT SHAKEPROOF			2
		617010	NYLATCH PLUNGER	C.J. FOX & SON	HN3P-32-4-1	4
		617011	NYLATCH GROMMET	C.J. FOX & SON	HN3G	4
		620003	SOLDER P.C.B TERMINAL LUG	HARWIN	H2105A	13
		630024	STANDARD STEATITE INSUL. BEAD	PARK ROYAL PORCELAIN CO.	TYPE No 2 (16SWG)	24
		920082	FUSE HOLDER 20mm P/MTG	BELLING & LEE	L2002	1
		920071	FUSE 1-6A QUICK ACTING 20mm	BESWICK	3501-1-6-F-250v	1
		590004	SLEEVE - PTFE	HELLERMANN ELECTRIC	FE10	A/R
		590055	SLEEVE Ø1.0 SIL. RUBBER	" "	HIS CONT. BLACK	30mm

NOTES

SEE SHEET 2 FOR LATEST ISSUE

ISS						
ECO						
DATE						
CHKD						

DATE 17 JULY 79 | datron ELECTRONICS LTD
DRAWN W.G.SMITH | TITLE CURRENT P.C.B
RELEASED
APPROVED
DATE
DRAWING NUMBER 400304 | SHEET 7 OF 7

J.W. 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R1	000123	12K 5% 1/4W CARBON	MULLARD	CR25	3
R2	000332	3K3 " " "	"	"	6
R3	000332	3K3 " " "	"	"	–
R4	000103	10K " " "	"	"	6
R5	000123	12K " " "	"	"	–
R6	000222	2K2 " " "	"	"	2
R7	000222	2K2 " " "	"	"	–
R8	000123	12K " " "	"	"	–
R9	000333	33K " " "	"	"	1
R10	000562	5K6 " " "	"	"	1
R11	000103	10K " " "	"	"	–
R12	000105	1M " " "	"	"	1
R13	000332	3k3 " " "	"	"	–
R14	000103	10k " " "	"	"	–
R15	000332	3k3 " " "	"	"	–
R16	000332	3k3 " " "	"	"	–
R17	000681	680R " " "	"	"	2
R18	000681	680R " " "	"	"	–
R19	000332	3k3 " " "	"	"	–
R20	000103	10k " " "	"	"	–
R21	000103	10K " " "	"	"	–
R22	000103	10k " " "	"	"	–

ES

	1	2	3 C	4	5	6	7	8	9
	RELEASED	902/887	972	981	1021	1085	1166	1199	1329
DATE	26-3-79	15-6-79	25-10-79	6-11-79	2-1-80	22.4.80	24.9.80	24.2.81	8-7-82
	MD	MD	MD	MD	MD	MD	MD	MD	MD

5-3-79
B.J.
MD

datron ELECTRONICS LTD

REAR INPUT / RATIO ASSY.

DRAWING NUMBER 400307. SHEET 3 OF 8

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
C1	150022	2µ2F 20% 35V DIP. TANT	UNION CARBIDE	K2R2E35	2
C2	150022	2µ2F 20% 35v DIP. TANT	UNION CARBIDE	K2R2E35	–
C3	150020	10µF 20% 25v DIP. TANT	UNION CARBIDE	K10E25	2
C4	150020	10µF 20% 25V DIP. TANT	UNION CARBIDE	K10E25	–
C5	102101	100pF 10% 500V CER DISC	ITT	CD10	1
C6	110013	100nF 20% 250v POLYESTER	MULLARD	C280AEP100K	1
C7	150014	680nF 20% 35V DIP. TANT	UNION CARBIDE	KR68E35	2
C8	150014	680nF 20% 35V DIP. TANT	UNION CARBIDE	KR68E35	–

NOTES

SEE SHEET 2 FOR LATEST ISSUE

ISS			
E.C.O.			
DATE			
CHKD			

DATE 23.9.80
DRAWN JL
CHECKED
APPROVED
DATE

datron ELECTRONICS LTD

TITLE REAR INPUT/RATIO 1061/1071

DRAWING NUMBER 400307 SHEET 4 OF 8

W 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
D1	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	8
D2	200001	" " " " "	"	"	—
D3	200001	" " " " "	"	"	—
D4	200001	" " " " "	"	"	—
D5	200001	" " " " "	"	"	—
D6	200001	" " " " "	"	"	—
D7	200001	" " " " "	"	"	—
D8	200001	" " " " "	"	"	—
Q1	240001	Si NPN TRANSISTOR	NATIONAL	BC184/TO18	4
Q2	240001	" " "	"	"	—
Q3	250001	Si PNP TRANSISTOR	NATIONAL	BC214/TO18	2
Q4	250001	" " "	"	"	—
Q5	240001	Si NPN TRANSISTOR	NATIONAL	BC184/TO18	—
Q6	240001	" " "	"	"	—
M1	280011	DUAL D FLIP-FLOP	MOTOROLA	MC14018BCP	1
J1	604036	CON PIN STRIP OF 10 HORIZ TYPE	AMP	103740-8	2
J2, J3	605052	8 WAY POLARISED SOCKET	MOLEX	(22-01-2085) 6471-8-1	2

NOTES

SEE SHEET 5

		DATE	5-3-79	datron ELECTRONICS LTD
		DRAWN	BJ	TITLE REAR INPUT / RATIO ASSY
		CHECKED	MD	
		APPROVED		
		DATE		DRAWING NUMBER 400307 SHEET 5 OF 8

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
J4 & J6	604033	FLAT WAFER PIN (4WAY GOLD)	MOLEX	22-27-2041 / GOLD	3
J5	605051	4 WAY POLARISED SOCKET	MOLEX	22-01-2045	1
	400379/4	WIRE/TERMINAL ASSY.	HOLDEN CORDS		6
	400379/5	" " "	" "		4
RL1 & RL4	330018	RELAY 2P2W 7v HOLD-IN	P & B	SEE DRAWING	2
RL2 & RL3	330019	RELAY 4P2W 7v HOLD-IN	P & B	SEE DRAWING	2
	410106-5	COMPONENT PCB.			1
	410132-4	RELAY PCB.			1
	450185-1	SOCKET PLATE			1
	450241-1	RELAY BRACKET			1
	540002	22SWG TINNED COPPER WIRE			A/R
	540008	7/2 PTFE INS. WHITE WIRE			140mm

NOTES.

SEE SHEET 5 FOR LATEST ISSUE

		DATE	5-3-79	datron ELECTRONICS LTD
		DRAWN	BJ	TITLE REAR INPUT / RATIO ASSY
		CHECKED	MD	
		APPROVED		
		DATE		DRAWING NUMBER 400307 SHEET 6 OF 8

NO.				
E.C.O.				
DATE				
CHKD				

AW. 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
	590001	SLEEVE MAX CABLE ϕ 3·0mm	HELLERMANN ELECTRIC	H15x20mm BLK HELSYN	28
	590004	SLEEVE - PTFE	" "	FE10	A/R
	602007	RELAY SOCKET 2 POLE PCB MOUNT	POTTER & BRUMFIELD	27E212	1
	602009	RELAY SOCKET 4 POLE PCB MOUNT	" "	27E213	1
J10, J11	604008	7 WAY PLUG PANEL MOUNT	PYE CONNECTORS	M7P	2
	605009	7 WAY SOCKET	PYE CONNECTOR	M7S	2
	605060	14 WAY DIL SOCKET	ASTRALUX OR JERMYN	ICN-246-SAT OR A23-2023Y	1
	605057	CRIMP TERMINAL	MOLEX	4800-GL	2
	606001	LOCKING HOOD	PYE CONNECTORS	MHN	2
	606002	NUT	PYE CONNECTORS	MN	2
	606003	WASHER	" "	MLW	2
	611004	SCREW M3x6mm STEEL POZI-PAN. ZINC PLATED	GKN		7
	611007	SCREW M3x6mm STEEL POZI-CSK. ZINC PLATED	GKN		7
	611016	" M3x8mm " " PAN " " "			4
	612020	STANDOFF NYLON M3x19 TRANSIPILLAR.	W.K. ELECTRONICS	TPI/G·S/19/M3/I/I	5

NOTES

SEE SHEET 3 FOR LATEST ISSUE

ISS																			
ECO																			
DATE																			
CHKD																			

DATE 5-3-79 | DRAWN B.N | CHECKED MD | APPROVED | DATE

datron ELECTRONICS LTD

TITLE REAR INPUT / RATIO ASSY

DRAWING NUMBER 400307 | SHEET 7 OF 8

A 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
–	613005	WASHER M3 INT/SHAKEPROOF ST.	GKN DISTRIBUTORS	ZINC PLATED	13
	615001	NUT BBA FULL HEX STEEL		ZINC PLATED	2
	615002	" M3 " " "		" "	2
	630005	CLIP FOR P&B R10 2 POLE RELAY	POTTER & BRUMFIELD	20C249	1
	630028	CLIP FOR P&B R10 4 POLE RELAY	" "	20C250	1
	700069	DPDT SLIDE SWITCH	WAYCOM	S-5022CD03-0 + 3/4" TRIGGER & STYLE 2/C PCB MOUNT, CONTACTS.	1

NOTES

SEE SHEET 3 FOR LATEST ISSUE

ISS																			
ECO																			
DATE																			
CHKD																			

DATE 5-3-79 | DRAWN B.N | CHECKED MD | APPROVED | DATE

datron ELECTRONICS LTD

TITLE REAR INPUT / RATIO ASSY

DRAWING NUMBER 400307 | SHEET 8 OF 8

J.W 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. Per Assy
R1	000102	1KΩ 5% ¼W CARBON	MULLARD	CR25	1
R2	000682	6K8 " " "			1
R3	090001	PTC THERMISTOR	MULLARD	VA8650	2
R4	090001	" "			"
R5	066102	1KΩ 3/8 SQ VERTICAL POT	BECKMAN	72XW	1
R6	000104	100K 5% ¼W CARBON	MULLARD	CR25	1
R7	070128	21K5Ω 0·1% WIRE WOUND	MANN	MX 125	1
R8	070066	10K 0·1% WIRE WOUND	MANN	MX 125	1
C1	101103	0·01μF 250V CER D SC	ITT	CD10K3N00JS 335003C	2
C2	101103	0·01μF			"
C3 *	102330	33pF 500V CER D SC	ITT	CD10PG330JS 335003C	1 4
C4	102330	33pF		"	1
C5	110013	0·1μF 20% 250V POLYESTER	MULLARD	C280AE/P100K	1

NOTES. * ONLY REQUIRED WHEN M1 ALTERNATIVE (TYPE 101) IS USED
CCT DIAG. 430308
CHECK PROC 480308. CHECK LIST 470308
SEE SHEET 2 FOR LATEST ISSUE

ISS	1	2	3
ECO	RELEASED	907	945
DATE	27 12 78	20 6 79	10 9 79
CHKD	MD	MD	BJ

DATE 22-11-78
DRAWN BJ
CHECKED MD
APPROVED
DATE

datron ELECTRONICS LTD
TITLE ANALOGUE OUTPUT PCB ASSY
DRAWING NUMBER 400308
SHEET 2 OF 4

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. Per Assy
D1	213001	10V 5W ZENER	MOTOROLA	"934"	2
D2	213001	10V " "			"
M1 *	260002	OP AMP	FAIRCHILD	μA741C	1 4
M2	260026	OP AMP	NATIONAL	LM212H	1
	400379/4	WIRE / TERMINAL ASSY	HOLDEN CORDS.		5
	410107-3	P.C.B.			1
	450186-1	SOCKET PLATE.			1
	510000	7/·2 PVC INSUL (BLACK) WIRE			50mm
	510222	7/·2 PVC INSUL (RED) WIRE			50mm
	590001	SLEEVE MAX CABLE ϕ 3·0	HELLERMANN ELECTRIC	H15 x 20mm BLACK HELSW	2
	605007	5 WAY SOCKET	PYE CONNECTORS	M5S	1
V1	605052	8 WAY POLARISED SOCKET	MOLEX	(22-01-2085) 6471-8-1	1

NOTES. * M1 ALTERNATIVE 260025 (LM101).

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CHKD													

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TITLE ANALOGUE OUTPUT PCB ASSY
DRAWING NUMBER 400308
SHEET 3 OF 4

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
	606001	LOCKING HOOD	PYE CONNECTORS	MHN	1
	606002	NUT	" "	MN	1
	606003	WASHER	" "	MLW	1
	612019	STANDOFF LOCKING TYPE 7/8" NYLON: RICHCO		LCBS-14R	4
	620003	SOLDER PCB TERMINAL LUG	HARWIN	H2105A	2
	630024	INSULATING BEADS, STEATITE.		(TYPE N°2)	8.
	620007	TEST POINT TERMINAL	MICROVAR	C30	5
*	604006	5 WAY PLUG	PYE CONNECTORS.	M5P.	1.
*	611016	SCREW POSI-PAN M3x8			4
*	613005	SHAKEPROOF WASHER M3			4

NOTES * FITTED AT FINAL ASSEMBLY.

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ISS													
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CHKD													

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DATE

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TITLE ANALOGUE OUTPUT PCB ASSY.

DRAWING NUMBER 400308

SHEET 4 OF 4

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
R1	000104	100K 5% ¼W CARBON	MULLARD	CR25	15
R2	000101	100R " " "	"	"	4
R3	000101	100R " " "	"	"	-
R4	000105	1M 10% " "	"	"	11
R5	000822	8K2 " " "	[illegible]	[illegible]	3
R6	011302	13KO 1% 50ppm MF	HOLCO	[illegible]	2
R7	011302	13KO " " "	"	"	-
R8	000101	100R 5% ¼W CARBON	MULLARD	[illegible]	-
R9		NOT USED			
R10	000101	100R 5% ¼W CARBON	"	"	-
R11		FSV		[illegible]	-
R12	000472	4k7 " " "	"	"	12
R13	000104	100k " " "	"	"	-
R14	000472	4k7 " " "	"	"	-
R15		FSV		[illegible]	
R16	050038	6K34 1% 15ppm MF	ACI	EE-0-100-C4	2
R17	050037	4K75 1% 15ppm MF	ACI	EE-0-100-C4	2
R18		PART OF KIT WITH D59			
R19		PART OF KIT WITH D60			
R20	063203	20K POT CERMET	BECKMAN	[illegible]	1
R21	000104	100K 5% ¼W CARBON	MULLARD	[illegible]	-
R22	000102	1K " " "	"	"	8
R23	063504	500K POT CERMET	[illegible]	[illegible]	2

NOTES CIRCUIT DIAGRAM = 450328
CHECK PROCEDURE = 460828
CHECK LIST = 470328
SEE SHEET 2 FOR LATEST ISSUE

ISS	10	11	12	13	14	15	16
ECO	[illegible]	1000	[illegible]	1098. 1107	1128 1050 1117	1144 1160 1114 1182 1124 1188 1191 1190 1193 1192	
DATE	29.10.79	12.8.80	27.2.80	15.4.80	2.6.80	23.9.80	
CHKD	MD	MD	MD	[illegible]	[illegible]	[illegible]	

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TITLE 1061 ANALOGUE PCB ASSEMBLY
DRAWING NUMBER 400328
SHEET 2 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
R24	000824	820K 5% ¼W CARBON	MULLARD	[illegible]	2
R25	000122	1K2 5% ¼W CARBON	MULLARD	[illegible]	-
R26	000185	1M8 10% ¼W CARBON	"	"	-
R27	000275	2M7 5% ¼W CARBON	"	"	3
R28	000222	2K2 5% ¼W CARBON	"	"	7
R29	000100	10R "	"	"	8
R30	000100	10R "	"	"	-
R31	000473	47K "	"	"	3
R32	000393	39K "	"	"	1
R33	000473	47K "	"	"	-
R34	000102	1K "	"	"	-
R35	000104	100k " " "	"	"	-
R36	000104	100k " " "	"	"	-
R37	000562	5K6 " " "	"	"	3
R38	050034	825R 1% 15ppm MF	ACI	EE0-100 C4	2
R39	050053	64R2 1% 1/10W 15ppm MF	"	EE-0-10-64R2-F-C4	2
R40	050053	64R2 1% 1/10W 15ppm MF	"	EE-0-10-64R2-F-C4	-
R41		NOT USED			-
R42	050031	196R 1% 15ppm MF	ACI	EE 0-100 C4	2
R43	019768	97R6 1% 50ppm MF	HOLCO	H8C	2
R44		NOT USED			
R45		NOT USED			
R46	000182	1K8 5% ¼W CARBON	MULLARD	CR25	1

NOTES.

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TITLE 1061 ANALOGUE PCB ASSEMBLY
DRAWING NUMBER 400328
SHEET 3 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R47	000100	10R 5% 1/4W CARBON	MULLARD	CR25	-
R48	000103	10K " " "	"	"	12
R49	000103	10K " " "	"	"	-
R50	000103	10K " " "	"	"	-
R51	000100	10R " " "	"	"	-
R52	000334	330K " " "	"	"	4
R53	000334	330K " " "	"	"	-
R54	000334	330K " " "	"	"	-
R55	000334	330K " " "	"	"	-
R56	000104	100K " " "	"	"	-
R57	000221	220R " " "	"	"	3
R58	000821	820R " " "	"	"	1
R59	000563	56K " " "	"	"	6
R60	000222	2K2 " " "	"	"	-
R61	000105	1MO 10% 1/4W CARBON	"	"	-
R62	000105	1MO " " "	"	"	-
R63	000221	220R 5% 1/4W CARBON	"	"	-
R64		NOT USED			
R65	000102	1K 5% 1/4W CARBON	"	"	-
R66	000183	18K " " "	"	"	1
R67	000562	5K6 " " "	"	"	-
R68	018060	806R 1% 50ppm MF	HOLCO	H8	2
R69	000114	110K 5% 1/4W CARBON	MULLARD	CR25	2

NOTES.

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TITLE 1061 ANALOGUE PCB ASSEMBLY.

DRAWING NUMBER 400328 — SHEET 4 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R70	018060	806R 1% 50ppm MF	HOLCO	H8	-
R71	070115	12K0 1% 10ppm WW	MANN	MX125	2
R72	000472	4K7 5% 1/4W CARBON	MULLARD	CR25	-
R73	000103	10K " " "	"	"	-
R74	000275	2M7 " " "	"	"	-
R75	000565	5M6 " " "	"	"	-
R76	000106	10M 10% 1/4W CARBON	"	"	1
R77	000226	22M " " "	"	"	2
R78	000473	47K 5% 1/4W CARBON	ALLEN BRADLEY	CB2261	1
R79	000392	3K9 " " "	MULLARD	CR25	-
R80	000104	100K " " "	"	"	2
R81	000472	4K7 " " "	"	"	-
R82	000472	4K7 " " "	"	"	-
R83	000472	4K7 " " "	"	"	-
R84	011378	13R7 1% 1/8W MF	"	"	-
R85		NOT USED	HOLCO	H8	1
R86	000472	4K7 5% 1/4W CARBON	MULLARD	CR25	-
R87	000114	110K " " "	"	"	-
R88		NOT USED			-
R89	050031	196R 1% ppm MF	ACI	EE 0·100-C4	-
R90	019768	97R6 1% 50ppm MF	HOLCO	H8C	-
R91		NOT USED			
R92		NOT USED			

NOTES.

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TITLE 1061 ANALOGUE PCB ASSEMBLY.

DRAWING NUMBER 400328 — SHEET 5 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
R93	000124	120K 5% ¼W CARBON	MULLARD	CR25	2
R94	000102	1K	"	"	—
R95	000106	10M 10%	"	"	—
R96	011473	147K 1% 50ppm MF	HOLCO	H8	1
R97		NOT USED			—
R98		NOT USED			—
R99	000472	4K7 5% ¼W CARBON	MULLARD	CR25	—
R100	000472	4K7	"	"	—
R101	000100	10R 5% ¼W CARBON	MULLARD	CR25	—
R102	000100	10R	"	"	—
R103	000271	270R	"	"	4
R104	000151	150R	"	"	1
R105	000271	270R	"	"	—
R106	000222	2K2	"	"	—
R107	000222	2K2	"	"	—
R108	090038	10KBII ATTEN SET	MAIN		1 SET
R109	090038	10KBII	"		—
R110	090038	9K	"		—
R111	090038	1K	"		—
R112	000682	6K8 5% ¼W CARBON	MULLARD	CR25	1
R113	000105	1MO 10% ¼W CARBON	"	"	—
R114	042214	2M21% 100ppm CERMET FILM	ALLEN BRADLEY	TYPE CC	1
R115	041004	1M 1% 100ppm CERMET FILM	"	"	1

NOTES

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TITLE 1061 ANALOGUE PCB ASSEMBLY

DRAWING NUMBER 400328 SHEET 6 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
R116	000105	1M 5% ¼W CARBON	MULLARD	CR25	—
R117	000105	1M	"	"	—
R118	000105	1M	"	"	—
R119	008012	27K 2W CARBON FILM	ERIE		2
R120	008012	27K	"		—
R121	008011	22K	"		2
R122	008011	22K	"		—
R123	000225	2M2 5% ¼W CARBON	MULLARD	CR25	2
R124	011213	121K 1% 50ppm MF	HOLCO	H8	2
R125	000225	2M2 5% ¼W CARBON	MULLARD	CR25	—
R126	013651	3K65 1% 50 ppm MF	HOLCO	H8	1
R127	041005	10MO 1% ½W 100ppm CF	ALLEN BRADLEY	CC	—
R128	042215	22M1 1% 100 ppm CF	ALLEN-BRADLEY	CC	2
R129	042215	22M1 " 100 ppm	"	"	—
R130	000362	3K6 5% ¼W CARBON	MULLARD	CR25	2
R131	000362	3K6 " " "	"	"	—
R132	000105	1M " " "	"	"	—
R133	000105	1M " " "	"	"	—
R134	000394	390K " " "	"	"	1
R135	000275	2M7 " " "	"	"	—
R136	000395	3M9 " " "	"	"	1
R137	000223	22K " " "	"	"	3
R138	000125	1M2 " " "	"	"	1

NOTES.

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DRAWING NUMBER 400328 SHEET 7 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R139	000336	33M 10% ¼W CARBON	ALLEN-BRADLEY	CB3361	1
R140	090049	18M MATCHED PAIR	HOLCO		1 PAIR
R141	090049	18M " "	"		
R142	000272	2K7 5% ¼W CARBON	MULLARD	CR25	1
R143	090039	3M3, 10MΩ INPUT ATTEN. SET	MANN		1 SET.
R144	000100	10R 5% ¼W CARBON	MULLARD	CR25	–
R145	000100	10R " " "	"	"	–
R146	000222	2K2 " " "	"	"	–
R147	000752	7K5 " " "	"	"	1
R148	090039	100K, 10MΩ INPUT ATTEN. SET.	MANN		–
R149	090039	3M3, " " " "	"		–
R150	063204	200K POT CERMET	BECKMAN	72P	1
R151	063205	2M POT CERMET	"	"	1
R152	011000	100R 1% MF (DO NOT SOLDER)	HOLCO	H8C	1
R153	011822	18K2 1% ⅛W MF	HOLCO	H8	1
R154	000103	10K 5% ¼W CARBON	MULLARD	CR25	–
R155	000221	220R " " "	"	"	–
R156	090039	3M3, 10MΩ INPUT ATTEN. SET	MANN		–
R157	000103	10K 5% ¼W CARBON	MULLARD	CR25	–
R158	000332	3K3 " " "	"	"	1
R159	063103	10K POT CERMET	BECKMAN	72P	1
R160	063504	500K " "	"	"	–
R161	000104	100K 5% ¼W CARBON	MULLARD	CR25	–

NOTES.

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DRAWING NUMBER 400328

SHEET 8 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R162	000392	3K9 5% ¼W CARBON	MULLARD	CR25	–
R163	000107	100M 10% ¼W CARBON	ALLEN-BRADLEY	CB1071	1
R164	000104	100K 5% ¼W CARBON	MULLARD	CR25	–
R165	000104	100K " " "	"	"	–
R166	000563	56K 5% " "	"	"	–
R167	000562	5K6 " " "	"	"	–
R168	000563	56K " " "	"	"	–
R169	000563	56K " " "	"	"	–
R170	000564	560K " " "	"	"	2
R171	000564	560K " " "	"	"	–
R172	000335	3M3 10% ¼W CARBON	MULLARD	CR25	1
R173	000680	68R 5% " "	"	"	2
R174	000152	1K5 " " "	"	"	2
R175	000822	8K2 " " "	"	"	–
R176	000680	68R " " "	"	"	–
R177	000152	1K5 " " "	"	"	–
R178	000822	8K2 " " "	"	"	–
R179	440067	PART OF KIT	DATRON.		1
R180	440067	" " "	"		–
R181	440067	" " "	"		–
R182	000472	4K7 5% ¼W CARBON	MULLARD	CR25	–
R183	000472	4K7 5% " "	"	"	–
R184	000270	27R " " "	"	"	2

NOTES.

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DRAWING NUMBER 400328

SHEET 9 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R185	000270	27R 5% ¼W CARBON	MULLARD	[illegible]	–
R186	000104	100K			–
R187	000222	2K2			–
R188	000103	10K			–
R189	000103	10K			–
R190	000561	560R			–
R191	000561	560R			–
R192	000155	1M5			–
R193	000104	100K			–
R194	011213	121K 1% ⅛W M.F.	[illegible]	[illegible]	–
R195	015112	5IK1			1
R196	000223	22K 5% ¼W CARBON	MULLARD	[illegible]	–
R197	000222	2K2			–
R198	011002	10K 1% ⅛W MF	HOLCO	[illegible]	1
R199	008007	10R 5% 0·2W CARBON	MULLARD	[illegible]	2
R200	008007	10R			–
R201	000824	820R 5% ¼W CARBON	MULLARD	[illegible]	–
R202	000103	10K			–
R203	000104	100K			–
R204	000105	1M			–
R205	000105	1M			–
R206	000563	56K			–
R207	000103	10K			–

NOTES

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TITLE 1061 ANALOGUE PCB ASSEMBLY.

DRAWING NUMBER 400328 SHEET 10 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R208	000333	33K 5% ¼W CARBON	MULLARD	[illegible]	1
R209	000563	56K			–
R210	000103	10K			–
R211	050038	6K34 1% 5ppm MF	ACI	[illegible]	–
R212	050034	825R 1% 15ppm MF	ACI	[illegible]	–
R213	050037	4K75 1% 15ppm MF	ACI	[illegible]	–
R214	070115	12KO 1% 10ppm WW	MANN	[illegible]	–
R215	000271	270R 5% ¼W CARBON	MULLARD	[illegible]	–
R216	000271	270R 5% ¼W CARBON	MULLARD	[illegible]	–
R217		NOT USED			–
R218	090063	P.T.C. THERMISTOR	TEIAG	[illegible]	1
R219	000224	220K 5% ¼W CARBON	MULLARD	[illegible]	3
R220	000224	220K			–
R221	000224	220K			–
R222	000224	220K			–
R223					
R224	012212	22K1 1% ⅛W 50ppm MF	HOLCO	[illegible]	2
R225	012212	22K1 1% ⅛W 50ppm MF	HOLCO	[illegible]	–
R226	000223	22K 5% ¼W CARBON	MULLARD	[illegible]	–
R227	000471	470R 5% ¼W CARBON	MULLARD	[illegible]	1
R228		NOT USED			–
R229		NOT USED			–
R230		NOT USED			–

NOTES.

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TITLE 1061 ANALOGUE PCB ASSEMBLY.

DRAWING NUMBER 400328 SHEET 11 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R231	000104	100K 5% ¼W CARBON	MULLARD	CR25	–
R232		NOT USED			–
R233	000102	1K 5% ¼W CARBON	MULLARD	CR25	–
R234	000102	1K 5% ¼W CARBON	MULLARD	CR25	–
R235	000102	1K 5% ¼W CARBON	MULLARD	CR25	–
R236	016192	61k9 1% ⅛W 50ppm MF	HOLCO	H8C	2
R237	016192	61k9 1% ⅛W 50ppm MF	HOLCO	H8C	–
R238	000104	100K 5% ¼W CARBON	MULLARD	CR25	–
R239	000102	1K 5% ¼W CARBON	MULLARD	CR25	–
R240	000472	4k7 5% ¼W CARBON	MULLARD	CR25	–
R241	000331	330R 5% ¼W CARBON	MULLARD	CR25	1
R242	000103	10K 5% ⅓W CARBON	MULLARD	CR25	–
AN1	090050	3K3 SIL NETWORK	BECKMAN	764-1-R3·3K	1
AN2	090042	R-2R LADDER NETWORK	ERIE		1
C1	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	12
C2	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	–
C3	104017	0·5pF 500V CER DISC	ERIE	831	1

NOTES.

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TITLE: 1061 ANALOGUE PCB ASSY.

DRAWING NUMBER: 400328

SHEET: 12 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
C4	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	–
C5	102471	470pF 500V CER DISC	ERIE	801	3
C6	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	–
C7	150020	10µF " " " "	" "	"	–
C8	102150	15pF 500V CER DISC	ERIE	801	1
C9	140016	0·47µF 400V POLYPROP	RIFA	PHE 412 HFK	1
C10	110015	0·015µF 63V POLYESTER	WIMA	FKS2	1
C11	120026	680pF 20% 100v POLYCARB	WIMA	FKC2 MIN	1
C12	120004	680nF 10% 63v POLYCARB	ASHCROFT	A2B6811B	1
C13	102101	100pF 500V CER DISC	ERIE	801	3
C14	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	–
C15	150020	10µF " " " "	" "	"	–
C16	102101	100pF 500V CER DISC	ERIE	801	–
C17	102100	10pF 500V CER DISC	ERIE	801	1
C18	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	–
C19	150020	10µF " " " "	" "	"	–
C20	102470	47pF 500V CER DISC	ERIE	801	4
C21	102470	47pF 500V CER DISC	ERIE	801	–
C22	102101	100pF 500V CER DISC	"	"	–
C23	101103	0·01µF 250V CER DISC	"	"	6
C24	101103	0·01µF 250V CER DISC	"	"	–
C25	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	–
C26	102471	470pF 500V CER DISC	ERIE	801	–

NOTES

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TITLE: 1061 ANALOGUE PCB ASSEMBLY.

DRAWING NUMBER: 400328

SHEET: 13 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	
C27	101103	0·01μF 250V CER DISC	ERIE	801	–
C28	102470	47pF 500V " "	"	"	–
C29	102410	4[illegible]pF 500V			–
C30		NOT USED			
C31	130073	1nF 5% 160V POLYSTYRENE	SUFLEX	HSC1000.5-10/160	1
C32	101103	0·01μF 250V CER DISC	ERIE	801	–
C33	110013	0·1μF 10% 250V POLYESTER	MULLARD	C280AE/P100K	3
C34	101103	0·01μF 250V CER DISC	ERIE	801	–
C35	120016	2n2F 20% 100V POLYCARB	WIMA	FKC2MIN	1
C36	102471	470pF 500V CER DISC	"	"	–
C37	110013	0·1μF 10% 250V POLYESTER	MULLARD	C280AE/P100K	–
C38	102102	1nF 10% 500V CER DISC	ITT	CD10	2
C39	440067	PART OF KIT	DATRON		–
C40	440067	" " "	"		–
C41	440067	" " "	"		–
C42	110013	0·1μF 10% POLYESTER	MULLARD	C280AE/P100K	–
C43	150020	10μF 20% 25V DIP TANT	UNION CARBIDE	KIDE25	–
C44	150020	10μF " " "	"	"	–
C45	180006	47μF 25V AL ELECT	MULLARD	016-16479	2
C46	180006	47μF 25V "	"	"	–
C47	180022	33μF 40V "	"	016-17539	2
C48	180022	33μF 40V "	"	"	–
C49	180024	10μF 63V AL ELECT	MULLARD	016-16479	1

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TITLE 1061 ANALOGUE PCB ASSEMBLY

DRAWING NUMBER 400328 SHEET 14 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	
C50	180024	10μF 63V AL ELECT	MULLARD	016-16479	–
C51	101103	0·01μF 250V CER DISC	ERIE	801	–
C52	110017	0·022μF 10% 250V POLYESTER	MULLARD	C280AE/P22K	1
C53	102332	3n3F 500V CER DISC	ERIE	801	2
C54	102332	3n3F " "	"	"	–
C55	440067	PART OF KIT	DATRON		–
C56	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	KIDE35	1
C57	130013	18pF ±1pF 160V POLYSTYRENE	SUFLEX	HS	1
C58	110027	3300pF 20% 100V POLYESTER	WIMA	FKS2-MIN	1
C59		NOT USED			–
C60	102222	2n2F 500V CER DISC	ERIE	801	1
C61		NOT USED			
C62		NOT USED			
C63		NOT USED			
C64	102102	1nF 10% 500V CER DISC	ITT	CD10	–
D1	200008	SI LOW LEAKAGE	FAIRCHILD	1N458A	25
D2	210056	C5V6 400mW ZENER	MULLARD	BZY88C	2
D3	200008	SI LOW LEAKAGE	FAIRCHILD	1N458A	–
D4	210056	C5V6 400mW ZENER	MULLARD	BZY88C	–
D5	200001	SI GEN. PURPOSE	FAIRCHILD	1N4148	19
D6	200008	SI LOW LEAKAGE	FAIRCHILD	1N458A	–

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TITLE 1061 ANALOGUE PCB ASSEMBLY

DRAWING NUMBER 400328 SHEET 15 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D7	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D8	210082	C8V2 400mW ZENER	MULLARD	BZY88C	1
D9	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D10	200008	" " "	"	"	–
D11	200008	" " "	"	"	–
D12	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D13	200001	" " "	"	"	–
D14	200001	" " "	"	"	–
D15	200001	" " "	"	"	–
D16	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D17	200008	" " "	"	"	–
D18	210068	C6V8 400mW ZENER	MULLARD	BZY88C	2
D19	210068	" " "	"	"	–
D20	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D21	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D22	210220	C22V 400mW ZENER	MULLARD	BZY88C	2
D23	210220	" " "	"	"	–
D24	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D25	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D26	200008	" " "	"	"	–
D27	200008	" " "	"	"	–
D28	200008	" " "	"	"	–
D29	200008	" " "	"	"	–

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DRAWING NUMBER 400328

SHEET 16 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D30	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D31	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D32	200001	" " "	"	"	–
D33	200001	" " "	"	"	–
D34	200001	" " "	"	"	–
D35	210047	C4V7 400mW ZENER	MULLARD	BZY88C	2
D36	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	–
D37	200008	" " "	"	"	–
D38	200008	" " "	"	"	–
D39	200008	" " "	"	"	–
D40	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D41	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D42	200001	" " "	"	"	–
D43	200002	Si RECTIFIER 1A.50V.	MOTOROLA	1N4001	4
D44	200002	" " " "	"	"	–
D45	200002	" " " "	"	"	–
D46	200002	" " " "	"	"	–
D47	210150	C15V 400mW ZENER	MULLARD	BZY88C	1
D48	200001	Si GEN PURPOSE	FAIRCHILD	1N4148	–
D49	210200	C20V 400mW ZENER	MULLARD	BZY88C	2
D50	210100	C10V 400mW ZENER	MULLARD	BZY88C	2
D51	210100	" " "	"	"	
D52	210200	C20V 400mW ZENER	MULLARD	BZY88C	–

NOTES

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SHEET 17 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
D53	200008	Si LOW LEAKAGE	FAIRCHILD	IN458A	-
D54	200008	"	"	"	-
D55	200001	Si GEN PURPOSE	"	IN4148	-
D56	200001	" " "			
D57		NOT USED			-
D58		NOT USED			-
D59	219007	214013 SELECTED WITH R18			-
D60	219007	214013 SELECTED WITH R19			-
D61	210033	C3V3 400mW ZENER	MULLARD	BZY88C3V3	-
D62	210033	" "	"	"	-
D63	200001	Si GEN PURPOSE	FAIRCHILD	IN4148	-
D64	210120	12V 400mW ZENER	MULLARD	BZY88C12	-
D65	210120	12V 400mW ZENER	MULLARD	BZY88C12	-
D66	200008	Si LOW LEAKAGE	FAIRCHILD	IN458A	-
D67	200008	" "	"	"	-
D68	200008	" "	"	"	-
D69		NOT USED			
D70		NOT USED			
D71	210047	4V7 400mw ZENER	MULLARD	BZY88C4V7	-
D72	200001	Si GEN PURPOSE	FAIRCHILD	IN4148	-
D73	213009	15V 5W ZENER DIODE	UNITRODE	TVS 515	-
D74	213009	15V 5W ZENER DIODE	UNITRODE	TVS 515	-

NOTES

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TITLE: 10G1 ANALOGUE PCB ASSEMBLY

DRAWING NUMBER: 400328 SHEET 18 OF 24

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DESIGNATOR	DATRON PART No	DESCRIPTION	MANUFACTURER	MANUFACTURER'S PART No	No Per Assy
Q1	230001	N CHAN CURRENT LIM	SILICONIX	E506	2
Q2	250008	SIL PNP TRANSISTOR	NATIONAL	BC214C/TO18	1
Q3	250008	SIL PNP TRANSISTOR	NATIONAL	BC214C/TO18	-
Q4	230001	N CHAN CURRENT LIM	SILICONIX	E506	-
Q5	230027	LOW LEAKAGE N-FET	TELEDYNE	U304	7
Q6	230027	" " "	"	"	-
Q7	230027	" " "	"	"	-
Q8	230027	" " "	"	"	-
Q9	230027	" " "	"	"	-
Q10	230027	" " "	"	"	-
Q11	230027	" " "	"	"	-
Q12	240017	LOW DRIFT DUAL NPN TRANS	NATIONAL	LM394	1
Q13	230002	N-CHAN J-FET	TELEDYNE	U1324E	6
Q14	230002	" "	"	"	-
Q15	230002	" "	"	"	-
Q16	230002	" "	"	"	-
Q17		NOT USED			
Q18	230002	N-CHAN J-FET	TELEDYNE	U1324E	-
Q19	230002	" "	"	"	-
Q20	240006	Si NPN	FAIRCHILD	2N3904	4
Q21	240006	"	"	"	-
Q22	240006	"	"	"	-
Q23	240006	"	"	"	-

NOTES.

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TITLE: 10G1 ANALOGUE PCB ASSEMBLY

DRAWING NUMBER: 400328 SHEET 19 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
Q24	240014	Si NPN	FAIRCHILD	BC337	2
Q25	250011	Si PNP	"	BC327	2
Q26	250011	" "	"	"	–
Q27	250001	Si PNP	"	BC214	2
Q28	240014	Si NPN	"	BC337	–
Q29	240001	Si NPN	"	BC184	3
Q30	240009	Si NPN	NATIONAL	2N5550	1
Q31	250009	Si PNP	"	2N5401	1
Q32	240001	Si NPN	"	BC184	–
Q33	240001	Si NPN	"	BC184	–
Q34	250001	Si PNP	"	BC214	–
Q35	230031	N-CHAN DUAL JFET	TELEDYNE	SU2656M	3
Q36	230031	N-CHAN DUAL JFET	TELEDYNE	SU2656M	–
Q37	230031	N-CHAN DUAL JFET	TELEDYNE	SU2656M	–

NOTES.

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TITLE 1061 ANALOGUE PCB ASSEMBLY

DRAWING NUMBER 400328 SHEET 20 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
M1 *	220030	Hi SPEED OPTO SELECTED	DATRON	HP4351 (RED)	2
M2	220017	DUAL OPTO ISOLATOR	FAIRCHILD	FCD880	7
M3	260002	LINEAR IC OP. AMP.	"	741HC	3
M4	220029	Hi SPEED OPTO SELECTED	DATRON	HP4351 (WHITE)	2
M5	220029	" " " "	"	HP4351 (WHITE)	–
M6 *	220030	" " " "		HP4351 (RED)	–
M7	220017	2KSV DUAL OPTO ISOLATOR	FAIRCHILD	FCD880	–
M8	220017	" " "	"	"	–
M9	220017	" " "	"	"	–
M10	220017	" " "	"	"	–
M11	220017	" " "	"	"	–
M12	280075	DUAL 4 I/P NAND	MOTOROLA	MC14012 BCP	1
M13	280015	QUAD LATCH	MOTOROLA	MC14076 BCP	4
M14	280015	" "		"	–
M15	260029	VOLTAGE COMPARATOR	NATIONAL	LM311HC	1
M16	280082	HEX INVERTER	FAIRCHILD	F40014 BPC	1
M17	280079	QUAD 2 I/P OR GATE	MOTOROLA	MC14071 BCP	1
M18	280008	QUAD 2 I/P NAND GATE		MC14011 BCP	3
M19	280008	" " " " "	"	"	–
M20	280015	QUAD LATCH	"	MC14076 BCP	–
M21	280015	" "	"	"	–
M22	260057	5534 OP AMP (DIL)	SIGNETICS	NE5534N	1
M23	220019	DUAL OPTO ISOLATOR	FAIRCHILD	FCD880/SELECTED	1

NOTES. * ALTERNATIVE HP4351 (220018)

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TITLE 1061 ANALOGUE PCB ASSEMBLY.

DRAWING NUMBER 400328 SHEET 21 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
M24	2200[illegible]	DUAL OPTO ISOLATOR	FAIRCHILD	FCD880	–
M25	260069	411 OP. AMP	NATIONAL	LF411CH	3
M26	280022	QUAD BILATERAL SWITCH	MOTOROLA	MC14016 BCP	1
M27	280008	QUAD 2 I/P NAND GATE	"	MC14011 BCP	–
M28	280044	BINARY UP/DOWN COUNTER	"	MC14516 BCP	1
M29	280011	DUAL D FLIP-FLOP	"	MC4013 BCP	1
M30	260026	LINEAR IC OP AMP	NATIONAL	LM212H	2
M31	260069	411 OP. AMP	"	LF411CH	–
M32	260026	LINEAR IC OP AMP	"	LM212H	–
M33	260002	" " " "	FAIRCHILD	741 HC	–
M34	260069	411 OP. AMP	NATIONAL	LF411CH	–
M35	* 290082	4051 MUX SELECT	[illegible]	MC4051BCL (GREEN)	1
M36	260002	741 OP AMP	FAIRCHILD	μA741HC	–
M37		NOT USED			
M38		NOT USED			
M39	260028	DUAL LINEAR IC	FAIRCHILD	μA1458CTC	1

NOTES * ALTERNATIVE 290081 MC14051BCL (WHITE)

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DRAWING NUMBER 400328

SHEET 22 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
RL1	330018	RELAY 2P2W 7v HOLD IN	P&B	SEE DRAWING	
RL2	330017	RELAY 1P2W MINATURE	OMROM	G2E 184PH (12VDC)	
	400379/1	WIRE / TERMINAL ASSY			7
	400379/2	" "			4
	410095-8A	P.C.B.			1
	459112	RELAY BRACKET	KDP		1
	540002	22 SWG TINNED COPPER WIRE			A/R
	512999	7/0.2 PTFE INSUL WHITE WIRE			165mm
	590001	SLEEVE MAX CABLE Ø 3.0	HELLERMANN ELECTRIC	H15x30—BLK HELSYN	5
	590004	SLEEVE – PTFE 1mm	"	FBIC	A/R
	590055	SLEEVE Ø1.0 SIL RUBBER	"	H.S CONT BLACK	250mm
J3	571075/C	16 WAY AP/3M RIBBON CABLE	DATRON		
	602001	F.S.V. TERMINAL	MOLEX	02-24-1875	6
J2,4,5	605002	16 WAY DIL SOCKET	JERMYN	A3220	3
	6050[illegible]60	14 WAY DIL SOCKET	ASTRALUX	ICL-143-SGT	8
	605061	16 WAY DIL SOCKET	ASTRALUX	ICL-163-SGT	12
J1&J6	605052	8 WAY POLARISED SOCKET	MOLEX	22-01-2085	2
	605059	8 WAY DIL SOCKET	ASTRALUX	ICL-083-SGT	5
	606005	CLIP FOR 605002	ANTIFERENCE	RC-74	3

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DRAWING NUMBER 400328

SHEET 23 OF 24

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
	611004	SCREW M3 x 6mm POZI-PAN	GKN	ZINC PLATED	1
	613005	WASHER M3 INT/SHAKEPROOF St	GKN	" "	1
	615002	NUT M3 FULL HEX STEEL		" "	1
	617010	NYLATCH PLUNGER HN3P	RICHCO	HN3P-32-4-1	8
	617011	" GROMMET HN3G	"	HN3G-32-1	8
	620005	CLOVERLEAF P.T.F.E. TERM.	SEALECTRO	FTE 15 P20	7
	620007	TEST POINT TERMINAL	MICROVAR	C30	25
	620003	SOLDER PCB TERMINAL LUG	HARWIN	H2105A	5
	630036	STANDARD STEATITE INSUL. BEAD	PARK ROYAL PORCELAIN CO	TYPE No1 (18 SWG)	6
	615005	NUT 3-48 UNC FULL HEX STEEL.			1
	613014	WASHER M2·5 INT/SHAKEPROOF	ST. GKN DISTRIBUTORS.	ZINC PLATED	1

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DRAWING NUMBER 400328

SHEET 24 OF 24

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
R1	000103	10k 5% 4W CARBON	MULLARD	CR25	21
R2	000103	10k " "	"	"	—
R3	000103	10k " "			—
R4	000103	10k " "			—
R5	000103	10k " "			—
R6	000102	1k " "			3
R7	000103	10k " "			—
R8	000472	4k7 " "			8
R9	000102	1k " " "			—
R10	000684	680k " " "	"		3
R11	063204	200k POT CERMET	BECKMAN	72P	1
R12	000103	10k 5% 1/4W CARBON	MULLARD	CR25	—
R13	000102	1k " " "	"	"	—
R14		NOT USED			—
R15	000472	4k7 5% 1/4W CARBON			—
R16	000332	3k3			2
R17	000683	68k			2
R18	000222	2k2			7
R19	000393	39k			1
R20	000104	100k			4
R21	000104	100k			—
R22	000104	100k			—
R23	000221	220R			2

NOTES. CIRCUIT DIAGRAM = 430329
CHECK PROCEDURE = 460329
CHECK LIST = 470329
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ECO	1147	1188	1214	1241	1243	1353	1369	1431	1461	1519	1559	1605
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SHEET 2 OF 16

JW 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
R24	000102	1k 5% 1/4W CARBON	MULLARD	CR25	—
R25	000332	3k3 " " "			—
R26	000103	10k " " "			—
R27	000102	1k " " "			—
R28	000682	6k8 " " "			1
R29	000104	100k " " "			—
R30		NOT USED			—
R31	000472	4k7 " " "			—
R32	000472	4k7 " " "			—
R33	000222	2k2 " " "			—
R34	000104	100k " " "			—
R35	000104	100k " " "			—
R36	000104	100k " " "			—
R37	000106	10M 10% " "			1
R38	000104	100k 5% " "			—
R39	000103	10k " " "			—
R40	000103	10k " " "			—
R41	000332	3k3 " " "			—
R42	000103	10k " " "			—
R43	000104	100k " " "			—
R44	000103	10k " " "			—
R45	000364	360k " " "			1
R46	000472	4k7 " " "			—

NOTES.

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JW 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
R47	000103	10k 5% ¼W CARBON	MULLARD	CR25	—
R48	000104	100k " " "	"	"	—
R49	000103	10k " " "	"	"	—
R50	000104	100k " " "	"	"	—
R51	000562	5k6 " " "	"	"	1
R52	000224	220k " " "	"	"	1
R53	000123	12k " " "	"	"	1
R54	000105	1M " " "	"	"	1
R55	000684	680k " " "	"	"	—
R56	000823	82 K " " "	"	"	1
R57		NOT USED			—
R58	000100	10R " " "	"	"	2
R59	000220	22R " " "	"	"	2
R60	000220	22R " " "	"	"	—
R61	000100	10R " " "	"	"	—
R62		NOT USED			—
R63	000222	2k2 5% ¼W CARBON	MULLARD	CR25	—
R64	000222	2k2 " " "	"	"	—
R65	000103	10k " " "	"	"	—
R66	000103	10k " " "	"	"	—
R67	000271	270R " " "	"	"	1
R68	000103	10k " " "	"	"	—
R69	000561	560R " " "	"	"	1

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J.W. 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R70	000222	2k2 5% ¼W CARBON	MULLARD	CR25	—
R71	000102	1k " " "	"	"	—
R72	000221	220R " " "	"	"	—
R73	000102	1k " " "	"	"	—
R74	000124	120k " " "	"	"	1
R75	000471	470R " " "	"	"	1
R76	000824	820k " " "	"	"	1
R77		NOT USED			—
R78		NOT USED			—
R79	000472	4k7 " " "	"	"	—
R80	000683	68k " " "	"	"	—
R81	000183	18k " " "	"	"	1
R82	000334	330k " " "	"	"	1
R83	063202	2K POT CERMET	BECKMAN	72 P	1
R84	000223	22k 5% ¼W CARBON	MULLARD	CR25	1
R85	000472	4k7 " " "	"	"	—
R86		NOT USED			—
R87	000104	100k			—
R88	000103	10k " " "	"	"	—
R89	000103	10k " " "	"	"	—
R90		NOT USED			—
R91	012002	20kΩ 1% ⅛W 50ppm MF	HOLCO	H8C	1
R92	015231	5k23 " " "	"	"	1

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J.W. 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
R93		NOT USED			—
R94	000102	1k 5% ¼W CARBON	MULLARD	CR25	—
R95	000472	4k7 " "	"	"	—
R96	000103	10k " "	"	"	—
R97	000222	2k2 " "	"	"	—
R98		NOT USED			—
R99	000104	100k 5% ¼W CARBON	"	"	—
R100	000104	100k " "	"	"	—
R101	000684	680K " "	"	"	—
R102	000103	10k " "	"	"	—
R103	000222	2k2 " "	"	"	—
R104	000102	1k " "	"	"	—
AN1	090046	10K x 7 2% NETWORK	BECKMAN	764-1-R10k	5
AN2		NOT USED			—
AN3	090046	10k x 7 2% NETWORK	BECKMAN	764-1-R10K	—
AN4	090042	8 BIT 'R-2R' LADDER NETWORK	SEE DWG		1
AN5	090046	10K x 7 2% NETWORK	BECKMAN	764-1-R10K	—
AN6	090046	10K x 7 2% NETWORK	BECKMAN	764-1-R10K	—
AN7	090046	10k x 7 2% NETWORK	BECKMAN	764-1-R10K	—

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
C1	150002	10µF 20% 16v DIP TANT	UNION CARBIDE	K10E16	4
C2	150002	10µF			
C3	150002	10µF " "	" "	"	—
C4	150016	1µF " 35v "		K1R0E35	7
C5	110013	·1µF 10% 250v POLYESTER	MULLARD	C280AE/P100k	2
C6	101103	·01µF 250v CER DISC	ERIE	801	5
C7	150006	4·7µF 20% 6v DIP TANT	UNION CARBIDE	K4R7E16	1
C8		NOT USED			—
C9	150002	10µF 20% 16v DIP TANT	UNION CARBIDE	K10E16	—
C10	102101	100pF 500v CER DISC	ERIE	801	5
C11	150002	10µF 20% 16v DIP TANT	UNION CARBIDE	K10E16	—
C12	150016	1µF " 35v " "	" "	K1R0E35	—
C13	150002	10µF " 16v "		K10E16	—
C14	110013	0·1µF 10% 250v POLYESTER	MULLARD	C280AE/P100K	—
C15	150002	10µF 20% 16V DIP TANT	UNION CARBIDE	K10E16	—
C16	102221	220pF 500v CER DISC	ERIE	801	3
C17	150002	10µF 20% 16v DIP TANT	UNION CARBIDE	K10E16	—
C18	150002	10µF " " " "	" "	"	—
C19	150016	1µF " 35v "	" "	K1R0E35	—
C20	150002	10µF " " " "	" "	K10E16	—
C21	102101	100pF 500v CER DISC	ERIE	801	—
C22	101103	·01µF 250v CER DISC	ERIE	801	—
C23	120016	2n2F 20% 100v POLYCARD	WIMA	FKC 2MIN	1

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
C24	130016	130pF 2½% 25v POLYSTYRENE	SUFLEX	HSQ130/2½-7/25	1
C25	102471	470pF 500v CER DISC	ERIE	801	2
C26	150016	1µF 20% 35v DIP TANT	UNION CARBIDE	KIROE35	–
C27	150002	10µF " 16v " "	" "	K10E16	–
C28	102220	22pF 500v CER DISC	ERIE	801	3
C29	150016	1µF 20% 35v DIP TANT	UNION CARBIDE	KIROE35	–
C30	150002	10µF 20% 16V " "	" "	K10E16	–
C31	110005	·01µF 10% 250v. POLYESTER	MULLARD	C280AE/P10K	2
C32	110003	·068µF			1
C33	150014	·68µF 20% 35v DIP TANT	UNION CARBIDE	KR68E35	1
C34	101103	·01µF 250v CER DISC	ERIE	801	–
C35	150002	10µF 20% 16V DIP TANT	UNION CARBIDE	K10E16	–
C36	101103	·01µF 250v CER DISC	ERIE	801	–
C37	102220	22pF 500v CER DISC		"	–
C38	102221	220pF 500v " "	"	"	–
C39	101103	·01µF 250v " "	"	"	–
C40	110005	10nF 20% 250v POLYESTER	MULLARD	C280AE/P10K	–
C41	110027	3n3F 20% 100V POLYESTER	WIMA	FKS2MIN	1
C42	102471	470pF 500v CER DISC	ERIE	801	–
C43	102101	100pF 500v CER DISC.	"	801	–
C44	150002	10µF 20% 16v DIP TANT	UNION CARBIDE	K10E16	–
C45	150016	1µF 20% 35v " "	" "	KIROE35	–
C46	102220	22pF 500v CER DISC	ERIE	801	–

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J.W. 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
C47	150004	100µF 20% 6V3 DIP. TANT.	UNION CARBIDE	K100E6V3	1
C48		NOT USED			–
C49	101103	·01µF 250v CER. DISC	ITT	CD10	–
C50	102101	100pF 500v CER. DISC	ITT	801	–
C51	102101	100pF 500v CER. DISC	ITT	801	–
C52		NOT USED			–
C53	102221	220pF 500v CER. DISC	ERIE	801	–
C54	130008	68pF 5% 160v POLYSTYRENE	SUFLEX	HS	1
C55	150016	1µF 20% 35v DIP. TANT	UNION CARBIDE	K10E16	–
C56	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	2
C57	104025	100nF +80% -20% / 50V CER DISC	SIEMENS	B37449	–

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J W 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED
D1	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	10
D2	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D3	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D4	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D5	200008	200mA 75V Lo Si DIODE	FAIRCHILD	IN4584	1
D6	220010	Si HOT CARRIER DIODE	HP	HSCH-3081 / HSCH-3083	1
D7	210027	2V7 400mW ZENER	MULLARD	BZY88C2V7	1
D8	210033	3V3 400mW ZENER	MULLARD	BZY88C3V3	1
D9	220022	DUAL 3 DF VARICAP DIODE	THOMPSON - CSF	BB104B	2
D10	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D11	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D12	200002	1A 50V GP Si DIODE	FAIRCHILD	IN4001	2
D13	213006	5V 5W ZENER	UNITRODE	TVS505	1
D14	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D15	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D16	200002	1A 50V GP Si DIODE	FAIRCHILD	IN4001	—
D17	200001	Si GP DIODE	FAIRCHILD	IN4148	—
D18		NOT USED			—
D19		NOT USED			—
D20	220022	DUAL 3 DF VARICAP DIODE	THOMPSON - CSF	BB104B	—
D21	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—

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PCB ASSY

400809

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED
Q1	240001	Si NPN TRANSISTOR		BC184	3
Q2	240001	" " "	"	"	—
Q3	240007	" " "	"	2N4044	2
Q4		NOT USED			—
Q5	240006	Si NPN TRANSISTOR		2N3906	2
Q6	250004	Si PNP	"	2N3906	3
Q7	250004	" "	"	"	—
Q8	240001	" NPN	"	BC184	—
Q9	240001	" "	"	"	—
Q10	250004	" PNP	"	BC214	—
Q11					—
Q12	250004	" PNP	"	BC214	1
Q13	240007	" NPN	"	2N4044	—
Q14	240001	" "	"	BC184	—
Q15	240006	" "	"	2N3906	—
Q16	250004	" PNP	"	2N3906	—
Q17	250008	" "	"	BC214C	1

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PCB ASSY

400809

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
M1	280011	DUAL D FLIP-FLOP	MOTOROLA	MC14013BCP	2
M2	280022	QUAD BILATERAL SWITCH	"	MC14016BCP	1
M3	280024	TRI-STATE HEX NON-INV. BUFFER	"	MC14503BCP	7
M4	280024	" " " " " "	"	"	–
M5	280024	" " " " " "	"	"	–
M6	280024	" " " " " "	"	"	–
M7	280015	QUAD LATCH	"	MC14076BCP	5
M8	280015	" "	"	"	–
M9	280015	" "	"	"	–
M10	280024	TRI-STATE HEX. NON-INV BUFFER	"	MC14503BCP	–
M11	280015	QUAD LATCH	"	MC14076BCP	–
M12	280015	" "	"	"	–
M13	280044	BINARY UP/DOWN COUNTER	"	MC14516BCP	4
M14	280044	" " " "	"	"	–
M15	290003	TIMER – ASTABLE	SIGNETICS	NE 555V	3
M16	270058	DUAL 1-of-4 DECODER	NATIONAL	74LS155	1
M17	270048	QUAD 2 I/P NAND GATE	NATIONAL	74LS00	2
M18	290113-19	4k x 8 EPROM - SEE PROG. SPEC.		TMS2532JL/PROGRAMMED	1
M19	280066-1	256 X 4 STATIC RAM	SEE DRAWING		2

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J.W 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
M20	280066-1	256 X 4 STATIC RAM	SEE DRG		–
M21	270064	QUAD TRISTATE BUFFER	NATIONAL	DM 74LS125N	1
M22	280011	DUAL D FLIP-FLOP	"	MC14013BCP	–
M23	270053-3	A-D CHIP	FERRANTI	ULA 2035H	1
M24	280024	TRI-STATE HEX. NON-INV. BUFFER	MOTOROLA	MC14503BCP	–
M25	280024	" " " " " "	"	"	–
M26	280006	DUAL J-K FLIP-FLOP	"	MC14027BCP	1
M27	280004	14BIT BINARY COUNTER	"	MC14020BCP	1
M28	270051	DUAL 4 I/P AND GATE	NATIONAL	74LS21	2
M29	270055	DUAL 4 I/P NAND GATE	"	74LS20	2
M30	290057-19	4k x 8 EPROM - SEE PROG. SPEC		TMS 2532 JL/PROGRAM'D	1
M31	280096	1K x 4BIT STATIC CMOS RAM	SEE DRAWING		2
M32	270069	BCD DECIMAL DECODER LS	NATIONAL	DM 74LS42N	1
M33	270051	DUAL 4 I/P AND GATE	"	74LS21	–
M34	270055	DUAL 4 I/P NAND GATE	"	74LS20	–
M35	290056-19	4k x 8 EPROM - SEE PROG. SPEC		TMS 2532 JL/PROGRAM'D	1
M36	280096	1K x 4BIT STATIC CMOS RAM	SEE DRAWING		–
M37	280025	QUAD BILATERAL SWITCH	MOTOROLA	MC14066BCP	2
M38	280071	TRIPLE 3 I/P NOR GATE	MULLARD	HEF4025P	2
M39	280017	HEX INVERTER	MOTOROLA	MC14069BCP	1
M40	280083	QUAD 2 I/P NOR GATE	MULLARD	HEF4001BP	1
M41	280044	BINARY UP/DOWN COUNTER	MOTOROLA	MC14516BCP	–
M42	280003	QUAD LATCH	"	MC14042BCP	2

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
M43	290003	TIMER - ASTABLE	SIGNETICS	NE 555V	–
M44	270048	QUAD 2 I/P NAND GATE	NATIONAL	74LS00	–
M45	270050	HEX INVERTER	"	74LS04	1
M46	280025	QUAD BILATERAL SWITCH	MOTOROLA	MC14066BCP	–
M47	280070	DIVIDE-BY-8 COUNTER/DIVIDER	MULLARD	HEF 4022P	1
M48	280071	TRIPLE 3 I/P NOR GATE	"	HEF 4025P	–
M49	280023	QUAD 2 I/P NOR GATE	MOTOROLA	MC14001 BCP	1
M50	280044	BINARY UP/DOWN COUNTER	"	MC14516 BCP	–
M51	280003	QUAD LATCH	"	MC14042 BCP	–
M52	270056	8 I/P NAND GATE	NATIONAL	74LS30	1
M53	280061	MICRO PROCESSOR CHIP	MOTOROLA	MC6800L	1
M54	270023	QUAD 2 I/P NAND GATE	NATIONAL	[illegible]37	1
M55	270054	QUAD 2 I/P AND GATE	"	[illegible]	2
M56	270054	" " " "	"	"	–
M57	270057	DUAL JK FLIP-FLOP	"	[illegible]	1
M58	280009	HEX INVERTER/BUFFER	MOTOROLA	MC14049	2
M59	280009	HEX INVERTER/BUFFER	MOTOROLA	MC14049	–
M60, M62	260031	VOLTAGE DETECTOR	INTERSIL	ICL8211	2
M61	290003	TIMER - ASTABLE	SIGNETICS	NE555V	–
S1		NOT USED			–
S2		NOT USED			–
S3		NOT USED			–

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JW 1163

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
L1	370003	1000μH 40Ω R.F. CHOKE	[illegible]	[illegible]	1
L2	370002	100μH ·40Ω " "	TOKO	YXRS 18576	1
	590004	SLEEVE - PTFE	HELLERMAN ELECTRIC	FE10	A/R
TP1-TP33, LINKS	540002	22 SWG. BTC WIRE			A/R
	920048	BUS STRIP	MEKTRON	M823 14 7 3F	1
	590055	SLEEVE Ø1·0 SIL RUBBER	HELLERMAN ELECTRIC	HIS CONT BLACK	10 cm
	630098	COMPONENT CLIP	[illegible]	KKU-8	1
	606005	CLIP FOR 605002.	ANTIFERENCE	RC74	3
J1, J2, J4	605002	16 WAY D.I.L. LOW PROFILE SKT	JERMYN OR ANTIFERENCE	A23-2001/Y OR ICN-163-33	3
	605065	28 WAY D.I.L. " " "	AUGAT	328-AG39D	1
	605060	14 WAY D.I.L. SOCKET	ASTRALUX OR JERMYN	ICL143-S3T	22
	605061	16 WAY D.I.L. SOCKET	" "	ICL163-S6T	29
	605050	40 WAY D.I.L. SOCKET	AUGAT	340-AG39D	1
	605063	22 WAY D.I.L. SOCKET	AUGAT	322-AG39D	2
	605064	24 WAY D.I.L. SOCKET		324-AG39D	3
	605062	18 WAY D.I.L. SOCKET	"	318-AG39D	2
JL3	604037	PROGRAMMING CLASS 160 PLUG	"	8136-475G8	1
	605059	8 WAY D.I.L. SOCKET	ASTRALUX	ICL-083-S6T	1
J5	605052	8 WAY POLARISED SOCKET	"	22-01-2085	1
J3	605102	24 WAY D.I.L SOCKET. GOLD	CA	CA-24S-10SD	1
	410096-11B	PCB			1

NOTES.

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TITLE 1061 DIGITAL PCB. ASSY.

DRAWING NUMBER 400329

SHEET 15 OF 16

JW 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
	400379/1	WIRE/TERMINAL ASSY.			8
J6		NOT USED			–
J7		NOT USED			–
	620003	SOLDER PCB TERMINAL LUG	HARWIN	H2105A	5
	620007	TEST POINT TERMINAL	MICROVAR	C 30	32
	630036	CERAMIC BEADS 18 SWG	PARK ROYAL PORCELAIN	Nº 1	2
	920140-1	28 PIN DIL. HEATSINK	AAVID	5806B	1

NOTES.

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DRAWING NUMBER 400329

SHEET 16 OF 16

W 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R1	000472	4k7 5% ¼W CARBON	MULLARD	CR25	3
R2	000103	10k " " "			—
R3	000183	18k " " "			—
R4	000103	10k " " "			—
R5	000104	100k " " "			1
R6		NOT USED			—
R7		NOT USED			—
R8	000102	1k 5% ¼W CARBON	MULLARD	CR25	10
R9	000102	1k " " "			—
R10	000102	1k " " "			—
R11	000102	1k " " "			—
R12	000102	1k " " "			—
R13	000102	1k " " "			—
R14	000102	1k " " "			—
R15	000102	1k " " "			—
R16		NOT USED			—
R17		NOT USED			—
R18	000102	1k " " "			—
R19	000102	1k " " "			—
R20	000102	1k " " "			—
R21		NOT USED			—
R22	000272	2k7 " " "			3
R23	000472	4k7 " " "			—

NOTES. CIRCUIT DIAGRAM = 430330
CHECK PROCEDURE = 460330
CHECK LIST = 470330

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DRAWING NUMBER 400330 SHEET 2 OF 7

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R24	000272	2k7 5% ¼W CARBON	MULLARD	CR25	—
R25	000272	2k7 " " "			—
R26	000272	2k7 " " "			—
R27	000182	1k8 " " "			—
R28	000222	2k2 " " "			—
R29	000272	2k7 " " "			—
R30	000102	1k " " "			—
R31	000102	1k " " "			—
R32	000102	1k " " "			—
R33	000102	1k " " "			—
R34	000102	1k " " "			—
R35	000102	1k " " "			—
R36	000102	1k " " "			—
R37	000102	1k " " "			—
R38	000472	4k7 " " "			—
R39	000393	39k " " "			1
R40		NOT USED			—
R41	000563	56K 5% ¼W CARBON	MULLARD	CR25	1
C1	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	3
C2		NOT USED			—
C3		NOT USED			—

NOTES.

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TITLE 1061 DISPLAY DRIVER PCB. ASSY.

DRAWING NUMBER 400330 SHEET 3 OF 7

J.W. 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
C4	110013	0·1μF 20% 250v POLYESTER	MULLARD	C280AE/P100K	8
C5	110013	" " " "	"	"	–
C6	110013	" " " "	"	"	–
C7	110013	" " " "	"	"	–
C8	110013	" " " "	"	"	–
C9	110013	" " " "	"	"	–
C10	110013	" " " "	"	"	–
C11	110013	" " " "	"	"	–
C12		NOT USED			–
C13	110005	0·01μF 20% 250v "	"	C280AE/P10K	3
C14	150020	10μF. 20% 25v. DIP. TANT	UNION CARBIDE	K10E25	–
C15	110005	0·01μF 20% 250v POLYESTER	MULLARD	C280AE/P10K	–
C16	110005	0·01μF 20% 250V POLYESTER	MULLARD	C280AE/P10K	–
C17	150020	10μF 20% 25V DIP. TANT	UNION CARBIDE	K10E25	–
C18	180019	10μF 20% 250v AL ELECT	I.T.T.	JF10-1005 250AA	1
D1		NOT USED			–
D2		NOT USED			–
D3		NOT USED			–
D4	200001	DIODE Si GP.	FAIRCHILD	IN4148	8
D5	200001	" " "	"	"	–
D6	200001	" " "	"	"	–

NOTES

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DATE	DRAWING NUMBER 400330 SHEET 4 OF 7

IW 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D7	200001	DIODE Si GP.	FAIRCHILD	IN4148	–
D8	200001	" " "	"	"	–
D9	200001	" " "	"	"	–
D10	200001	" " "	"	"	–
D11	200001	" " "	"	"	–
D12	213005	DIODE ZENER 7.5 V. ½W	MOTOROLA	BZX79C7.5	1
Q1	250009	Si PNP TRANSISTOR	NATIONAL	2N5401	11
Q2	250009	" " "	"	"	–
Q3	250009	" " "	"	"	–
Q4		NOT USED			–
Q5	240009	Si NPN TRANSISTOR	"	MPS L01	8
Q6	240009	" " "	"	"	–
Q7	240009	" " "	"	"	–
Q8	240009	" " "	"	"	–
Q9	240009	" " "	"	"	–
Q10	240009	" " "	"	"	–
Q11	240009	" " "	"	"	–
Q12	240009	" " "	"	"	–
Q13	250009	" PNP "	"	2N5401	–
Q14	250009	" " "	"	"	–
Q15	250009	" " "	"	"	–
Q16	250009	" " "	"	"	–

NOTES

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DATE	DRAWING NUMBER 400330 SHEET 5 OF 7

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. REQ. PER ASSY.
Q17	250009	Si PNP TRANSISTOR	NATIONAL	2N5401	1
Q18	250009	" " "			1
Q19	250009	" " "			1
Q20	250009	" " "			1
M1	280062	128 x 8 BIT STATIC RAM	MOTOROLA	MCM6810L	1
M2		NOT USED			-
M3		NOT USED			-
M4	280023	QUAD 2 I/P NOR GATE	"	MC14001BCP	2
M5	280023	QUAD 2 I/P NOR GATE	"	"	-
M6	280024	TRI-STATE HEX NON-INV BUFFER	"	MC14503BCP	1
M7	280024	" " " " "	"	"	-
M8	280059	DUAL BINARY UP COUNTER	"	MC14520BCP	1
M9	270045	QUAD 2-1 DATA SELECT LS TTL	NATIONAL	SN74LS157	1
M10		NOT USED			-
M11	280043	4 BIT LATCH/4 TO 16 LINE DECODER	MOTOROLA	MC14515CP	1
M12	270048	QUAD 2 I/P NAND LS TTL	NATIONAL	SN74LS00N	1
M13	280077	HEX GATE	MOTOROLA	MC14572	1
AN2-AN4	090065	330K±1% RESISTOR NETWORK	BECKMAN	724-1-330K	3

NOTES

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1061 DISPLAY DRIVER PCB ASSY

DRAWING NUMBER 400330 SHEET 6 OF 7

F.W. 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. REQ. PER ASSY.
J1	571095/C	16 WAY AP/3M RIBBON CABLE	DATRON	—	1
J2	605102	24 WAY DIL SKT GOLD	CA	CA-249-105D	1
	605060	14 WAY DIL SOCKET	AUGAT	314-AG39D	3
	605061	16 WAY DIL SOCKET	"	316-AG39D	5
	605064	24 WAY DIL SKT TIN PLATE	AUGAT	324-AG39D	2
TP1-TP6	540002	22 SWG BTC WIRE			A/R
	590004	SLEEVE - PTFE	HELLERMANN ELECTRIC	FE10	A/R
	410097-5B	PCB			1
	620007	TEST POINT TERMINAL	MICROVAR	C30	5

NOTES.

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TITLE 1061 DISPLAY DRIVER PCB. ASSY.

DRAWING NUMBER 400330 SHEET 7 OF 7

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R1		NOT USED			
R2	090001	P.T.C. THERMISTOR	MULLARD	VA8650	2
R3	090044	4MO 1% MF	ACI		(1 SET) R3 R4
R4		3K94 1% MF	ACI		–
R5	000106	10M 5% ¼W CARBON	MULLARD	CR25	5
R6	000332	3K3 " " "	"	"	2
R7	000106	10M " " "	"	"	–
R8	019761	9K76 1% 50ppm M.F.	HOLCO	HB	1
R9	090001	P.T.C. THERMISTOR	MULLARD	VA8650	–
R10		NOT USED			–
R11	000106	10M 5% ¼W CARBON	MULLARD	CR25	–
R12	000563	56K " " "	"	"	2
R13	000563	56K " " "	"	"	–
R14	000104	100K " " "	"	"	6
R15	000243	24K " " "	"	"	1
R16	000682	6K8 " " "	"	"	1
R17	000103	10K " " "	"	"	3
R18	000107	100M " " "	"	"	1
R19	000104	100K " " "	"	"	–
R20	041505	15M 1% 100ppm C.F.	ALLEN BRADLEY	CC	2
R21	041505	15M " " "	"	"	–
R22	000222	2K2 5% ¼W CARBON	MULLARD	CR25	2
R23	000391	390R " " "	"	"	1

NOTES.

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DATE	5-12-78	27-2-79	4 5 79
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1061 OHMS PCB ASSY

DRAWING NUMBER 400331

SHEET 2 OF 12

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R24	070093	57K77 0·1% WIREWOUND	MANN		1
R25	070094	577K7 0·1% WIREWOUND	MANN		1
R26	063204	2[illegible]K POT CERMET	BECKMANN	72P	2
R27	063204	200K " "	"	"	–
R28	000102	1M 5% ¼W CARBON	MULLARD	CR25	4
R29	070082	5K777 0·1% WIREWOUND	MANN		1
R30	070091	1K2839 0·1% WIREWOUND	MANN		2
R31	070091	1K2839 0·1% "	"		–
R32	000332	3K3 5% ¼W CARBON	MULLARD	CR25	1
R33	000154	150K 5% ¼W CARBON	MULLARD	CR25	1
R34	000561	560R " " "	"	"	1
R35	000106	10M 5% ¼W CARBON	MULLARD	CR25	–
R36	041005	10M 1% 100ppm C.F.	ALLEN BRADLEY	CC	1
R37	090049	18M MATCHED PAIR	HOLCO		1 PAIR
R38		18M			–
R39		F.S.V			–
R40		F.S.V			–
R41	000473	47K 5% ¼W CARBON	MULLARD	CR25	3
R42	000624	620K " " "	"	"	1
R43	000473	47K " " "	"	"	–
R44	000102	1K " " "	"	"	2
R45	000102	1K " " "	"	"	–
R46	000562	5K6 " " "	"	"	2

NOTES.

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TITLE 1061 OHMS P.C.B ASSY.

DRAWING NUMBER 400331

SHEET 3 OF 12

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R47	000473	47K 5% 1/4W CARBON	MULLARD	CR25	–
R48	000433	43K " " "	"	"	1
R49	000392	3K9 " " "	"	"	–
R50	000562	5K6 " " "	"	"	–
R51	000105	1M " " "	"	"	–
R52	000104	100K " " "	"	"	–
R53	000273	27K " " "	"	"	2
R54	000104	100K " " "	"	"	–
R55	000105	1M " " "	"	"	–
R56	000824	820K " " "	"	"	1
R57	000104	100K " " "	"	"	–
R58	000273	27K " " "	"	"	–
R59	000104	100K " " "	"	"	–
R60	000123	12K " " "	"	"	1
R61	000334	330K " " "	"	"	1
R62	000222	2K2 " " "	"	"	–
R63	000223	22K " " "	"	"	1
R64	000333	33K " " "	"	"	2
R65	000821	820R " " "	"	"	1
R66	000105	1M " " "	"	"	–
R67	000474	470K " " "	"	"	1
R68	000124	120K " " "	"	"	1
R69	000103	10K " " "	"	"	–

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DRAWING NUMBER 400331

SHEET 4 OF 12

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R70	000221	220R 5% 1/4W CARBON	MULLARD	CR25	1
R71	000103	10k 5% 1/4W CARBON	MULLARD	CR25	–
R72	000333	33k 5% 1/4W CARBON	MULLARD	CR25	–
R73	000823	82k 5% 1/4W CARBON	MULLARD	CR25	1
R74	000106	10M 5% 1/4W CARBON	MULLARD	CR25	–
AN1	090017	100k x 7 2% NETWORK	BECKMAN	764-1-R100k	1

NOTES.

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SHEET 5 OF 12

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
C1	120028	4700pF 20% 100V POLYCARB	WIMA	FKC 2 MIN.	1
C2	120026	680pF 20% 100V POLYCARB	WIMA	FKC 2 MIN.	1
C3	102470	47pF 500V CER DISC	ERIE	8C	2
C4	102470	47pF 500V CER DISC	ERIE	8C	—
C5	102222	2n2F 500V CER DISC	ERIE	8C	1
C6	101103	·01μF 250V CER DISC	ERIE	8C	4
C7	120022	1nSF 20% 100V POLYCARB	WIMA	FKC2 MIN	1
C8	120014	2·2μF 10% 63V POLYCARB	ASHCROFT	AZB222:B	2
C9	120014	2·2μF " " "	"	"	—
C10	110013	0·1μF 10% 250V POLYESTER	MULLARD	C280AE/P100K	2
C11	150001	22μF 20% 16V DIP TANT	UNION CARBIDE	[illegible]	2
C12	101103	·01μF 250V CER DISC	ERIE	8C	—
C13	150001	22μF 20% 16V DIP TANT	UNION CARBIDE	[illegible]	—
C14	101103	·01μF 250V CER DISC	ERIE	8C	—
C15	102100	10pF 500V CER DISC	"	"	2
C16	102100	10pF " " "	"	"	—
C17	102102	1nF " " "	"	"	1
C18	120021	0·47μF 10% 63V POLYCARB	ASHCROFT	AZB471:B	1
C19	150014	680nF 20% 35V DIP TANT	UNION CARBIDE	KR686J35	1
C20	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	[illegible]	1
C21	150020	10μF 20% 25V DIP TANT	UNION CARBIDE	K10E25	2
C22	101103	·01μF 250V CER DISC	ERIE	8C	—
C23	150020	10μF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—

NOTES

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IW 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
C24	102101	100pf 10% 500V Cer Disc	ITT	CD D	3
C25	102101	100pf " " " "	"	"	—
C26	102101	100pF " " " "	"	"	—
C27	110013	100nF 20% 250V POLYESTER	MULLARD	C280AE/P100K	—
C28	104025	100nF $^{+80}_{-20}$% 50V CER DISC	SIEMENS	B37449	1
D1	213001	10V 5W ZENER	MOTOROLA	1N5347	4
D2	213001	10V " "	"	"	—
D3		NOT USED			
D4		NOT USED			
D5		NOT USED			
D6		NOT USED			
D7	210027	2V7 400mW ZENER	MULLARD	BZY88C 2V7	1
D8	200001	Si G.P.	FAIRCHILD	1N4148	11
D9		NOT USED			
D10	213001	10V 5W ZENER	MOTOROLA	1N5347	—
D11	213001	10V " "	"	"	—
D12	200001	Si G.P.	FAIRCHILD	1N4148	—
D13		NOT USED			
D14	200008	Si LOW LEAKAGE	FAIRCHILD	1N458A	3
D15	200008	Si " "	"	"	—
D16	210075	7V5 400mW ZENER	MULLARD	BZY88C 7V5	1
D17	210120	12V 400mW ZENER	"	BZY88C12	1

NOTES.

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IW 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D18	200001	Si G.P.	FAIRCHILD	IN4148	–
D19	200001	Si "	"	"	–
D20	200001	Si "	"	"	–
D21	200001	Si "	"	"	–
D22	200001	Si "	"	"	–
D23	200001	Si "	"	"	–
D24		NOT USED			
D25	200001	Si "	"	"	–
D26	200001	Si "	"	"	–
D27	200001	Si GP.	"	"	–
D28	200008	Si. Low LEAKAGE	"	IN458A	–
Q1	230027	N-CHAN J FET SELECTED	TELEDYNE	U3114E.	7
Q2	230027	" " "	"	"	–
Q3	230027	" " "	"	"	–
Q4	230027	" " "	"	"	–
Q5	230002	N-CHAN J FET	SILICONIX	U1994E	6
Q6	230027	N-CHAN J FET SELECTED	TELEDYNE	U3114E	–
Q7	230027	" " "	"	"	–
Q8	230002	N-CHAN J FET.	SILICONIX	U1994E.	–
Q9	230002	" "	"	"	–
Q10	240017	Si NPN SUPERMATCH PAIR	NATIONAL	LM394	1
Q11	230029	N-CHAN J FET	SILICONIX	J309	4

NOTES.

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DRAWING NUMBER 400331

SHEET 8 OF 12

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
Q12		NOT USED			
Q13	240012	Si NPN	NATIONAL	2N3053	1
Q14	230027	N-CHAN J FET	TELEDYNE	U3114E	–
Q15	250001	Si PNP	NATIONAL	BC214	2
Q16	250011	Si PNP	NATIONAL	BC327	1
Q17	240001	Si NPN	NATIONAL	BC184	2
Q18	240001	Si NPN	NATIONAL	BC184	–
Q19	250001	Si PNP	NATIONAL	BC214	–
Q20	230002	N-CHAN J FET	SILICONIX	U1994E	–
Q21	230029	N-CHAN J FET.	SILICONIX	J309	–
Q22	230029	N-CHAN J FET.	SILICONIX	J309	–
Q23	230002	" "	"	U1994E	–
Q24	230002	" "	"	"	–
Q25	230029.	" "	"	J309	–

NOTES.

SEE SHEET 2 FOR LATEST ISSUE

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DATE 10-10-78
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DATE

datron ELECTRONICS LTD

TITLE 1061 OHMS P.C.B. ASSY.

DRAWING NUMBER 400331

SHEET 9 OF 12

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPA MANUFACTURER	MANUFACTURER'S PART No	No. Per Assy.
M1	280022	QUAD BILATERIAL SWITCH	MOTOROLA	MC 14016 BCP	2
M2	280022	" "			-
M3	260069	411 OP. AMP	NATIONAL	LF411 CH	1
M4	260026	OP AMP	NATIONAL	LM 212	1
M5	280025	QUAD ANALOGUE SWITCH	MOTOROLA	MC 14066 BCP	1
M6	280072	M'STABLE / ASTABLE M'VIBR	R C A	CD 4047 AE	1
M7	280015	QUAD LATCH	MOTOROLA	MC 14076 BCP	2
M8	280015	" "	"	"	-
M9	280011	DUAL D FLIP FLOP	MOTOROLA	MC 14013 BCP	1

NOTES

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TITLE 1061 OHMS PCB ASSY

DRAWING NUMBER 400331

SHEET 10 OF 12

JW 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. Per Assy.
RL1	330019	RELAY 4P 2W 7v HOLD-IN	P&B	SEE DRAWING	1
	400379/2	WIRE / TERMINAL ASSY			6
	410099-6	OHMS PCB.			1
	459112	RELAY BRACKET	KDP		1
	512999	7/0.2 PTFE INSULATED WHITE		TYPE C	280mm.
	540002	22 SWG TINNED COPPER WIRE			A/R
	590001	SLEEVE MAX CABLE Ø 3.0	HELLERMANN ELECTRIC	H15 x 20mm BLK HELSYN	9
	590055	SLEEVE Ø 1.0 SIL RUBBER	HELLERMANN ELECTRIC	H15 CONT BLACK	20mm
	590004	PTFE SLEEVE			A/R.
	602001	F.S.V. TERMINAL	MOLEX	02-04-1875	4
	571095/C	16 WAY AP/3M RIBBON CABLE	DATRON		1
	605060	14 WAY DIL SOCKET	ASTRALUX	ICL-143-S3T	5
	605061	16 WAY DIL SOCKET	ASTRALUX	ICL-163-66T	2
J1	605053	12 WAY POLARISED SOCKET	MOLEX	22-01-2125	1

NOTES.

J1 --

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TITLE 1061 OHMS PCB ASSY.

DRAWING NUMBER 400331

SHEET 11 OF 12

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
	611004	SCREW M3X6mm STEEL POZIPAN ZINC PLATED . GKN.			1
	613005	WASHER M3 INT/SHAKEPROOF.	GKN DISTRIBUTORS	ZINC PLATED	2
	615002	NUT M3 FULL HEX STEEL		ZINC PLATED	1
	615005	NUT 3-48 UNC FULL HEX	STEEL	" "	1
	617010	NYLATCH PLUNGER HN3P	ORDER FROM C.J. FOX & SONS.	HN3P-32-4-1	4
	617011	NYLATCH GROMMET HN3G		HN3G-32-1	4
	618002	TO5 MOUNTING PAD	JERMYN	TO518-004D	1
	620003	SOLDER PCB TERMINAL LUG	HARWIN	H2105A	8
	630024	STANDARD STEATITE INSUL BEAD	PARK ROYAL PORCELAIN CO	TYPE No 2 (16 SWG)	8
	620007	TEST TERMINAL	MICROVAR	C30	11

NOTES.

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DATE 10-10-78	datron ELECTRONICS LTD
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CHECKED	
APPROVED	
DATE	DRAWING NUMBER 400331 SHEET 12 OF 12

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED
R1	000103	10k 5% ¼W CARBON	MULLARD	C[illegible]25	1
R2	000104	100k 5% ¼W CARBON	MULLARD	C[illegible]25	4
R3	000104	100k 5% ¼W CARBON	MULLARD	C[illegible]25	–
R4	000104	100k 5% ¼W CARBON	MULLARD	C[illegible]25	–
R5	000104	100k 5% ¼W CARBON	MULLARD	C[illegible]25	–
AN1	090017	100k x 7 2% NETWORK	BECKMAN	764-1-R100k	2
AN2	090017	100k x 7 2% NETWORK	BECKMAN	764-1-R100k	–
C1	150002	10μF 20% 16V DIP TANT	UNION CARBIDE	K10E16	1
C2	150005	2μ2F 20% 16V DIP TANT	UNION CARBIDE	K2R2E16	1
C3	150008	470nF 20% 35V DIP TANT	UNION CARBIDE	KR47E35	1
C4	110005	10nF 20% 250v POLYESTER	MULLARD	C280AE P10K	1
C5	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	K1R0E35	5
C6	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	K1R0E35	–
C7	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	K1R0E35	–
C8	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	K1R0E35	–
C9	150016	1μF 20% 35V DIP TANT	UNION CARBIDE	K1R0E35	–

NOTES

SEE SHEET 2 FOR LATEST ISSUE

	1	2	3	4	5	6	7	8	9	10	11
ISS	RELEASED	1078	1093	1137	1165	1198	1217	1246	1253	1403	1592
DATE	1.2.80	22.2.80	26.3.80	28.7.80	6.8.80	25.8.80	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
CHKD	MD	AJ	AJ	AJ	MD	MD	MD	MD	MD	MD	MD

datron

1061
BCD/PROG PCB ASSY

400332 2 SHEET OF 4

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED
D1	200001	75mA 75V GP Si DIODE	FAIRCHILD	1N4148	1
M1	280081	PERIPHERAL INTERFACE ADAPTOR	MOTOROLA	MC6821P	1
M2	290003	555 TIMER	FAIRCHILD	μA555TC	1
M3	270050	HEX INVERTOR LS	NATIONAL	DM74LS04N	2
M4	270048	QUAD 2 I/P NAND LS	NATIONAL	DM74LS00N	1
M5	270051	DUAL 4 I/P AND LS	NATIONAL	DM74LS21N	1
M6	270050	HEX INVERTOR LS	NATIONAL	DM74LS04N	–
M7	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	9
M8	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M9	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M10	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M11	290080-19	2716 EPROM PROGRAMMED	DATRON	IB2716(290080-1SS)	1
M12	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M13	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M14	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M15	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M16	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M17	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503BCP	9
M18	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503BCP	–
M19	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503BCP	–
M20	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503BCP	–
M21	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503BCP	–

NOTES.

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DATE 1.2.80
DRAWN [illegible]
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TITLE 1061
BCD/PROG. PCB. ASSY.

DRAWING NUMBER 400332 SHEET 3 OF 4

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
M22	280066	256 X 4 BIT STATIC CMOS RAM	SEE DRAWING		2
M23	280066	256 X 4 BIT STATIC CMOS RAM	SEE DRAWING		-
M24	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503 BCP	-
M25	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503 BCP	-
M26	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503 BCP	-
M27	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503 BCP	-
	620007	TEST POINT TERMINAL	MICROVAR	C 30	9
J3	605051	4 WAY POLARISED SOCKET	MOLEX	(22-01-2045) 6471-4-1	1
J1, J2	605102	24 WAY DIL. SOCKET GOLD	CA	CA 24S 10SD	2
	605064	24 WAY DIL. SOCKET	AUGAT	324-AG39D	1
	605060	14 WAY DIL. SOCKET	ASTRALUX OR JERMYN	ICL 143 S3T	4
	605061	16 WAY DIL. SOCKET	ASTRALUX OR JERMYN	ICL 163-S6T	18
J5	573120/C	24 WAY AP/3M RIBBON CABLE	DATRON		1
	605050	40 WAY DIL. SOCKET	AUGAT	340-AG39D	1
	605059	8 WAY DIL. SOCKET	ASTRALUX	ICL-083-S6T	1
	605063	22 WAY DIL. SOCKET	AUGAT	322-AG39D	2
J4	605002	16 WAY DIL. SOCKET	ASTRALUX	ICN-163-S3G	1
	606005	SOCKET CLIP	ASTRALUX	RC-74	1
	605056	CRIMP TERMINAL	MOLEX	4809-TL	1
	400379/1	WIRE/TERMINAL ASSY			3
	410100-4A	PCB			1

NOTES.

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TITLE 1061 BCD/PROG. PCB. ASSY.

DRAWING NUMBER 400332

SHEET 4 OF 4

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R1	000123	12K 5% ¼W CARBON	[illegible]	[illegible]	2
R2		NOT USED			–
R3	000332	3K3 5% ¼W CARBON	[illegible]	[illegible]	3
R4		NOT USED			–
R5	000123	12K 5% ¼W CARBON	[illegible]	[illegible]	–
R6	000222	2K2 5% ¼W CARBON	[illegible]	[illegible]	1
R7		NOT USED			–
R8		NOT USED			–
R9		NOT USED			–
R10	000562	5K6 5% ¼W CARBON	MULLARD	CR25	1
R11	000103	10K 5% ¼W CARBON	MULLARD	CR25	4
R12	000105	1M 5% ¼W CARBON	MULLARD	CR25	1
R13		NOT USED			–
R14	000103	10k 5% ¼W CARBON	MULLARD	CR25	–
R15		NOT USED			–
R16	000332	3k3 5% ¼W CARBON	MULLARD	CR25	–
R17		NOT USED			–
R18	000681	680R 5% ¼W CARBON	MULLARD	CR25	1
R19	000332	3k3 5% ¼W CARBON	MULLARD	CR25	–
R20	000103	10k 5% ¼W CARBON	MULLARD	CR25	–
R21	000103	10K 5% ¼W CARBON	MULLARD	CR25	–

NOTES

SEE SHEET 3 FOR LATEST ISSUE

ISS	A	1	2 C	3	4	5	6	7	8
E.C.O.		Rell To Prod	372/395	981	1034	1085	1167	1199	1326
DATE	2 MAY 79	17 MAY 79	25 OCT 79	6 NOV 79	2 JAN 80	22 APR 80	28 AUG 80	24 FEB 81	2 SEP 82
CHKD			MD	MD	MD	MD	MD	MD	MD

4 MAY 79 — N. J. SMITH — MD — 7 MAY 79

datron ELECTRONICS LTD

REAR INPUT ASSY

1061/71

400386

SHEET 3 OF 8

W 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
C1	150022	2µ2F 20% 35v DIP TANT	UNION CARBIDE	K2R2E35	1
C2		NOT USED			–
C3		NOT USED			–
C4	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	1
C5	102101	100pF 10% 500v CER DISC	ITT	CD10	1
C6	110013	100nF 20% 250V POLYESTER	MULLARD	C280AE P100K	1
C7	150014	680nF 20% 35V DIP TANT	UNION CARBIDE	K68E35	1

NOTES

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ISS									
E.C.O.									
DATE									
CHKD									

DATE 23.9.80 — DRAWN IL — CHECKED — APPROVED

datron ELECTRONICS LTD

TITLE REAR INPUT ASSY

1061/1071

DRAWING NUMBER 400386

SHEET 4 OF 8

W 1164

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D1	200001	75mA. 75V GP SL DIODE	FAIRCHILD	IN414B	5
D2		NOT USED			–
D3	200001	75mA. 75V GP SL DIODE	FAIRCHILD	IN414B	–
D4		NOT USED			–
D5	200001	75mA. 75v GP Si DIODE	FAIRCHILD	IN4148	–
D6	200001	75mA 75v GP Si DIODE	FAIRCHILD	IN4148	–
D7	200001	75mA 75v GP Si DIODE	FAIRCHILD	IN4148	–
Q1	240001	SL NPN TRANSISTOR	NATIONAL	BC184/TO18	2
Q2		NOT USED			–
Q3	250001	SL PNP TRANSISTOR	NATIONAL	BC214/TO18	1
Q4		NOT USED.			–
Q5		NOT USED			–
Q6	240001	Si NPN TRANSISTOR	NATIONAL	BC184/TO18	–
M1	280011	DUAL D FLIP-FLOP	MOTOROLA	MC14013 BCP	1
J1	604036	CON. PIN STRIP OF 10 HORIZ TYPE	AMP	163740-8	2
J2, J3	605052	8WAY POLARISED SOCKET	MOLEX	(22-01-2085) 6471-8-1	2

NOTES

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ISS	
E.C.O	
DATE	
CHKD	

DATE 9 MAY 79
DRAWN W.G.SMITH
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APPROVED
DATE 17 MAY 79

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TITLE REAR INPUT ASSY. 1061/71
DRAWING NUMBER 400386
SHEET 5 OF 8

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
J4 & J6	604033	FLAT WAFER PIN (4 WAY GOLD)	MOLEX	22-27-2041/GOLD	3
J5	605051	4WAY POLARISED SOCKET	MOLEX	22-01-2045	1
	400379/4	WIRE/TERMINAL ASSY	HOLDEN CORDS		6
	400379/5	WIRE/TERMINAL ASSY	HOLDEN CORDS		4.
RL1	330018	RELAY 2P2W 7v HOLD-IN	P&B	SEE DRAWING	1
RL2	330019	RELAY 4P2W 7v HOLD-IN	P&B	SEE DRAWING	1
RL3 & RL4		NOT USED.			
	410106-5	COMPONENT P.C.B			1
	410132-4	RELAY P.C.B.			1
	450257-1	SOCKET PLATE			1
	450241-1	RELAY BRACKET			1
	540002	22SWG TINNED COPPER WIRE			A/R
	540008	7/.2 PTFE INS. WHITE WIRE			140mm

NOTES

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DATE 17 MAY 79

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TITLE REAR INPUT ASSY 1061/71
DRAWING NUMBER 400386
SHEET 6 OF 8

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
	590001	SLEEVE. NAX CABLE Ø 3.0mm	HELLERMAN ELECTRIC	HSB×20mm BLK HELSYN	17
	590004	SLEEVE — PTFE	HELLERMAN ELECTRIC	FE10	30mm
J11	604008	7 WAY PLUG PANEL MTG	PYE CONNECTORS	[illegible]	1
	605009	7 WAY SOCKET	PYE CONNECTORS	MTS	1
	605057	CRIMP TERMINAL	MOLEX	4209 [illegible]	7
	606001	LOCKING HOOD	PYE CONNECTORS	[illegible]	1
	606002	NUT	PYE CONNECTORS	[illegible]	1
	606003	WASHER	PYE CONNECTORS	[illegible]	1
	605060	14 WAY DIL SOCKET	AUSTRALIK OR JERMYN	ICN-246-SAT or A2S-2025Y	1
	611004	SCREW M3×6mm STEEL POZI PAN	GKN	[illegible] PLATED	7
	611007	SCREW M3×6mm STEEL POZI CSK	GKN	[illegible] PLATED	1
	611016	SCREW M3×8mm STEEL POZI PAN	GKN	[illegible] PLATED	4
	612020	STAND-OFF NYLON M3×10 TRANSPILLAR	W K ELECTRONICS	TPI/6.5/19/M3/I/I	5

NOTES

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ISS			
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DATE			
CHKD			

DATE 9 MAY 79
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DATE 17 MAY 79

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TITLE REAR INPUT ASSY
1061/71
DRAWING NUMBER 400386
SHEET 7 OF 8

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
	613005	WASHER M3 INT SHAKE PROOF ST	GKN DISTRIBUTORS	[illegible] PLATED	3
	615001	NUT BSA FULL HEX STEEL		[illegible] PLATED	2
	615002	NUT M3 FULL HEX STEEL		[illegible] PLATED	2
	700069	DPDT SLIDE SWITCH	WAYCOM	S-5022 CDD3-0+¾ TRIGGER & STYLE 2 C PCB MOUNT CONTACTS	1

NOTES.

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ISS			
ECO			
DATE			
CHKD			

DATE 9 MAY 79
DRAWN W.G.SMITH
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DATE 17 MAY 79

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TITLE REAR INPUT ASSY.
1061/71
DRAWING NUMBER 400386
SHEET 8 OF 8

JW 1164

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R1	090054	8k2 ·25% 10ppm MF	ACI	SEE DRG	1
R2	011181	1K18 1% 1/8W 50ppm MF	HOLCO	H8C	1
R3	015900	590R 1% 1/8W 50ppm MF	HOLCO	H8C	1
R4	012940	294R 1% 1/8W 50ppm MF	HOLCO	H8C	2
R5	011470	147R 1% 1/8W 50ppm MF	HOLCO	H8C	1
R6	090054	16k ·25% 10ppm MF	ACI	SEE DRG	–
R7 *	000434	430K 5% 1/4W CARBON	MULLARD	CR25	1
R8	000394	390k 5% 1/4W CARBON	MULLARD	CR25	1
R9	000103	10k 5% 1/4W CARBON	MULLARD	CR25	5
R10	000155	1M5 5% 1/4W CARBON	MULLARD	CR25	2
R11	000105	1M 5% 1/4W CARBON	MULLARD	CR25	5
R12	000152	1k5 5% 1/4W CARBON	MULLARD	CR25	1
R13	000224	220k 5% 1/4W CARBON	MULLARD	CR25	1
R14	000333	33k 5% 1/4W CARBON	MULLARD	CR25	3
R15	000104	100k 5% 1/4W CARBON	MULLARD	CR25	6
R16	000685	6M8 5% 1/4W CARBON	MULLARD	CR25	1
R17	000332	3k3 5% 1/4W CARBON	MULLARD	CR25	5
R18	090053	100k ·25% 10ppm MF	ACI	SEE DRG	–
R19	090053	100k ·25% 10ppm MF	ACI	SEE DRG	–
R20	000274	270K CARBON (DO NOT SOLDER)	MULLARD	CR25	1
R21	000821	820R 5% 1/4W CARBON	MULLARD	CR25	1
R22	290026	RMS KIT	DATRON	SEE DRG	1
R23	000270	27R 5% 1/4W CARBON	MULLARD	CR25	1

NOTES. * NOT USED ON ASSEMBLIES FITTED INTO 1061's

SEE SHEET 2 FOR LATEST ISSUE

ISS	22	22
ECO	1730	1801
DATE	23.10.84	21.85
CHKD		

DATE 17.8.79 | DRAWN IL | CHECKED RJW | APPROVED | DATE

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TITLE AC PCB ASSY

DRAWING NUMBER 400402 | SHEET 2 OF 17

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER		
R24	000107	100M 10% 1/4W CARBON	ALLEN BRADLEY		1
R25	000331	330R 5% 1/4W CARBON	MULLARD		2
R26	000154	150k 5% 1/4W CARBON	MULLARD		2
R27	000334	330k 5% 1/4W CARBON	MULLARD		1
R28	290026	RMS KIT	DATRON		–
R29	000221	220R 5% 1/4W CARBON	MULLARD		4
R30	000680	68R 5% 1/4W CARBON	MULLARD		3
R31	000561	560R 5% 1/4W CARBON	MULLARD		2
R32	000182	1K8 5% 1/4W CARBON	MULLARD		2
R33	000333	33k 5% 1/4W CARBON	MULLARD		–
R34	000103	10k 5% 1/4W CARBON	MULLARD		–
R35	063500	50R POT 3/8" SQ. CERMET	BECKMAN		2
R36	049093	909K 1% 1/2W 100ppm C.F.	ALLEN BRADLEY		1
R37	000222	2k2 5% 1/4W CARBON	MULLARD		3
R38	000124	120k 5% 1/4W CARBON	MULLARD		1
R39	000104	100k 5% 1/4W CARBON	MULLARD		–
R40	000103	10k 5% 1/4W CARBON	MULLARD		–
R41	000332	3k3 5% 1/4W CARBON	MULLARD		–
R42	000332	3k3 5% 1/4W CARBON	MULLARD		–
R43	000332	3k3 5% 1/4W CARBON	MULLARD		–
R44	000331	330R 5% 1/4W CARBON	MULLARD		–
R45	000680	68R 5% 1/4W CARBON	MULLARD		–
R46	000472	4K7 5% 1/4W CARBON	MULLARD		1

NOTES

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R47	090051	4K7 ·25% 10ppm MF	ACI	SEE DRG	1
R48	090053	8K975·25% 10ppm MF	ACI	SEE DRG	—
R49	000912	9K1 5% 1/4W CARBON	MULLARD	CR25	1
R50	063500	50R POT 3/8" SQ. CERMET	BECKMAN	72P	—
R51	000107	100M 10% 1/4W CARBON	ALLEN BRADLEY	CB10	—
R52	000122	1K2 5% 1/4W CARBON	MULLARD	CR25	1
R53	000333	33K 5% 1/4W CARBON	MULLARD	CR25	—
R54	000123	12K 5% 1/4W CARBON	MULLARD	CR25	1
R55	000681	680R 5% 1/4W CARBON	MULLARD	CR25	1
R56	000103	10K 5% 1/4W CARBON	MULLARD	CR25	—
R57	000105	1M 5% 1/4W CARBON	MULLARD	CR25	—
R58	000104	100K 5% 1/4W CARBON	MULLARD	CR25	—
R59	000120	12R 5% 1/4W CARBON	MULLARD	CR25	2
R60	000120	12R 5% 1/4W CARBON	MULLARD	CR25	—
R61	000222	2K2 5% 1/4W CARBON	MULLARD	CR25	—
R62	000332	3K3 5% 1/4W CARBON	MULLARD	CR25	—
R63	008060	680R 5% 1/5W CARBON	MULLARD	CR16	1
R64	000222	2K2 5% 1/4W CARBON	MULLARD	CR25	—
R65	000101	100R 5% 1/4W CARBON	MULLARD	CR25	4
R66	000221	220R 5% 1/4W CARBON	MULLARD	CR25	—
R67	000183	18K 5% 1/4W CARBON	MULLARD	CR25	1
R68	090053	18K ·25% 10ppm MF	ACI	SEE DRG	—
R69	090051	4K7 ·25% 10ppm MF	ACI	SEE DRG	—

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R70	000272	2K7 5% 1/4W CARBON	MULLARD	CR25	2
R71	000272	2K7 5% 1/4W CARBON	MULLARD	CR25	—
R72	000330	33R 5% 1/4W CARBON	MULLARD	CR25	1
R73	000105	1M 5% 1/4W CARBON	MULLARD	CR25	—
R74	000824	820k 5% 1/4W CARBON	MULLARD	CR25	1
R75	063504	500k POT 3/8"SQ CERMET	BECKMAN	72P	2
R76	090067	62K6 ·25% 10ppm MF	ACI	SEE DRG	1
R77	000104	100k 5% 1/4W CARBON	MULLARD	CR25	—
R78	000225	2M2 5% 1/4W CARBON	MULLARD	CR25	2
R79	011103	110k 1% 1/8W MF	HOLCO	H8C	1
R80	000561	560R 5% 1/4W CARBON	MULLARD	CR25	—
R81	000101	100R 5% 1/4W CARBON	MULLARD	CR25	—
R82	000101	100R 5% 1/4W CARBON	MULLARD	CR25	—
R83	000104	100K 5% 1/4W CARBON	MULLARD	CR25	—
R84	000184	180k 5% 1/4W CARBON	MULLARD	CR25	3
R85	000184	180k 5% 1/4W CARBON	MULLARD	CR25	—
R86	000103	10K 5% 1/4W CARBON	MULLARD	CR25	—
R87	000153	15K 5% 1/4W CARBON	MULLARD	CR25	3
R88	000680	68R 5% 1/4W CARBON	MULLARD	CR25	—
R89	000221	220R 5% 1/4W CARBON	MULLARD	CR25	—
R90	063204	200k POT 3/8" SQ. CERMET	BECKMAN	72P	1
R91	000105	1M 5% 1/4W CARBON	MULLARD	CR25	—
R92	000105	1M 5% 1/4W CARBON	MULLARD	CR25	—

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R93	000225	2M2 5% ¼W CARBON	MULLARD	CR25	–
R94	000564	560K 5% ¼W CARBON	MULLARD	CR25	2
R95	090052	9k ·25% 10ppm MF	ACI	SEE DRG	1
R96	090052	1k ·25% 10ppm MF	ACI	SEE DRG	–
R97	000101	100R 5% ¼W CARBON	MULLARD	CR25	–
R98	000471	470R 5% ¼W CARBON	MULLARD	CR25	1
R99	000182	1k8 5% ¼W CARBON	MULLARD	CR25	–
R100	000184	180k 5% ¼W CARBON	MULLARD	CR25	–
R101	012940	294R 1% ⅛W MF	HOLCO	H8C	–
R102	090067	62k6 ·25% 10ppm MF	ACI	SEE DRG	–
R103	000221	220R 5% ¼W CARBON	MULLARD	CR25	–
R104	000154	150K 5% ¼W CARBON	MULLARD	CR25	–
R105	000564	560K 5% ¼W CARBON	MULLARD	CR25	–
R106	011001	1k00 1% ⅛W 50ppm MF	HOLCO	H8C	3
R107	011823	182k 1% ⅛W 50ppm MF	HOLCO	H8C	
R108	042215	22M1 1% ½W 100ppm MF	ALLEN BRADLEY	CC	2
R109	090066	1k ·25% 10ppm MF	ACI	SEE DRG	–
R110	090066	10k1 ·25% 10ppm MF	ACI	SEE DRG	–
R111	090066	111k ·25% 10ppm MF	ACI	SEE DRG	–
R112	063504	500k POT 3/8" SQ CERMET	BECKMAN	72P	–
R113	042215	22M1 1% ½W 100ppm MF	ALLEN BRADLEY	CC	–
R114	090066	1M ·25% 10ppm MF	ACI	SEE DRG	–
R115	000153	15k 5% ¼W CARBON	MULLARD	CR25	–

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R116	000336	33M 10% ¼W CARBON	ALLEN BRADLEY	CB10	1
R117	000102	1k 5% ¼W CARBON	MULLARD	CR25	2
R118	000155	1M5 5% ¼W CARBON	MULLARD	CR25	–
R119	000107	100M 10% ¼W CARBON	ALLEN BRADLEY	CB10	–
R120	000102	1k 5% ¼W CARBON	MULLARD	CR25	–
R121	063105	1M POT 3/8" SQ CERMET	BECKMAN	72P	1
R122	000104	100k 5% ¼W CARBON	MULLARD	CR25	–
R123	090066	277k ·25% 10ppm MF	ACI	SEE DRG	–
R124	090066	277k ·25% 10ppm MF	ACI	SEE DRG	–
R125	090066	277k ·25% 10ppm MF	ACI	SEE DRG	–
R126	090066	277k ·25% 10ppm MF	ACI	SEE DRG	–
R127		NOT USED			–
R128	000476	47M 10% ¼W CARBON	ALLEN BRADLEY	CB10	1
R129	008059	820R 5% ½W CARBON	MULLARD	CR16	1
R130		NOT USED			–
R131		" "			–
R132		" "			–
R133	011001	1k00 1% ⅛W 50ppm M/F	HOLCO	H8C	–
R134	011001	1k00 1% ⅛W 50ppm M/F	HOLCO	H8C	–
R135		NOT USED			–
R136	000153	15k 5% ¼W CARBON	MULLARD	CR25	–

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
C1	120018	1μ5F 10% 63v POLYCARB	ASHCROFT	A2B1521B	1
C2	120030	820nF 10% 63v POLYCARB	ASHCROFT	SEE DRG.	1
C3	120021	470nF 10% 63v POLYCARB	ASHCROFT	A2B4711B	2
C4	120024	6μ8F 10% 63v POLYCARB	ASHCROFT	A2B6821B	1
C5	120020	220nF 10% 63v POLYCARB	ASHCROFT	A2B2211B	1
C6	150012	100nF 20% 35v DIP TANT	UNION CARBIDE	KR10E35	1
C7	120021	470nF 10% 63v POLYCARB	ASHCROFT	A2B4711B	–
C8	102680	68pF 5% 500v CER DISC	ITT	CD10	1
C9	102101	100pF 10% 500v CER DISC	ITT	CD10	2
C10	102471	470pF 10% 500v CER DISC	ITT	CD10	1
C11	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	8
C12	110013	100nF 20% 250v POLYESTER	MULLARD	C280AEP100k	5
C13	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C14	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C15	101103	10nF 25% 250v CER DISC	ITT	CD10	7
C16	150003	47μF 20% 6V3 DIP TANT	UNION CARBIDE	K47E6V3	1
C17	110013	100nF 20% 250v POLYESTER	MULLARD	C280AEP100k	–
C18	102108	1pF ±·5pF 500v CER DISC	ITT	CD06	1
C19	102470	47pF 5% 500v CER DISC	ITT	CD10	2
C20	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C21	110013	100nF 20% 250v POLYESTER	MULLARD	C280AEP100k	–
C22	102100	10pF 5% 500v CER DISC	ITT	CD10	3
C23	102100	10pF 5% 500v CER DISC	ITT	CD10	–

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
C24	102478	4p7F ±·5pF 500v CER DISC	ITT	CD08	2
C25	102478	4p7F ±·5pF 500v CER DISC	ITT	CD08	–
C26	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C27	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C28	150016	1μF 20% 35v DIP TANT	UNION CARBIDE	K1R0E35	1
C29	130071	150pF 1% 160v POLYSTYRENE	SUFLEX	HSQ150/1–7/160	2
C30	130013	18pF ±1pF 160v POLYSTYRENE	SUFLEX	HS18/1–7/160	1
C31	110013	100nF 20% 250v POLYESTER	MULLARD	C280AEP100k	–
C32	110035	220nF 20% 63V POLYESTER	WIMA	MKS2 MIN	2
C33	110035	220nF 20% 63v POLYESTER	WIMA	MKS2 MIN	–
C34	102470	47pF 5% 500V CER DISC	ITT	CD10	–
C35	120022	1nSF 20% 100v POLYCARB	WIMA	FKC2 MIN	2
C36	120022	1nSF 20% 100v POLYCARB	WIMA	FKC2 MIN	–
C37	102101	100pF 10% 500v CER DISC	ITT	CD10	–
C38	102100	10pF 5% 500v CER DISC	ITT	CD10	–
C39	102331	330pF 10% 500v CER DISC	ITT	CD10	1
C40	150020	10μF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C41	101103	10nF 25% 250v CER DISC	ITT	CD10	–
C42	130072	9p1F ±·5pF 160v POLYSTYRENE	SUFLEX	HS 9·1/·5–7/160	1
C43	130071	150pF 1% 160v POLYSTYRENE	SUFLEX	HSQ150/1–7/160	–
C44	150023	33μF 20% 25v DIP TANT	UNION CARBIDE	K33E25	1
C45	102150	15pF 5% 500v CER DISC	ITT	CD10	1
C46	102120	12pF 5% 500v CER DISC	ITT	CD10	1

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
C47	101103	10nF 25% 250v CER DISC	ITT	CD10	–
C48	140035	10nF 1% 125v SILV MICA	ITT	454/274	1
C49	140034	1nF 1% 300v GLASS	ELECTROSIL	CYFM1S	1
C50	110013	100nF 20% 250v POLYESTER	MULLARD	C280AE P100K	–
C51	140033	91pF 1% 500v GLASS	ELECTROSIL	CYFM10	1
C52	110026	6n8F 20% 100v POLYESTER	WIMA	FKS2 MIN	1
C53	101103	10nF 25% 250v CER DISC	ITT	CD10	–
C54	101103	10nF 25% 250v CER DISC	ITT	CD10	–
C55	140039	15pF 5% 500v GLASS	ELECTROSIL	CYFM10	3
C56	150020	10µF 20% 25v DIP TANT	UNION CARBIDE	K10E25	–
C57	120001	220nF 10% 1kV POLYCARB	SUFLEX	SN1380	1
C58	140031	13pF 5% 500v GLASS	ELECTROSIL	CYFM10	1
C59	140039	15pF 5% 500v GLASS	ELECTROSIL	CYFM10	–
C60	140039	15pF 5% 500v GLASS	ELECTROSIL	CYFM10	–
C61	140008	10pf 1kV TRIMMER	JACKSON	TETFER VPC	1
C62	140036	25pF 1kV TRIMMER	JACKSON	TETFER VPC S646	2
C63	140036	25pF 1kV TRIMMER	JACKSON	TETFER VPC S646	–
C64	102228	2p2F ±·5pF 500v CER DISC	ITT	CD08	1
C65		NOT USED			–
C66	102220	22pF 5% 500v CER DISC	ITT	CD[illegible]	1
C67		NOT USED			–
C68	101103	10nF 25% 250v CER DISC	ITT	CD10	–
C69	101103	10nF 25% 250v CER DISC	ITT	CD10	–

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
C70	102330	33pF 5% 500v CER DISC	ITT	CD10	1
C71		NOT USED			–
C72		NOT USED			–
C73		NOT USED			–
C74	102332	3n3F 20% 500V CER DISC	ITT	CD10	1
C75	102102	1nF 10% 500V CER DISC	ITT	CD10	2
C76	102102	1nF 10% 500V CER DISC	ITT	CD10	–
C77	100828	8p2F ±·25pF 100V CER DISC	MULLARD	2222 683	1

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D1	220010	Si HOT CARRIER DIODE	HP	HSCH1001/IN6263	4
D2	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	5
D3	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	–
D4	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	–
D5	210100	10v 400mW ZENER	MULLARD	BZY88C10	2
D6	200008	200mA 125v LL Si DIODE	FAIRCHILD	IN458A	–
D7	210100	10v 400mW ZENER	MULLARD	BZY88C10	–
D8	220010	Si HOT CARRIER DIODE	HP	HSCH1001/IN6263	–
D9	220010	Si HOT CARRIER DIODE	HP	HSCH1001/IN6263	–
D10		NOT USED			–
D11	220021	QUAD 29pF VARICAP DIODE	THOMPSON-CSF	BB109G4	SET OF 4
D12	200008	200mA 125v LL Si DIODE	FAIRCHILD	IN458A	–
D13	220010	Si HOT CARRIER DIODE	HP	HSCH1001/IN6263	–
D14	220020	FET DIODE 100pA IR	TELEDYNE	PAD100	3
D15	220020	FET DIODE 100pA IR	TELEDYNE	PAD100	–
D16	200001	75mA 75v GP Si DIODE	FAIRCHILD	IN4148	1
D17	220020	FET DIODE 100pA IR	TELEDYNE	PAD100	–

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
Q1	230002	N-CHAN JFET	TELEDYNE	U1994JF	5
Q2	230002	N-CHAN JFET	TELEDYNE	U1994JF	–
Q3	230027	N-CHAN JFET	TELEDYNE	U3114JF	1
Q4	240013	Si NPN TRANSISTOR	NATIONAL	BC184C/TO18	4
Q5	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	7
Q6	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	7
Q7	240013	Si NPN TRANSISTOR	NATIONAL	BC184C/TO18	–
Q8	250008	Si PNP TRANSISTOR	NATIONAL	BC214C/TO18	6
Q9	230001	N-CHAN CURRENT LIM	SILICONIX	E506	1
Q10	250008	Si PNP TRANSISTOR	NATIONAL	BC214C/TO18	–
Q11	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	–
Q12	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	–
Q13	250008	Si PNP TRANSISTOR	NATIONAL	BC214C/TO18	–
Q14	240013	Si NPN TRANSISTOR	NATIONAL	BC184C/TO18	–
Q15	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	–
Q16	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	–
Q17	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	–
Q18	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	–
Q19	230035	N-CHAN JFET	TELEDYNE	U1897JF	1
Q20	250008	Si PNP TRANSISTOR	NATIONAL	BC214C/TO18	–
Q21	250008	Si PNP TRANSISTOR	NATIONAL	BC214C/TO18	–
Q22	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	–
Q23	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	–

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No USED Per Assy.
Q24	240006	Si NPN TRANSISTOR	NATIONAL	2N3904 / TO18	–
Q25	250004	Si PNP TRANSISTOR	NATIONAL	2N3906 / TO18	–
Q26	240006	Si NPN TRANSISTOR	NATIONAL	2N3904 / TO18	–
Q27	250008	Si PNP TRANSISTOR	NATIONAL	BC214C / TO18	–
Q28	240013	Si NPN TRANSISTOR	NATIONAL	BC184C / TO18	–
Q29	240006	Si NPN TRANSISTOR	NATIONAL	2N3904 / TO18	–
Q30	230003	N-CHAN JFET	TELEDYNE	U1899 JF	1
Q31	230002	N-CHAN JFET	TELEDYNE	U1994 JF	–
Q32	230031	N-CHAN DUAL JFET	TELEDYNE	SU2656M	2
Q33	240019	Si NPN DUAL TRANSISTOR	PMI	MAT01H	1
Q34	230031	N-CHAN DUAL JFET	TELEDYNE	SU2656M	–
Q35	230002	N-CHAN JFET	TELEDYNE	U1994 JF	–
Q36	230002	N-CHAN JFET	TELEDYNE	U1994 JF	–
Q37	230074	P-CHAN JFET	SILICONIX	J271	1

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No Per Assy.
M1	260027	714 OP AMP	FAIRCHILD	µA714HC	5
M2	260027	714 OP AMP	FAIRCHILD	µA714HC	–
M3	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	2
M4	280015	QUAD D-TYPE LATCH	MOTOROLA	MC14076BCP	–
M5	280011	DUAL D FLIP-FLOP	MOTOROLA	MC14013BCP	1
M6	290026	RMS KIT	DATRON	SEE DRG	–
M7	290077	7 x DARLINGTON DRIVER	[illegible]	ULN2004A / xR2204CP	1
M8	260027	714 OP AMP	FAIRCHILD	µA714HC	–
M9	260027	714 OP AMP	FAIRCHILD	µA714HC	–
M10	290066	FREQ SENSITIVE SWITCH	CONSUMER MICROCIRCUITS	FX301L	1
M11	260027	714 OP AMP	FAIRCHILD	µA714HC	–
RL1	330012-2	RELAY REED 1A GUARDED	HAMLIN	HE721A5134	4
RL2	330018	RELAY 2P2W 7v HOLD-IN	P & B	SEE DRAWING	2
RL3	330018	RELAY 2P2W 7v HOLD-IN	P & B	SEE DRAWING	–
RL4	330012-2	RELAY REED 1A GUARDED	HAMLIN	HE721A5134	–
RL5	330012-2	RELAY REED 1A GUARDED	HAMLIN	HE721A5134	–
RL6	330012-2	RELAY REED 1A GUARDED	HAMLIN	HE721A5134	–

NOTES.

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TITLE AC PCB ASSY

DRAWING NUMBER 400402 | SHEET 15 OF 17

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
TP's, TL's.	540002	22 SWG. TINNED COPPER WIRE			A/R
	590004	SLEEVE — PTFE	HELLERMANN ELECTRIC	FE10	A/R
J1	571095\C	16 WAY AP/3M RIBBON CABLE	DATRON		1
J2	605052	8 WAY POLARISED SOCKET	MOLEX	(22-01-2085) 6471-8-1	1
	590055	SLEEVE Ø1·0 SIL. RUBBER	HELLERMANN ELECTRIC	HIS CONT. BLACK	50 mm
	400379/1	WIRE/TERMINAL ASSY.			1
	410136-4	PCB			1
	450249-2	GUARD SHIELD			1
	459112-2	RELAY BRACKET			2
	605056	CRIMP TERMINAL	MOLEX	4809-TL	1
	512999	7/0·2 PTFE INSULATED (WHITE) WIRE			A/R
	590001	SLEEVE MAX. CABLE Ø3·0	HELLERMAN ELECTRIC	HIS x 20mm BLACK HELSYN	7
	590002	SLEEVE MAX. CABLE Ø6·0	HELLERMAN ELECTRIC	H30 x 25mm BLACK HELSYN	1
	602001	FSV TERMINAL	MOLEX	02-04-1875	2
	602004	BREAKAWAY TERMINAL STRIP	MOLEX	05-30-0001	16
	605060	14 PIN DIL SOCKET	ASTRALUX	ICL 143-S3T	1
	605061	16 PIN DIL SOCKET	ASTRALUX	ICL 163-S6T	3
	605057	CRIMP TERMINAL	MOLEX	4809-GL	1
	611007	SCREW M3 x 6 mm STEEL POZI-	CSK ZN/PLATED GKN		3
	611016	SCREW M3 x 8mm STEEL POZI-	PAN ZN/PLATED GKN		5

NOTES.

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TITLE AC PCB ASSY.

DRAWING NUMBER 400402

SHEET 16 OF 17

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
	612021	STANDOFF M3×16 HEX STEEL	HARWIN	R6077-M3	3
	613005	WASHER M3 INT./SHAKEPROOF ST.	GKN DISTRIBUTORS	ZINC PLATED	5
	613014	WASHER M2·5 INT/SHAKEPROOF ST.	GKN DISTRIBUTORS	ZINC PLATED	2
	615002	NUT M3 FULL HEX STEEL		ZINC PLATED	2
	615005	NUT 3-48 UNC FULL HEX.ST.		ZINC PLATED	2
	617010	NYLATCH PLUNGER HN3P	ORDER FROM C J FOX & SONS	HN3P-32-4-1	4
	617011	NYLATCH GROMMET HN3G	ORDER FROM C.J. FOX & SONS	HN3G-32-1	4
	620003	SOLDER PCB TERMINAL LUG	HARWIN	H 2105A	2
	620005	CLOVERLEAF PTFE TERMINAL	SEALECTRO	FTE 15 P20	15
	630107	BRASS STRIP ·375mm THK×15·5	RIGHTON	½ HARD	130mm
	620007	TEST POINT TERMINAL	MICROVAR	C30	6

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No USED Per Assy
R1	000104	100K 5% ¼W CARBON	MULLARD	CR25	3
R2	000103	10K 5% ¼W CARBON	MULLARD	CR25	1
R3	000104	100k 5% ¼W CARBON	MULLARD	CR25	–
R4	000104	100k 5% ¼W CARBON	MULLARD	CR25	–
R5	000102	1K 5% ¼W CARBON	MULLARD	CR25	2
R6	000561	560R 5% ¼W CARBON	MULLARD	CR25	1
R7	000102	1k 5% ¼W CARBON	MULLARD	CR25	–
R8	000332	3K3 5% ¼W CARBON	MULLARD	CR25	1
AN1	090017	100k x 7 2% NETWORK	BECKMAN	764-1-R100K	1
C1	150015	10μF 20% 35v DIP TANT	UNION CARBIDE	K10E35	3
C2	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	9
C3	150015	10μF 20% 35v DIP TANT	UNION CARBIDE	K10E35	–
C4	150015	10μF 20% 35v DIP TANT	UNION CARBIDE	K10E35	–
C5	150016	1μF 20% 35v DIP TANT	UNION CARBIDE	K1E35	
C6	150012	100nF 20% 35v DIP TANT	UNION CARBIDE	KR10E35	1
C7	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
C8	101103	10nF 25% 250V CER DISC	ITT	CD10	1
C9	102681	680pF 10% 500V CER DISC	ITT	CD10	1
C10	102101	100pF 10% 500V CER DISC	ITT	CD10	1
C11	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
C12	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
C13	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–

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1061/1065/1071/1081
IEEE PCB ASSY

400427 SHEET 2 OF 5

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	PART No	No USED Per Assy
C14	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
C15	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
C16	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
C17	104025	100nF +80% -20% 50V CER DISC	SIEMENS	B37449	–
M1	280086	BI-DIRECTIONAL BUS TRANSC'R	MOTOROLA	MC3447P	2
M2	280086	BI-DIRECTIONAL BUS TRANSC'R	MOTOROLA	MC3447P	–

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TITLE 1061/1065/1071/1081
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DRAWING NUMBER 400427 SHEET 3 OF 5

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
M3		FITTED AT FINAL ASSY.			—
M4		NOT USED			—
M5		NOT USED			—
M6	280024	TRI-STATE HEX. BUFFER	MOTOROLA	MC14503BCP	1
M7		NOT USED			—
M8	270050	HEX. INVERTER LS	NATIONAL	DM74LS04N	1
M9	280064	GPIA	MOTOROLA	MC68488P	1
M10	280068	DUAL PREC. M'STABLE M'VIBR.	MOTOROLA	MC14538BCP	1
M11	270055	DUAL 4 I/P NAND LS	NATIONAL	DM74LS20N	2
M12	270055	DUAL 4 I/P NAND LS	NATIONAL	DM74LS20N	—
M13	270051	DUAL 4 I/P AND LS	NATIONAL	DM74LS21N	1
J1	605102	24 WAY DIL. SOCKET GOLD	CA	CA-24S 105D	1
J2	605002	16 WAY DIL. LOW PROFILE SKT.	JERMYN OR ANTIFERENCE	A23-2001/Y OR ICN-63-S3	1
J3	573120/C	24 WAY AP/3M CABLE ASSY	DATRON		1
J4	605051	4 WAY POLARISED SOCKET	MOLEX	(22-01-2045) 6471-4-1	1
	400379/1	WIRE/TERMINAL ASSY			2
	410165-4A	PCB			1
	540002	22 SWG BTC WIRE			A/R
	590004	SLEEVE - PTFE	HELLERMANN ELECTRIC	FE10	A/R
	605060	14 WAY DIL. SOCKET	ASTRALUX OR JERMYN	ICL-143-S3T	4

NOTES

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SHEET 4 OF 5

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
	605061	16 WAY DIL. SOCKET	ASTRALUX OR JERMYN	ICL-163-S6T	2
	605050	40 PIN DIL. LOW PROF' SKT	AUGAT	340-AG39D	1
	605064	24 PIN DIL SOCKET	AUGAT	324-AG39D	3
	605056	CRIMP TERMINAL	MOLEX	4809-TL	2
	606005	CLIP FOR 605002	ANTIFERENCE	RC-74	1
	620007	TEST POINT TERMINAL	MICROVAR	C30	5
	900004	SILICONE RUBBER COMPOUND	RS	555-588	A/R

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SHEET 5 OF 5

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R1		NOT USED			—
R2		NOT USED			—
R3	000473	47k 5% ¼W CARBON	MULLARD	CR25	4
R4	000223	22k 5% ¼W CARBON	MULLARD	CR25	2
R5	080045-2	5k5 ·01% 3ppm M FOIL	VISHAY	SEE DRG	4
R6	000152	1k5 5% ¼W CARBON	MULLARD	CR25	1
R7	000473	47k 5% ¼W CARBON	MULLARD	CR25	—
R8	000473	47k 5% ¼W CARBON	MULLARD	CR25	—
R9	000103	10k 5% ¼W CARBON	MULLARD	CR25	3
R10	000333	33k 5% ¼W CARBON	MULLARD	CR25	2
R11	000105	1M 5% ¼W CARBON	MULLARD	CR25	7
R12	000103	10k 5% ¼W CARBON	MULLARD	CR25	—
R13	000155	1M5 5% ¼W CARBON	MULLARD	CR25	1
R14	000105	1M 5% ¼W CARBON	MULLARD	CR25	—
R15	000333	33k 5% ¼W CARBON	MULLARD	CR25	—
R16		NOT USED			—
R17		NOT USED			—
R18		NOT USED			—
R19		NOT USED			—
R20	000223	22k 5% ¼W CARBON	MULLARD	CR25	—
R21	000102	1k 5% ¼W CARBON	MULLARD	CR25	3
R22	000473	47k 5% ¼W CARBON	MULLARD	CR25	—
R23	000101	100R 5% ¼W CARBON	MULLARD	CR25	—

NOTES. CIRCUIT DIAGRAM = 430552
CHECK PROCEDURE = 460552
CHECK LIST = 470552

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TITLE 1061A AC PCB ASSY
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SHEET 2 OF 20

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R24	000562	5kG 5% ¼W CARBON	MULLARD	CR25	3
R25	000122	1k2 5% ¼W CARBON	MULLARD	CR25	1
R26	000102	1k 5% ¼W CARBON	MULLARD	CR25	—
R27		FSV			1
R28	290026	RMS KIT	DATRON	SEE DRG	1 KIT
R29	000120	12R 5% ¼W CARBON	MULLARD	CR25	1
R30	090111-1	100M 5% THICK FILM	HOLSWORTHY	SEE DRG	3
R31	000331	330R 5% ¼W CARBON	MULLARD	CR25	3
R32	012743	274k 1% ⅛W 50ppm MF	HOLCO	H8C	1
R33	011003	100k 1% ⅛W 50ppm MF	HOLCO	H8C	1
R34	290026	RMS KIT	DATRON	SEE DRG	—
R35	000221	220R 5% ¼W CARBON	MULLARD	CR25	5
R36	000680	68R 5% ¼W CARBON	MULLARD	CR25	3
R37	000271	270R 5% ¼W CARBON	MULLARD	CR25	2
R38	000271	270R 5% ¼W CARBON	MULLARD	CR25	—
R39	000224	220k 5% ¼W CARBON	MULLARD	CR25	1
R40	000104	100k 5% ¼W CARBON	MULLARD	CR25	9
R41	000103	10k 5% ¼W CARBON	MULLARD	CR25	—
R42	000104	100k 5% ¼W CARBON	MULLARD	CR25	—
R43	000104	100k 5% ¼W CARBON	MULLARD	CR25	—
R44		NOT USED			—
R45	016811	6k81 ⅛W 50ppm MF	HOLCO	H8C	1
R46	012742	27k4 1% ⅛W 50ppm MF	HOLCO	H8C	1

NOTES

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
R47		NOT USED			—
R48		NOT USED			—
R49		NOT USED			—
R50		NOT USED			—
R51		NOT USED			—
R52	080064-B	28k0 ·1% 3ppm M.FOIL	VISHAY	SEE DRG	1
R53	080065	1K60 ·1% 10ppm M.FOIL	VISHAY	VSRC1	1
R54	018250	825R 1% 50ppm M.F	HOLCO	H8C	1
R55	013920	392R 1% 50ppm M.F	HOLCO	H8C	1
R56	012000	200R 1% 50ppm M.F	HOLCO	H8C	1
R57	000182	1k8 5% ¼W CARBON	MULLARD	CR25	1
R58	000151	150R 5% ¼W CARBON	MULLARD	CR25	4
R59	000752	7k5 5% ¼W CARBON	MULLARD	CR25	1
R60	000100	10R 5% ⅓W CARBON	MULLARD	CR25	3
R61	063100	10R POT ⅜ SQ. CERMET	BECKMAN	72P	4
R62	063100	10R POT ⅜ SQ. CERMET	BECKMAN	72P	2
R63	000100	10R 5% ¼W CARBON	MULLARD	CR25	—
R64	012003	200k 1% ⅛W 50ppm MF	HOLCO	H8C	—
R65	000124	120k 5% ¼W CARBON	MULLARD	CR25	1
R66		NOT USED			1
R67		NOT USED			—
R68		NOT USED			—
R69	000103	10k 5% ¼W CARBON	MULLARD	CR25	—

NOTES

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SHEET 4 OF 20

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
R70	080045-2	5k5 ·01% 3ppm M. FOIL	VISHAY	SEE DRG	—
R71	000331	330R 5% ¼W CARBON	MULLARD	CR25	—
R72	000680	68R 5% ¼W CARBON	MULLARD	CR25	—
R73	000562	5K6 5% ¼W CARBON	MULLARD	CR25	—
R74	080045-2	5k5 ·01% 3ppm M.FOIL	VISHAY	SEE DRG	—
R75	080044-2	4k9925 ·01% 3ppm M.FOIL	VISHAY	SEE DRG	1
R76	014991	4k99 1% ⅛W 50ppm MF	HOLCO	H8C	1
R77	090111-1	100M 5% THICK FILM	HOLSWORTHY	SEE DRG	—
R78	000182	1k8 5% ¼W CARBON	MULLARD	CR25	—
R79	000752	7k5 5% ¼W CARBON	MULLARD	CR25	—
R80	000471	470R 5% ¼W CARBON	MULLARD	CR25	1
R81	000105	1M 5% ¼W CARBON	MULLARD	CR25	—
R82	000475	4M7 5% ¼W CARBON	MULLARD	CR25	1
R83		NOT USED			—
R84	000103	10k 5% ¼W CARBON	MULLARD	CR25	—
R85	000105	1M 5% ¼W CARBON	MULLARD	CR25	—
R86	013323	332k 1% ⅛W 50ppm MF	HOLCO	H8C	1
R87	011503	150k 1% ⅛W 50ppm MF	HOLCO	H8C	1
R88	000105	1M 5% ¼W CARBON	MULLARD	CR25	—
R89	000104	100k 5% ¼W CARBON	MULLARD	CR25	—
R90	000182	1k8 5% ¼W CARBON	MULLARD	CR25	—
R91	000101	100R 5% ¼W CARBON	MULLARD	CR25	—
R92	000221	220R 5% ¼W CARBON	MULLARD	CR25	—

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R93	000221	220R 5% ¼W CARBON	MULLARD	CR25	—
R94	011002	10kO 1% ⅛W 50ppm MF	HOLCO	H8C	1
R95	000272	2k7 5% ¼W CARBON	MULLARD	CR25	2
R96	000272	2k7 5% ¼W CARBON	MULLARD	CR25	—
R97	080047-2	10k ·01% 3ppm M FOIL	VISHAY	SEE DRG	1
R98	080045-2	5kS ·01% 3ppm M FOIL	VISHAY	SEE DRG	—
R99	000101	100R 5% ¼W CARBON	MULLARD	CR25	—
R100	000335	3M3 5% ¼W CARBON	MULLARD	CR25	1
R101	063105	1M POT ⅜ SQ CERMET	BECKMAN	72P	1
R102	000225	2M2 5% ¼W CARBON	MULLARD	CR25	1
R103	012001	2k00 1% ⅛W 50ppm MF	HOLCO	H8C	4
R104	012001	2k00 1% ⅛W 50ppm	HOLCO	H8C	—
R105	000221	220R 5% ¼W CARBON	MULLARD	CR25	—
R106	000104	100k 5% ¼W CARBON	MULLARD	CR25	—
R107		NOT USED			—
R108		NOT USED			—
R109		NOT USED			—
R110		NOT USED			—
R111		NOT USED			—
R112	013923	392k 1% ⅛W 50ppm MF	HOLCO	H8C	2
R113	013923	392k 1% ⅛W 50ppm MF	HOLCO	H8C	—
R114	000104	100k 5% ¼W CARBON	MULLARD	CR25	—
R115	000222	2k2 5% ¼W CARBON	MULLARD	CR25	3

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TITLE 1061A 1062 AC PCB ASSY

DRAWING NUMBER 400552 SHEET 6 OF 20

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
R116	000752	7kS 5% ¼W CARBON	MULLARD	CR25	—
R117	000680	68R 5% ¼W CARBON	MULLARD	CR25	—
R118	000221	220R 5% ¼W CARBON	MULLARD	CR25	—
R119	063204	200k POT ⅜ SQ CERMET	BECKMAN	72P	1
R120	000274	270k 5% ¼W CARBON	MULLARD	CR25	1
R121	080043-2	1k ·1% 3ppm M FOIL	VISHAY	SEE DRG	2
R122	080046-2	9k ·1% 3ppm M FOIL	VISHAY	SEE DRG	1
R123	000103	10k 5% ¼W CARBON	MULLARD	CR25	—
R124		NOT USED			—
R125	012001	2k00 1% ⅛W 50ppm MF	HOLCO	H8C	1
R126	000102	1k 5% ¼W CARBON	MULLARD	CR25	—
R127	000682	6k8 5% ¼W CARBON	MULLARD	CR25	2
R128	000101	100R 5% ¼W CARBON	MULLARD	CR25	—
R129	000331	330R 5% ¼W CARBON	MULLARD	CR25	—
R130	000182	1k8 5% ¼W CARBON	MULLARD	CR25	—
R131	080050-2	62kG ·1% 3ppm M FOIL	VISHAY	SEE DRG	2
R132	080050-2	62kG ·1% 3ppm M FOIL	VISHAY	SEE DRG	—
R133	000330	33R 5% ¼W CARBON	MULLARD	CR25	1
R134	000104	100k 5% ¼W CARBON	MULLARD	CR25	—
R135	000103	10k 5% ¼W CARBON	MULLARD	CR25	—
R136	018251	8k25 1% ⅛W 50ppm MF	HOLCO	H8C	1
R137		NOT USED			—
R138		NOT USED			—

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TITLE 1061A 1062 AC PCB ASSY

DRAWING NUMBER 400552 SHEET 7 OF 20

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
R139	011822	18k2 1% 1/8W 50ppm MF	HOLCO	H8C	2
R140	014321	4k32 1% 1/8W 50ppm MF	HOLCO	H8C	1
R141		NOT USED			—
R142	080043-2	1k ·1% 3 ppm M.FOIL	VISHAY	SEE DRG.	—
R143	080048-2	10k1 ·1% 3 ppm M.FOIL	VISHAY	SEE DRG.	1
R144	080051-2	111k ·1% 3 ppm M.FOIL	VISHAY	SEE DRG	1
R145	000474	470K 5% 1/3W CARBON	MULLARD	CR25	1
R146	080062	1M ·1% 5 ppm M.FILM	VTM	MAR7-TIG-1M-0.1%	1
R147	000101	100R 5% 1/4W CARBON	MULLARD	CR25	—
R148	063104	100k POT 3/8 SQ. CERMET	BECKMAN	72P	1
R149	011822	18k2 1% 1/8W 50ppm MF	HOLCO	H8C	—
R150	000100	10R 5% 1/4W CARBON	MULLARD	CR25	—
R151	000100	10R 5% 1/4W CARBON	MULLARD	CR25	—
R152		NOT USED			—
R153	000104	100k 5% 1/4W CARBON	MULLARD	CR25	—
R154	013320	332R 1% 1/8W 50ppm MF	HOLCO	H8C	1
R155	041004	1M 1% 1/2W 100ppm CF	ALLEN BRADLEY	CC	1
R156	011000	100R 1% 1/8W 50ppm MF	HOLCO	H8C	1
R157	000105	1M 5% 1/4W CARBON	MULLARD	CR25	—
R158	090111-1	100M 5% THICK FILM	HOLSWORTHY	SEE DRG	—
R159	000241	240R 5% 1/4W CARBON	MULLARD	CR25	1
R160	012001	2k00 1% 1/8W 50ppm MF	HOLCO	H8C	—
R161	000101	100R 5% 1/4W CARBON	MULLARD	CR25	—

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
R162	012151	2k15 1% 1/8W 50ppm MF	HOLCO	H8C	1
R163	000912	9k1 5% 1/4W CARBON	MULLARD	CR25	1
R164	008047	470R 1% 1/2W MET-GLAZE	NEOHM	RGP0207	1
R165	008049	100k 1% 1/2W MET-GLAZE	NEOHM	RGP0207	1
R166	008048	560R 1% 1/2W MET-GLAZE	NEOHM	RGP0207	1
R167	080052-2	277k ·1% 3 ppm M.FOIL	VISHAY	SEE DRG.	4
R168	080052-2	277k ·1% 3 ppm M.FOIL	VISHAY	SEE DRG	—
R169	080052-2	277k ·1% 3 ppm M.FOIL	VISHAY	SEE DRG	—
R170	080052-2	277k ·1% 3 ppm M.FOIL	VISHAY	SEE DRG	—
R171	000104	100k 5% 1/3W CARBON	MULLARD	CR25	—
R172	000562	5k6 5% 1/3W CARBON	MULLARD	CR25	—
R173	000822	8k2 5% 1/3W CARBON	MULLARD	CR25	1
R174	000105	1M 5% 1/3W CARBON	MULLARD	CR25	—
R175	000222	2k2 5% 1/3W CARBON	MULLARD	CR25	—
R176	000222	2k2 5% 1/3W CARBON	MULLARD	CR25	—
R177	000821	820R 5% 1/3W CARBON	MULLARD	CR25	2
R178	000821	820R 5% 1/3W CARBON	MULLARD	CR25	—
R179	000682	6k8 5% 1/3W CARBON	MULLARD	CR25	—
R180	000103	10k 5% 1/3W CARBON	MULLARD	CR25	—
R181	000103	10k 5% 1/3W CARBON	MULLARD	CR25	—
R182	000154	150k 5% 1/3W CARBON	MULLARD	CR25	1
ANI	090017	100k ×7 2% NETWORK	BECKMAN	L08-1-R100k	1

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
C1	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	11
C2	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C3		NOT USED			—
C4	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	21
C5	120018	1µF 10% 63V POLYCARB	ASHCROFT	A2B1521B	1
C6	110015	15nF 20% 63V POLYESTER	WIMA	MKS2	1
C7	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C8	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C9	102101	100pF 10% 500V CER DISC	ITT	CD10	3
C10	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C11	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C12	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C13	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C14		NOT USED			—
C15	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C16	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C17		NOT USED			—
C18	102121	120pF 10% 500V CER DISC	ITT	CD10	1
C19		NOT USED			—
C20	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C21	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C22	120012	1µF 10% 160V POLYCARB	ASHCROFT	A2B1525B	1
C23	102102	1nF 10% 500V CER DISC	ITT	CD10	2

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
C24	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C25	101103	10nF 25% 250V CER DISC	ITT	CD10	8
C26	102101	100pF 10% 500V CER DISC	ITT	CD10	—
C27	102680	68pF 5% 500V CER DISC	ITT	CD10	1
C28	150004	100µF 20% 6V3 DIP TANT	UNION CARBIDE	K100E6V3	1
C29	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C30	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C31	102150	15pF 5% 500V CER DISC	ITT	CD10	2
C32	102150	15pF 5% 500V CER DISC	ITT	CD10	—
C33	102478	4p7F ±·5pF 500V CER DISC	ITT	CD08	2
C34	102478	4p7F ±·5pF 500V CER DISC	ITT	CD08	—
C35	102228	2p2F ±·5pF 500V CER DISC	ITT	CD08	1
C36	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C37	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C38	150023	33µF 20% 25V DIP TANT	UNION CARBIDE	K33E25	1
C39		NOT USED			—
C40		NOT USED			—
C41	130065	1n8F 1% 63V POLYSTYRENE	SUFLEX	HS1800/1-10/63	2
C42	102108	1pF ±·5pF 500V CER DISC	ITT	CD06	1
C43	130070	13pF ±·5pF 160V POLYSTYRENE	SUFLEX	HS13/1-7/160	2
C44	140058-1	150pF x2 MATCHED SET	DATRON	SEE DRG	1
C45	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C46	130070	13pF ± 1pF 160V POLYSTYRENE	SUFLEX	HS13/1-7/160	—

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy
C47	102278	2p7F ± ·5pF 500V CER DISC	ITT	CD10	1
C48	110035	220nF 20% 63V POLYESTER	WIMA	MKS2	2
C49	110035	220nF 20% 63V POLYESTER	WIMA	MKS2	—
C50	102100	10pF 5% 500V CER DISC	ITT	CD10	1
C51	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C52	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C53	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C54	110040	33nF 20% 63V POLYESTER	WIMA	MKS2	—
C55		NOT USED			1
C56	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C57	130065	1n8F 1% 63 V POLYSTYRENE	SUFLEX	HS1800/1 - 10/63	—
C58	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C59	102470	47pF 5% 500V CER DISC	ITT	CD10	2
C60	102102	1nF 10% 500V CER DISC	ITT	CD10	—
C61	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C62	140058-1	150pF x2 MATCHED SET	DATRON	SEE DRG	—
C63	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C64	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C65	140057-1	10nF ½% 125V SILV. MICA	DATRON	SEE DRG	1
C66	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C67	140056-1	1nF ½% 300V GLASS	DATRON	SEE DRG	1
C68	110026	6n8F 20% 100V POLYESTER	WIMA	FKS2	1
C69	140055-1	91pF ½% 500V GLASS	DATRON	SEE DRG	1

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No. USED Per Assy.
C70	130025	22pF ±1pF 160V POLYSTYRENE	SUFLEX	HS	1
C71	102330	33pF 5% 500V CER DISC	ITT	CD10	1
C72	110042	100nF 20% 63V POLYESTER	WIMA	MKS2	—
C73	150001	22µF 20% 16V DIP TANT	UNION CARBIDE	K22E16	1
C74	120010	1µ5F 10% 160V POLYCARB	ASHCROFT	A2B1525B	3
C75	120010	1µ5F 10% 160V POLYCARB	ASHCROFT	A2B1525B	—
C76	120010	1µ5F 10% 160V POLYCARB	ASHCROFT	A2B1525B	—
C77	120001	220nF 10% 1kV POLYCARB	SUFLEX	SN	1
C78	140031	13pF 5% 500V GLASS	ELECTROSIL	CYFM10	1
C79	140008	10pF 1kV TRIMMER	JACKSON	TETFER VPC	3
C80	102338	3p3F ± ·5pF 500V CER DISC	ITT	CD08	1
C81	140023	20pF 2% 500V GLASS	ELECTROSIL	CYFM10	2
C82	140008	10pF 1kV TRIMMER	JACKSON	TETFER VPC	—
C83	140039	15pF 5% 500V GLASS	ELECTROSIL	CYFM10	1
C84	140008	10pF 1kV TRIMMER	JACKSON	TETFER VPC	—
C85	140023	20pF 2% 500V GLASS	ELECTROSIL	CYFM10	—
C86	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C87	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C88	150002	10µF 20% 16V DIP TANT	UNION CARBIDE	K10E16	2
C89	150002	10µF 20% 16V DIP TANT	UNION CARBIDE	K10E16	—
C90	110027	3n3F 20% 100V POLYESTER	WIMA	FKS2	1
C91	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C92	130074	6·8pF ±1pF 16V POLYSTYRENE	SUFLEX	HS	1

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
C93	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C94	150020	10µF 20% 25V DIP TANT	UNION CARBIDE	K10E25	—
C95	150016	1µF 20% 35V DIP TANT	UNION CARBIDE	K1R0E35	1
C96	110009	1µF 20% 100V POLYESTER	WIMA	MKS	1
C97	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C98	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C99	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C100	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C101	101103	10nF 25% 250V CER DISC	ITT	CD10	—
C102	102101	100pF 10% 500V CER DISC	ITT	CD10	—
C103	102470	47pF 5% 500V CER DISC	ITT	CD10	—
C104	104017	0.5pF 0.1% 500V CER DISC	ITT	CD08	1

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
D1		NOT USED			—
D2	210100	10V 400mW ZENER	MULLARD	BZY88C10	2
D3		NOT USED			—
D4	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	10
D5	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	3
D6	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—
D7	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—
D8	210120	12V 400mW ZENER	MULLARD	BZY88C12	3
D9	210120	12V 400mW ZENER	MULLARD	BZY88C12	—
D10		NOT USED			—
D11	210100	10V 400mW ZENER	MULLARD	BZY88C10	—
D12	220010	Si HOT CARRIER DIODE	HP	HSCH1001 / IN6263	2
D13	220010	Si HOT CARRIER DIODE	HP	HSCH1001 / IN6263	—
D14	220036	DUAL 500pF VARICAP DIODE	MULLARD	BB212	1
D15	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—
D16	213011	1V5 250mW ZENER	MULLARD	BZV46-1V5	1
D17	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—
D18	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—
D19	210120	12V 400mW ZENER	MULLARD	BZY88C12	—
D20	210082	8V2 400mW ZENER	MULLARD	BZY88C8V2	1
D21		NOT USED			—
D22	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—
D23	200008	200mA 125V LL Si DIODE	FAIRCHILD	IN458A	—

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy.
D24	200008	200mA 125V LL.Si. DIODE	FAIRCHILD	IN458A	—
D25	200008	200mA 125V LL.Si. DIODE	FAIRCHILD	IN458A	—
D26	220020	FET DIODE 100pA I_R	TELEDYNE	PAD100/INSUL'D CASE	2
D27	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D28	200001	75mA 75V GP Si DIODE	FAIRCHILD	IN4148	—
D29	213009	15V 5W ZENER	UNITRODE	TVS515	1
D30		NOT USED			—
D31	220020	FET DIODE 100pA I_R	TELEDYNE	PAD100/INSUL'D CASE	—
Q1		NOT USED			—
Q2		NOT USED			—
Q3	230027-1	N-CHAN JFET	TELEDYNE	U3114JF	2
Q4	240013	Si NPN TRANSISTOR	NATIONAL	BC184C/TO18	1
Q5	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	3
Q6	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	4
Q7	230042	N-CHAN I LIM 3·0mA	TELEDYNE	J509	3
Q8	230003	N-CHAN JFET	TELEDYNE	U1899JF	4
Q9	250011	Si.PNP TRANSISTOR	NATIONAL	BC327/TO18	3
Q10		NOT USED			—
Q11	250008	Si PNP TRANSISTOR	NATIONAL	BC214C/TO18	1
Q12	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	—
Q13	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	—
Q14	230042	N-CHAN I LIM 3·0mA	TELEDYNE	J509	—

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DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
Q15	250011	Si PNP TRANSISTOR	NATIONAL	BC327/TO18	—
Q16	230003	N-CHAN JFET	TELEDYNE	U1899JF	—
Q17	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/	—
Q18	230027-1	N CHAN JFET	TELEDYNE	U3114JF	—
Q19		NOT USED			—
Q20	230002	N-CHAN JFET	TELEDYNE	U1994JF	1
Q21	230056	N-CHAN JFET	SILICONIX	J212	1
Q22		NOT USED			—
Q23	230003	N-CHAN JFET	TELEDYNE	U1899JF	—
Q24		NOT USED			—
Q25	250004	Si PNP TRANSISTOR	NATIONAL	2N3906/TO18	—
Q26	240006	Si NPN TRANSISTOR	NATIONAL	2N3904/TO18	—
Q27	250011	Si PNP TRANSISTOR	NATIONAL	BC327/TO18	—
Q28	230042	N-CHAN I LIM 3·0mA	TELEDYNE	J509	—
Q29	230003	N-CHAN JFET	TELEDYNE	U1899JF	—
Q30		NOT USED			—
Q31	230035	N-CHAN JFET	TELEDYNE	U1897JF	4
Q32	230035	N-CHAN JFET	TELEDYNE	U1897JF	—
Q33	230035	N-CHAN JFET	TELEDYNE	U1897JF	—
Q34	230035	N-CHAN JFET	TELEDYNE	U1897JF	—
Q35	230058	N-CHAN I LIM 750μA	TELEDYNE	J504	1
Q36	230031	N-CHAN DUAL JFET	SILICONIX	U404	1

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DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy.
M1		NOT USED			–
M2	260050	412 DUAL BIFET OP AMP	NATIONAL	LF412CN	2
M3	260028	1458 DUAL OP AMP	FAIRCHILD	µA1458 CTC	1
M4	260063	7650 OP AMP	INTERSIL	ICL7650CTV	4
M5	280015	QUAD D-TYPE	MOTOROLA	MC14076 BCP	2
M6	280015	QUAD D-TYPE	MOTOROLA	MC14076 BCP	–
M7	280011	DUAL D FLIP FLOP	MOTOROLA	MC14013 BCP	1
M8		NOT USED			–
M9	260063	7650 OP AMP	INTERSIL	ICL7650 CTV	–
M10	260063	7650 OP AMP	INTERSIL	ICL7650 CTV	–
M11	290026	RMS KIT	DATRON	SEE DRG	1
M12	290077	7 x DARLINGTON DRIVER	SPRAGUE / EXAR	ULN2004A / XR2204CP	1
M13	260065	OP27 OP AMP	PMI	OP27 EZ	1
M14	260027	714 OP AMP	FAIRCHILD	µA714 HC	2
M15	280116	DUAL 4 CHAN AN MUX	SILICONIX	DG509 CJ	2
M16	260050	412 DUAL BIFET OP AMP	NATIONAL	LF412CN	–
M17	280116	DUAL 4 CHAN AN MUX	SILICONIX	DG509 CJ	–
M18		NOT USED			–
M19	290066	FREQ SENSITIVE SWITCH	CONSUMER MICROCIRCUITS	FX301L	1
M20	260027	714 OP AMP	FAIRCHILD	µA714 HC	–
M21	260063	7650 OP AMP	INTERSIL	ICL7650 CTV	–
M22	260047	2625 OP AMP	HARRIS	HA32625-5	1

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DESIGNATOR	DATRON PART No	DESCRIPTION			
RL1	330012-2	REED RELAY 1A GUARDED	HAMLIN	HE721A5134	4
RL2	330018-1	RELAY 2P2W 7V HOLD-IN	AMF	SEE DRG	2
RL3	330018-1	RELAY 2P2W 7V HOLD-IN	AMF	SEE DRG	–
RL4	330012-2	REED RELAY 1A GUARDED	HAMLIN	HE721A5134	–
RL5	330012-2	REED RELAY 1A GUARDED	HAMLIN	HE721A5134	–
RL6	330012-2	REED RELAY 1A GUARDED	HAMLIN	HE721A5134	–
	450388-1	GUARD SHIELD			1
	400379/5	WIRE / TERMINAL ASSY			1
	410217-4	PCB			1
	459112-2	RELAY BRACKET			2
	540002	22SWG BTC WIRE			A/R
	512999	7/·2 PTFE INSUL. (WHITE) WIRE			490 mm
J1	571095/C	16 WAY AP/3M RIBBON CABLE			1
	590001	SLEEVE MAX CABLE ⌀3·0	HELLERMANN ELECTRIC	H15 x 20mm BLACK HELSYN	7
	590004	SLEEVE – PTFE	HELLERMANN ELECTRIC	FE10	A/R
	605059	8 WAY D.I.L. SOCKET			4
	602001	FSV TERMINAL	MOLEX	02-04-1575	2
	602004	BREAKAWAY TERM^L STRIP	MOLEX	05-30-0001	16
J3	605052	8 WAY POLARISED SOCKET	MOLEX	(22-01-2085) 6471-8-1	1
	605060	14 PIN DIL SOCKET	ASTRALUX	ICL143-S3T	1
	605061	16 PIN DIL SOCKET	ASTRALUX	ICL163-S6T	5
	605057	CRIMP TERMINAL	MOLEX	4809-GL	2

NOTES

SEE SHEET 2 FOR LATEST ISSUE

ISS						
ECO						
DATE						
CHKD						

DATE 5.7.83 | datron ELECTRONICS LTD

DRAWN | TITLE 1061A 1062 AC PCB ASSY

CHECKED LOG | DRAWING NUMBER 400552 | SHEET 19 OF 20

APPROVED | DATE

DESIGNATOR	DATRON PART No.	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No.	No. USED Per Assy
	611007	M3 x 6mm POZI CSK STEEL	ZN. PL.		3
	611016	M3 x 8mm POZI PAN STEEL	ZN. PL.		5
	612021	M3 x 16mm SPACER	HARWIN	R6377-02	3
	613005	M3 INT. SHAKEPROOF			2
	613014	M2·5 INT. SHAKEPROOF			2
	615002	M3 FULL NUT STEEL ZN. PL.			2
	615005	3-48 UNC FULL NUT STEEL	ZN. PL.		2
	617010	NYLATCH PLUNGER 3/16"	HARTWELL CORP	HN3P-32-4-1	5
	617011	NYLATCH GROMMET 3/16"	HARTWELL CORP	HN3G-32-1	5
	620003	SOLDER PIN	HARWIN	H2105 A01	2
	620005	CLOVERLEAF PTFE INSUL.	SEALECTRO	FTE1SP59	9
	620007	TEST POINT TERMINAL	MICROVAR	TYPE C30	12
	630107	BRASS STRIP 15·5 x ·38mm	RIGHTON	CZ108 ½H	220mm
	613029	M3 CRINKLE WASHER S.S			3
	620001	CLOVERLEAF. PTFE INSUL.	SEALECTRO	FTE 12 P59	5
	620008	PTFE BUSH	SEALECTRO	119-0034	2
	630024	STEATITE BEAD 16 SWG	PARK ROYAL PORCELAIN	Nº 2	4

NOTES.

SEE SHEET 2 FOR LATEST ISSUE

ISS												
ECO												
DATE												
CHK'D												

DATE 5.7.83

DRAWN

CHECKED LOG

APPROVED

DATE

datron ELECTRONICS LTD

TITLE 1061A 1062 AC PCB ASSY

DRAWING NUMBER 400552

SHEET 20 OF 20

440082/440083/440084

DESIGNATOR	DATRON PART No	DESCRIPTION	PRINCIPAL MANUFACTURER	MANUFACTURER'S PART No	No USED Per Assy
	400427	IEEE 488 OPTION P.C.B. ASSY	DATRON		1
	400429	SOCKET/CABLE ASSY	DATRON		1
	400346	ADDRESS SWITCH P.C.B. ASSY	DATRON		1
	SEE TABLE	EXTERNAL TRIGGER ASSY	DATRON		1
	450169-3	STUD MOUNT STANDOFF			2
	450225-2	IEEE ADAPTOR PLATE			SEE TABLE
	611016	SCREW M3 x 8 POZ PAN HD			2
	613005	WASHER M3 INTERNAL SHKPROOF			2
	613020	WASHER M4 FLAT - STEEL			
	613021	WASHER M4 INTERNAL SHKPROOF			2
	615011	NUT M4 FULL HEX - STEEL			2
	630042	ADHESIVE CABLE CLIP	R CHECO	CFCC-8	2
M3	SEE TABLE	4K x 8 EPROM	DATRON		1

NOTES CIRCUIT REFER 430427

SEE SHEET 2 FOR LATEST ISSUE

TYPE	KIT	M3
1065	440082	29 0084 - [illegible]
1061	440083	29 0070 - [illegible]
1071	440084	29 0064 - [illegible]

ASSEMBLY QTY	SET TABLE NO
0	440435
1	440436
	440440

ISS	1	2	3	4	5	6	7	8	9
ECO	—	1217	1313	1371	1388, 1389	1410 1411	1451	1495	1519
DATE	6.1.81	19 8 81	13 7 82	24 8 82	9 9 82,	24 11 82	31 3 82	2 6 83	8 8 83
CHKD		EP	MD	MD	MD	MD	MD	EP	MD

6.1.81.

IL

MD

datron

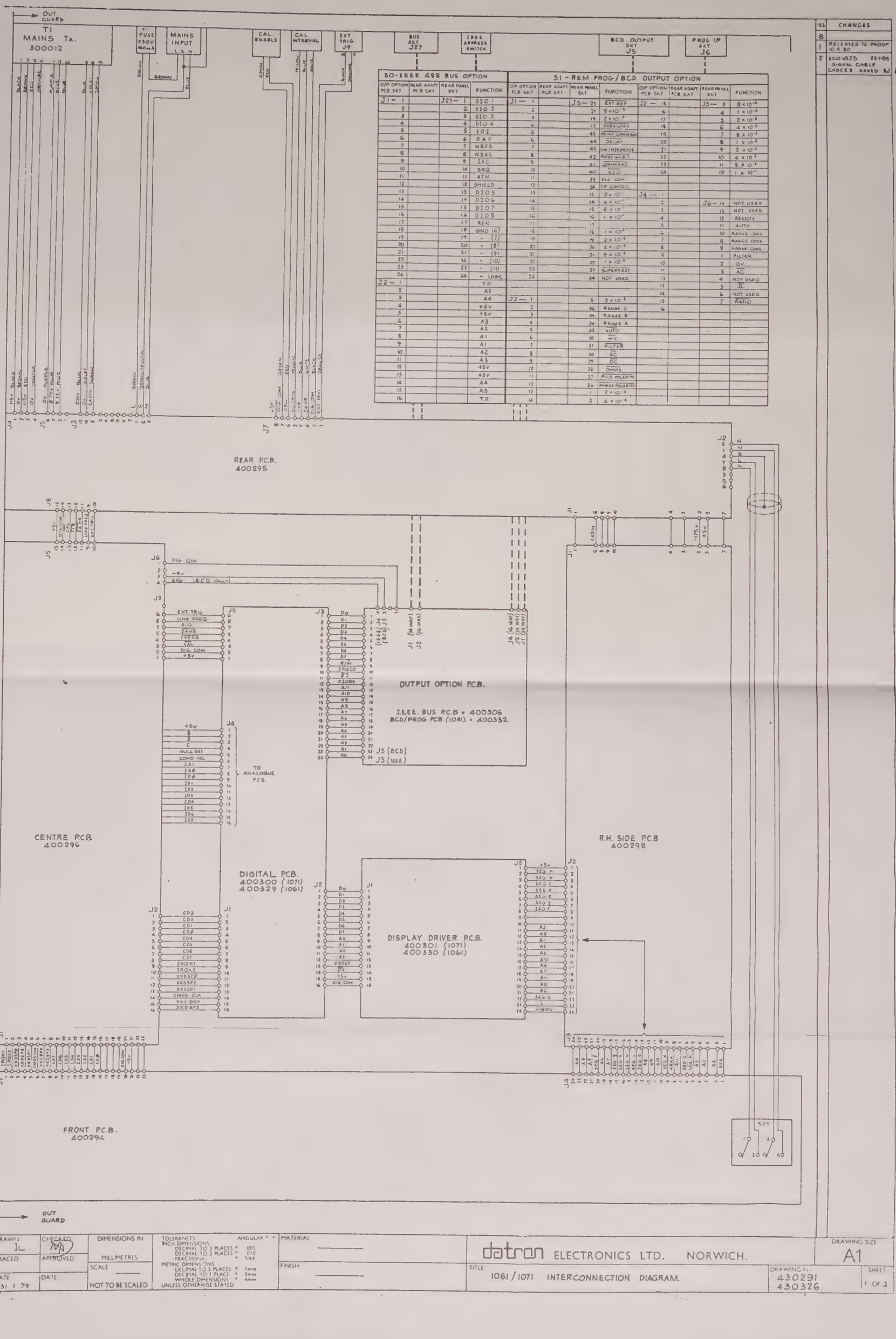
IEEE 488 OPTION

1071/1061/1065

440082
440083
440084

SHEET 1

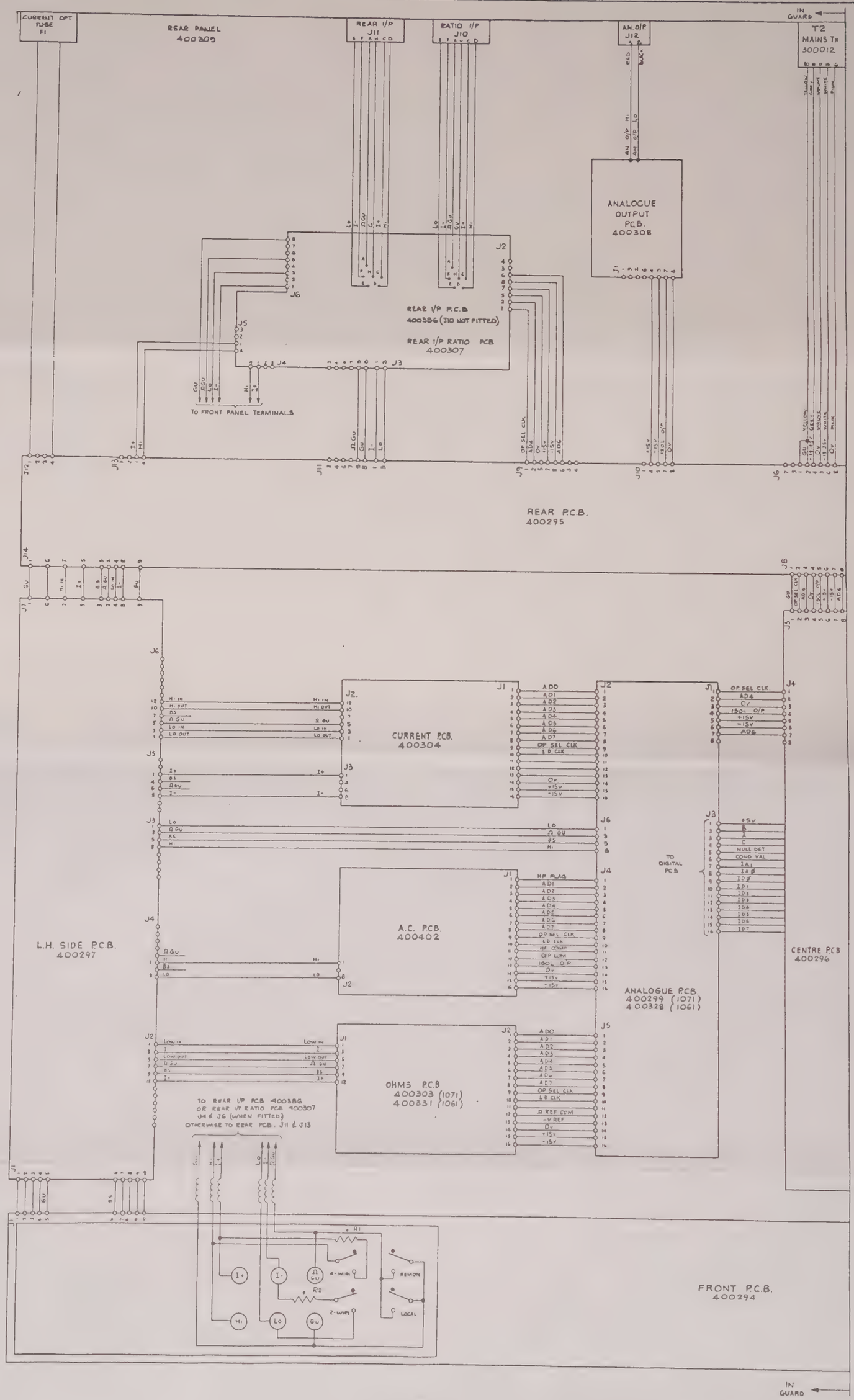
2W 1164



No	CHANGES
B	
1	RELEASED TO PRODN 10 4 80
2	ECO 6323 23-1-83 SIGNAL CABLE CHOICES ADDED IL

DRAWN IL	CHECKED MA	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONS ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °	MATERIAL —	datron ELECTRONICS LTD. NORWICH. DRAWING SIZE A1
TRACED	APPROVED	SCALE — NOT TO BE SCALED		FINISH —	TITLE 1061 / 1071 INTERCONNECTION DIAGRAM. DRAWING No. 430291 430326 SHEET 1 OF 2
DATE 31 1 79	DATE				

430291 /430326.


CURRENT OPT FUSE F1
REAR PANEL 400305
REAR I/P J11
RATIO I/P J10
AN. O/P J12
IN GUARD
T2 MAINS Tx 300012
ANALOGUE OUTPUT P.C.B. 400308
REAR I/P P.C.B 400386 (J10 NOT FITTED)
REAR I/P RATIO PCB 400307
TO FRONT PANEL TERMINALS
REAR P.C.B. 400295
CURRENT PCB. 400304
A.C. PCB. 400402
OHMS P.C.B 400303 (1071) 400831 (1061)
L.H. SIDE P.C.B. 400297
CENTRE PCB 400296
ANALOGUE P.C.B. 400299 (1071) 400328 (1061)
TO DIGITAL P.C.B
TO REAR I/P PCB 400386 OR REAR I/P RATIO PCB 400307 J4 & J6 (WHEN FITTED) OTHERWISE TO REAR PCB. J11 & J13
FRONT P.C.B. 400294
4-WIRE
2-WIRE
REMOTE
LOCAL

datron ELECTRONICS LTD. NORWICH.			DRAWING SIZE A
TITLE 1061/1071 INTERCONNECTION DIAGRAM		DRAWING No 430291/430326	SHEET 2 OF 2

DRAWING No 400294

FIRST USED ON

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

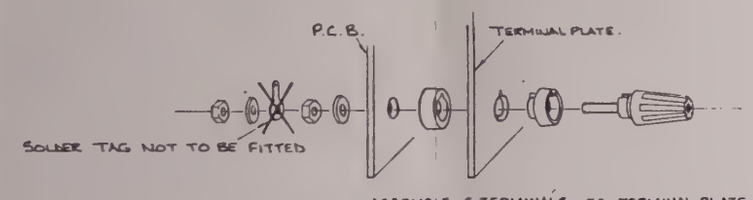
NOTES

IMPORTANT.

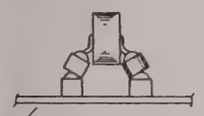
1. FIT M1 3 4 5 6 8 9 11 12 & 13 INTO 16 WAY AUGAT SOCKET PART NO 605061.
   FIT M7 & M10 INTO AUGAT SOCKET PART NO. 605062. 18 WAY.
   FIT M2 INTO AUGAT SOCKET PART No. 605060. 14 WAY.

2. TO REMOVE DISPLAY:-

No.	CHANGES
C	-
D	UPDATED PCB REMOVED 8/84
1	RELEASED TO PRODN 29-9-78
2	SKETCH FOR HOLTITE SOCKETS ADDED BJ ECO 778 5-12-78
3	ECO 836 WARNING LABEL 4200801-1 ADDED ECO 868 C3 & C4 WERE 0.33µF (50 Volt) 23 JAN 79 WGS
4	ECO 867. LK8 ADDED BJ 11 JUN 79.
5	ECO 988 26-10-79 UPDATED PCB BJ + 2 PIECES OF 630029 DELETED SOCKETS: 607011 & 607012 DISPLAY REMOVAL NOTE
6	ECO 1111 C9 ADDED JL 21 4 80
7	ECO 1213 LK3 DELETED JR 11-6-81
8	ECO 1352 MORE TAPE ADDED TO REVERSE OF DISPLAY. JR 1-7-82
9	ECO 1472 C10 ADDED COMPLETE WITH SOLDER TAG ENDS JL 2 6 82
10	ECO 1662 PCB WAS 166 4C R13, R14 C11 AND SCREEN PCB ADDED. C9 NOW TRACKED IN. JP 6 8 84
11	ECO 1661 C10 & 48A TAG DELETED AP. 10.9.84
CONT.	
12	ECO 1816 SCREEN PCB REMOVED. JP 10.1.85
13	ECO 1823. .83. SIGNAL CABLE ASSY ADDED. 24th JAN 85



ASSEMBLE 6 TERMINALS TO TERMINAL PLATE No 450179 & PCB AS SHOWN ABOVE. TERMINAL COLOURS MUST BE AS INDICATED BELOW.



RESISTORS R1 & R2
2 off INSULATING BEADS 630024
ASSEMBLED ON EACH LEG AS SHOWN
RESISTORS TO BE MOUNTED ON THE UNDER SIDE OF BOARD.

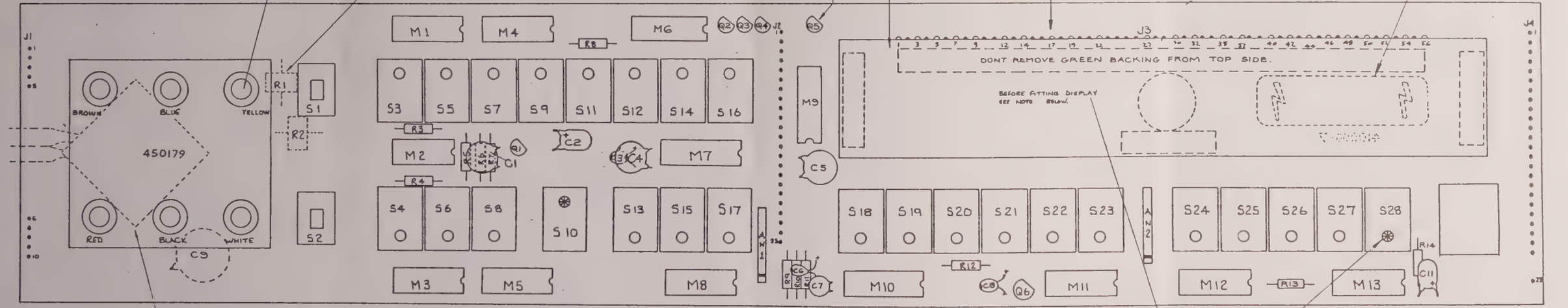
NOTE.
NO COMPONENTS TO STAND HIGHER THAN 7.5 mm (EXCEPT SWITCH'S & TERMINALS)
EXCEPT Q4 & Q5 WHICH MUST BE LESS THAN 6.5mm

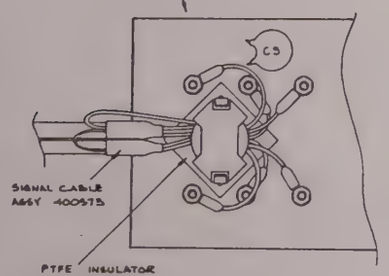
THE DISPLAY IS TO HAVE 7 PIECES OF PRESSURE SENSITIVE TAPE 630029. 1 PIECE (140mm LONG) STUCK TO THE P.C.B. BELOW THE LINE OF HOLES AND 6 PIECES (25 mm LONG) STUCK TOGETHER IN PAIRS AND STUCK TO THE REVERSE OF DISPLAY IN POSITIONS SHOWN. LINE UP AND PRESS DISPLAY FIRMLY INTO PLACE. THEN SOLDER THE 24 PINS WHICH ARE SHOWN BELOW (THERE ARE ONLY SOLDER PADS FOR THESE PINS).

FIT WARNING LABEL 4200801-1 IN APPROX POSITION SHOWN ON TRACK SIDE OF P.C.B. NEAR MAINS AND DISPLAY CONNECTIONS.

8 DIGIT DISPLAY B00017

FRONT P.C.B. 410090-7





SOLDER SIGNAL CABLE ASSY 400573 TO REVERSE SIDE OF PCB WITH PTFE INSULATOR 450451 BETWEEN PCB & CHOKE.

CONNECT WIRES TO TERMINALS THUS :-

WHITE WIRE	TO	WHITE	TERMINAL
YELLOW "	"	YELLOW	"
GREEN "	"	BLACK	"
RED "	"	RED	"
VIOLET "	"	BLUE	"
BLUE "	"	BROWN	"

WHEN ASSEMBLY IS FINISHED TAPE CABLES TO PCB USING MASKING TAPE 630039. DO NOT STICK MASKING TAPE OVER DISPLAY.!

ALL CAPACITORS TO BE LAID DOWN WHERE POSSIBLE, AS SHOWN. LEADS TO BE BENT AT RIGHT ANGLES THEN INSERTED INTO BOARD, THIS IS TO KEEP CAPACITORS AS LOW AS POSSIBLE.

GREEN L.E.D.s 2 OFF
ALL OTHERS RED.

PINS OF THE DISPLAY MUST BE CUT ALONG THE LINE SHOWN 'XX' THEN THE PINS MUST BE BENT FROM 'A' TO 'B' BEFORE INSERTION INTO P.C.B.

DRAWN B.J.	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± .°	MATERIAL	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH	TITLE FRONT P.C.B. ASSY. 1061 / 1071 / 1081.
DATE 19-4-78	DATE				DRAWING No. 400294. SHEET 1 OF 6

DRAWING SIZE A1

DRAWING No 430294

FIRST USED ON 1081/71

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. SIGNAL CD1 ON J2-15, THERE IS A LINE 'LIKE' IN THIS SIGNAL LINE.

ISS	CHANGES
2	REDRAWN BJ. 21-3-79
3	ECO 1068 (69) CURVE ON SHT2 BJ 11-6-79
4	ECO 1111 SEE SHEET 2 IL 21 4 80
5	ECO 1472 C10 ADDED (4n7F) IL 2 6 83
6	ECO 1662 R13 R14, C11 AND SCREEN PCB ADDED. ON M4, 5, 6, 8, 9, 11 AND M12 PIN 15 WENT TO 0V AND PIN 16 WAS NC M2 PIN 14 WAS N.C. JFM 7 8 84
7	ECO 1661 SEE SHT 2 MP 10 9 84
8	ECO 1823. SEE SHEET 2. BJ. 24th JAN 85

J2-1 IRQK1
J2-2 IRQK2
J2-21 +5V
J2-20 0V COMM
J2-17, J2-18, J2-19, J2-22, J2-23 UNUSED PINS
J2-7 KYBRES
J2-16 CD0
J2-15 CD1
J2-14 CD2
J2-13 CD3
J2-12 CD4
J2-11 CD5
J2-10 CD6
J2-9 CD7
J2-6 CMOS CLK
J2-4 KYDSPX
J2-5 KYDSPY
J2-3 KYDSPZ
J2-8 KYDSPS

TO J1 CENTRE PCB.

C5 0.01µF, C2 10µF 16V, C8 10µF 16V, C11 4µ7 16V, R13 1K, R14 4k7

M2. MC14018 BCP

M10. MM74C922

M7. MM74C922

1061 1071 SWITCH LEGENDS SHOWN WITH 1081 LEGENDS IN BRACKETS

S32 SPEC (RATIO ÷A), S31 TEST (Δ), S30 RATIO (REF), S33 HOLD (SIG), S26 MAX, S25 -C, S28 A=B (Hi Res), S24 (A-B), S18 MAX (Hold), S28 RESET, S27 MIN, S15 AC, S6 INPUT FILTER

S16 10Ω, S14 100Ω, S11 1K, S12 10K, S3 AUTO, S5 10Ω, S7 +1, S9 ↑, S13 KΩ, S17 DC, S4 INPUT ZERO, S8 I (PRT), S10 KEY

C7 0.01µF, C6 1.0µF 63V, C3 1.0µF 63V, C4 0.01µF

R7 100K, R6 1K, Q1 BC184, C1 100pF

CMOS DATA BUS

M12 MC14076., M11 MC14076., M8 MC14076., M5 MC14076., M6 MC14076., M4 MC4076., M9 MC14076.

AN2. 150R, AN1. 150R

R12 150Ω, R5 150Ω, R4 150Ω, R8 150Ω, R3 150Ω, R10 150Ω, R9 150Ω, R11 150Ω

Q6 BC184, Q2 BC184, Q3 BC184, Q4 BC184, Q5 BC184

M13. CA3081P., M3. CA3081P., M1. CA3081P.

SCREEN PCB

TO DIGITAL DISPLAY B00017

J3-1	ALKA	J4-1
J3-5	-17.5V	J4-2
J3-50		
J3-3	A1	J4-3
J3-7	A2	J4-4
J3-9	b	J4-5
J3-14	c	J4-6
J3-56	A11	J4-7
J3-52	AEKA	J4-8
J3-54	d	J4-9
J3-48	A10	J4-10
J3-42	A9	J4-11
J3-37	A8	J4-12
J3-46	e	J4-13
J3-44	i	J4-14
J3-40	a	J4-15
J3-35	h	J4-16
J3-30	g	J4-17
J3-32	A7	J4-18
J3-27	A6	J4-19
J3-19	f	J4-20
J3-22	A5	J4-21
J3-12	A3	J4-22
J3-17	A4	J4-23
		J4-24
	MAIN SWITCH GND	J4-25

TO J3 RIGHT HAND PCB.

DRAWN BJ.	CHECKED P.R.H.	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED. ANGULAR ± ° '	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE —— NOT TO BE SCALED		FINISH ——	TITLE FRONT PCB CIRCUIT DIAGRAM 1061/1071/1081
DATE 21-3-79	DATE				DRAWING No 430294

DRAWING SIZE A1

SHEET 1 OF 2

DRAWING No. 430294

FRONT PCB 400294

SIGNAL CABLE ASSY 400573

L1. INPUT COMMON MODE CHOKE

Hi — RED — J13-4

I+ — BLUE — J13-1

TO J13 REAR PCB. (VIA SIGNAL CABLE 400573) (WHEN REAR INPUT/RATIO IS FITTED, CONNECT TO J4 OF RATIO PCB.)

I- — VIOLET — J11-1

TO J11 REAR PCB. (VIA SIGNAL CABLE 400573) (WHEN REAR INPUT/RATIO IS FITTED, CONNECT TO J6 OF RATIO PCB.)

Lo — GREEN — J11-3

Gu — WHITE — WHITE — J11-8

ΩGu — YELLOW — J11-5

R1 VAB650 — 4 WIRE S1 2 WIRE

R2 VAB650 — 4 WIRE S1 2 WIRE

C9 2n2f 1kV

REMOTE S2 LOCAL

To J1 L.H. PCB — J1-1 — J1-2 — J1-3 — J1-4 — J1-5 (GU) — J1-6 (BS) — J1-7 — J1-8 — J1-9 — J1-10

Notes :-

1. Gu (J1-5) screens all circuitry on this sheet.
2. B.S. screens Hi & I+.
3. ΩG. screens Lo & I-.

ISS.	CHANGES
2	REDRAWN BJ 21.3.79.
3	ECO 899 SCREEN FOR Hi & I+ NOW CONNECTED TO Gu. VIA WHITE WIRE BJ 11-6-79
4	ECO 1111 C9 ADDED. IL 21.4.80
5	ECO 1472 C10 ADDED (4n7f) IL 2.6.83.
6	ECO 1662 SEE SHEET 1.
7	ECO 1661 C10 DELETED AP. 10.9.84.
8	ECO 1823 BJ. RE-DRAWN. 25-1-85 CABLE ASSY ADDED TO FRONT PCB ASSY. CHOKE ADDED TO SIGNAL CABLE.

datron INSTRUMENTS NORWICH ENGLAND

THIRD ANGLE PROJECTION	DRAWN B.S.JACKSON	DATE 24th JAN 85	DIMENSIONS IN MILLIMETRES	TOLERANCES: DECIMAL TO 2 PLACES ±·1mm; DECIMAL TO 1 PLACE ±·2mm; WHOLE DIMENSIONS ±4mm; ANGULAR ± ½° UNLESS OTHERWISE STATED	MATERIAL —	ASSY DRG & PARTS LIST 400294	TITLE FRONT PCB CIRCUIT DIAGRAM	DRAWING No. 430294.
ALL BURRS TO BE REMOVED	CHECKED M.J.D.	DATE 29.1.85	SCALE —		FINISH —	CIRCUIT DIAGRAM 430294		SHEET 2 OF 2
	APPR B Hunt	DATE 29.1.85	NOT TO BE SCALED			CHECK PROCEDURE 460294		
						CHECK LIST 470294		

DRAWING No. 400295

FIRST USED ON

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

4 OFF. STEEL M3x8mm SCREW. 611016.
9 OFF. NYLON M3 x 8mm SCREW. 611037.
4 OFF STEEL SHAKEPROOF WASHER. 613005.
9 OFF NYLON M3 WASHER . 613017.
2 OFF STEEL M3 NUTS. 615002.
3 OFF NYLON M3 NUTS. 615008.

ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES
C	
D	UPDATED PCB E.J. 22-8-78 NO
1	RELEASED TO PRODN 25.9.78
2	ECO 783 B-J 10 CONSISTENCY CHANGES B.J. NO
3	E.C.O. 836 SILKSCREEN LABEL ARRANGEMENT ADDED 25 JAN 79 W.O.S.
4	ECO 876, 882, 900 C6 & C7 ADDED. J5 NOW GOLD 16WAY 4 PCB U1, L2 & L5 ADDED. R13 ADDED. C8 & C9 LD. R7 & R13 CHANGED. G.G. 19.3.79 B
5	ECO 951 & 954 PCB WAS 4514 J1, J2 & J4, BOTH GOLD DELETED I FUSE HOLDER & CLIP WAS 20301.3 WI WAS 20301.2 MICA WASHERS WERE 618008 E.J. 31-10-79
6	E.C.O. 1085 1097 20PF SOLDER PINS & 10PF 4 WAY WAFER (J13) DELETED IL 21.4.80
7	ECO 1941. R9 WAS 1K4 NOW 8.J. 11.8.83
8	ECO 1452 M1 WAS MC7805 CT FIXINGS MODIFIED TO SUIT LM309K DEVICE C8 WAS 10nF CER DISC IL 17.2.83

CONT

THIS HEATSINK HELD TO PCB BY
STEEL M3x8mm SCREWS
STEEL M3 SHAKEPROOF WASHERS

Q1 HELD TO HEATSINK WITH
NYLON M3x8mm SCREW
NYLON M3 WASHER
NYLON M3 NUT

CROP LEADS TO 4mm

HEATSINK 450180

TO3 SIL PAD 618009

CRINKLE WASHER 613029 2. OFF

M3x8mm POZI-PAN SCREW 611016 2 off

M3 NUT 615002 2. off

INSULATING BUSH 618001 2. off

M3 SOLDER TAG 613009

MICA WASHER 618007

M1

Q1

P.C.B.

SLEEVE CONNECTIONS 590001

WIRE LENGTH 65mm

WIRE LENGTH 55mm

WIRE LENGTH 35mm.

I/P

GND

O/P

Q1

LEADS MADE FROM 7/2 PTFE WIRE WHITE 540008

3 off HEATSINK 450183

M2 & M3

PCB

THESE 2 HEATSINKS HELD TO PCB WITH NYLON M3 x 8mm SCREWS & NYLON M3 WASHERS

M2 & M3 HELD TO HEATSINKS WITH STEEL M3 x 8mm SCREWS
STEEL M3 SHAKEPROOF WASHERS
STEEL M3 NUTS

FIT WARNING LABEL 420080-4 IN APPROX POSITIONS SHOWN ON TRACK SIDE OF P.C.B. IN MAINS AREA.

HEATSINK COMPOUND ON BOTH SIDES OF MICA WASHERS ON Q1

4BA SOLDER TAG 613009
7/2 PTFE WIRE x 30mm 540008
HEATSHRINK x 20mm 590006

BLACK DOTS INDICATE THAT PIN HAS TO BE REMOVED BEFORE INSERTION INTO P.C.B.

MOUNT D4 ON STEATITE BEAD 630024 2 off

TEST POINT TERMINAL (620007) 8 OFF

REAR PCB 410091-5A

ADJUSTING SCREWS THIS SIDE

TP1 TP2 TP3 TP4 TP5 TP6 TP7 TP8 TP9

J1 J2 (THESE 5 PINS MOUNTED ON UNDERSIDE OF PCB) J3 J4 J5 J6 J7 J8 J9 J10 J11 J12 J13

LK1 LK2 LK3 LK4 LK5

W1 W2

C5 C6 C7 C8 C9 C10 C11 C12 C13 C14 C15 C16

R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R12 R13

D1 D2 D3 D4

Q1 Q2

L1 L2 L5

M2 M3

ALL LINKS USE 22 SWG TIN.CU. WIRE (DATRON PART No. 540002)

LINKS 1, 2 & 3 MUST BE SLEEVED USING 590004

FOR 115 V OPERATION
REMOVE LK1 , FIT LK2 & LK3

FLAT PIN ASSY GOLD (PART No 604033) 17 off.

BACKPLATE INDICATES WHICH WAY THE PIN ASSY FITS INTO THE P.C.B.

R4, R5 & R6 ARE POSITIONED UNDER THE CURVE OF C6 & C7. (AS SHOWN BELOW)

C7 C6

THE 4 LARGE CAPACITORS C6, C7, C13 & C15 ARE TO BE BEDDED INTO SILICONE RUBBER COMPOUND PART No 900004 BEFORE SOLDERING. THIS SHOULD FINISH UP AS ABOVE DRAWING

MICA WASHER 618007

M3x8mm NYLON SCREW
M3 NYLON WASHER
M3 NYLON NUT.

MAKE LK5 AS A TEST POINT LOOP TO FACILITATE EASE OF 'SNIPPING' WHEN NOT REQUIRE.

NO	CHANGES
9	R14 C1 DELETED. LK5 ADDED 4BA SOLDER TAG WIRE ASST ADDED IL 2.8.83 ECO1470
10	ECO 1517 J3,4,6,7,9,10 WERE TIN NOW GOLD IJA 17.8.83
11	ECO 1525 D1 AND D2 WERE 200010. PCB WAS 165UF.5 NOW 165UF.6 IJA 15.9.83
12	ECO. 1688 D4 MOUNTED ON STEATITE BEADS. IL 20.8.84

DRAWN B.J.	CHECKED	DIMENSIONS IN mm MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± .5°
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED	MATERIAL
DATE 26-4-78	DATE		FINISH

datron ELECTRONICS LTD. NORWICH.

TITLE: REAR P.C.B. ASSEMBLY. 1061/1071/1081

DRAWING SIZE A1

DRAWING No 400295

SHEET 1 of 6

DRAWING No. 430295

FIRST USED ON 1061/71

THIRD ANGLE PROJECTION

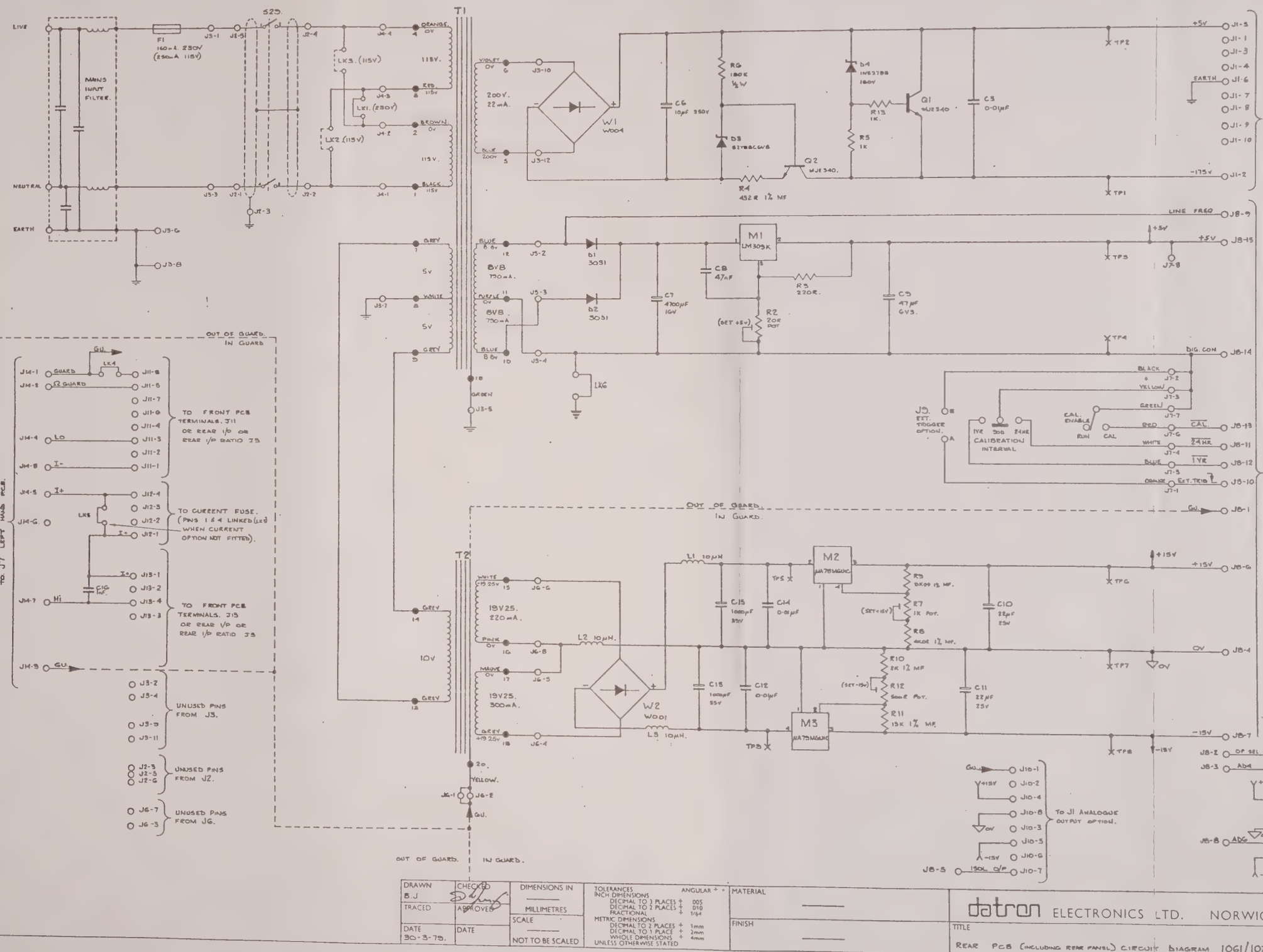
DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. T1, T2, MAINS INPUT FILTER, F1, EXT TRIG. OPTION, ERROR SELECT & CAL ENABLE. ARE ALL FITTED ON THE REAR PANEL ASSY 400308, BUT INCLUDED ON THIS CIRCUIT DIAGRAM.

ISS	CHANGES
2	RE DRAWN 30-3-79 B.J
3	ECO 882 & 900 MAINS C16, R18, L1, L2, L3 & LK4. R3 & R2 CHANGED 18-6-79 B.J
4	ECO 951 & 954 W1 WAS WOOG (A30018) VB PW No CHANGED. JE " " " LK5 ADDED. B.J. JB-8 ADDED 31-10-79
5	ECO. 1085 J13 PINS 1 TO 4 DELETED IL 21 4 80
6	ECO. 1125 FUSE IN NEUTRAL LINE TO J3-3 DELETED. IL 27 5 80
7	ECO 1452 M1 WAS MC7805CT C8 WAS 10nF IL 16.2.83
8	ECO. 1470 R1 (100k) & C1 (100nF) DELETED LK6 ADDED IL 1 6 83



DRAWN B.J	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE —— NOT TO BE SCALED		FINISH ——	TITLE REAR PCB (INCLUDING REAR PANEL) CIRCUIT DIAGRAM 1061/1071/1081
DATE 30-3-79.	DATE				DRAWING No. 430295

DRAWING SIZE A1

SHEET 1 OF 1

DRAWING No 400296.
FIRST USED ON 1061 / 1071

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
C	
D	D1, D2, D3, R1 & R2 ADDED IE NEW 1196 PCB BJ 25-8-78
1	RELEASED TO PRODN 25-5-78
2	ECO 784 RIBBON CABLE MODS BJ 6-12-78
3	ECO 849 PCB MODS BJ 4-5-79
4	ECO 867 PCB UPDATE BJ 11-6-79 & ECO 906 572130 CHANGE TO 572130A 605006 CLIPS REMOVED & ECO 876 J3, 4 & 6 NEW GOLD NOT TIN
5	ECO 952 25-10-79 PCB WAS 1196 9A. BJ P.C.B OF J5 MOVED PCB CUT OUT INCREASED IN DEPTH
6	ECO 1000 RIBBON CABLE ASSY WAS 572100/A IL 18 1 80
7	ECO 1102 PCB WAS ISSUE 5 IL 14 4 80
8	ECO 1217 RIBBON CABLE WAS 572130/B VR 16 6 81

1 OFF CABLE CLIP (DATRON PART No 630023)

CUT OFF PART SHOWN BY DOTTED LINE.
STICK ON OPPOSITE SIDE OF P.C.B. TO COVER SOLDER JOINTS OF J3 AS SHOWN DOTTED.

IMPORTANT.

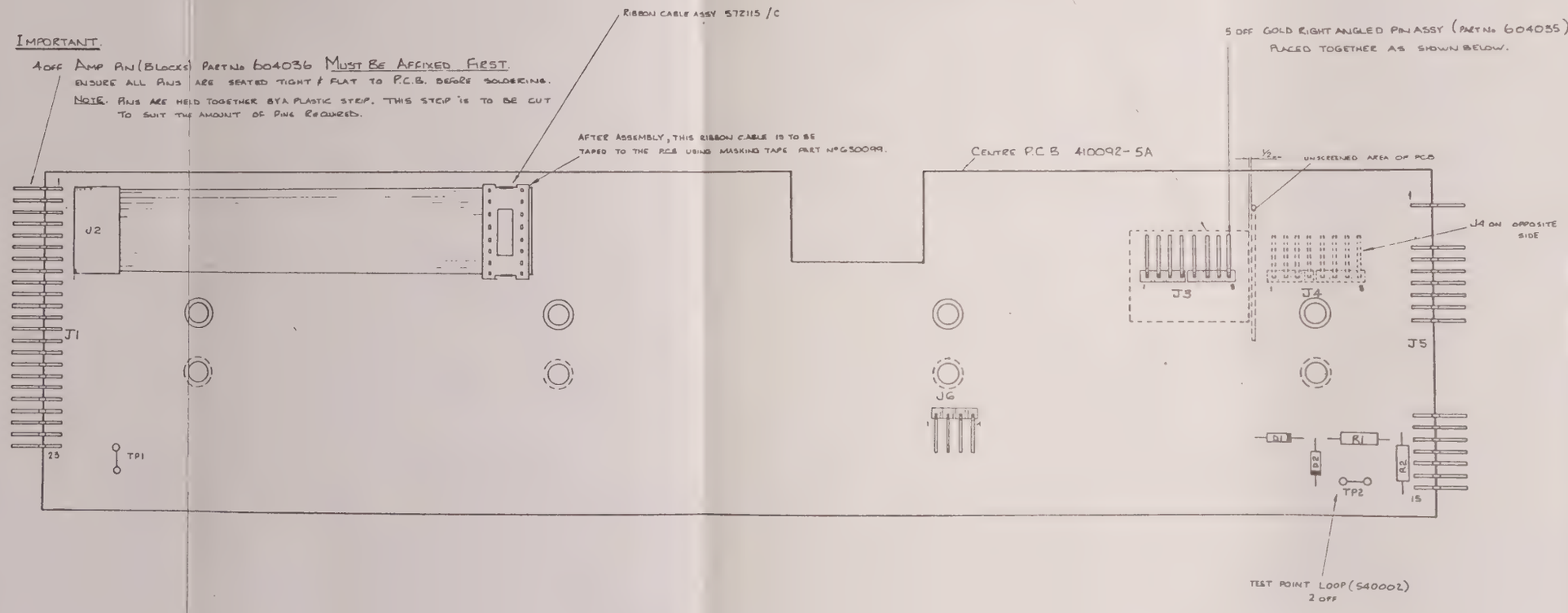
4 OFF AMP PIN (BLOCKS) PART No 604036 MUST BE AFFIXED FIRST.
ENSURE ALL PINS ARE SEATED TIGHT & FLAT TO P.C.B. BEFORE SOLDERING.
NOTE. PINS ARE HELD TOGETHER BY A PLASTIC STRIP. THIS STRIP IS TO BE CUT TO SUIT THE AMOUNT OF PINS REQUIRED.

RIBBON CABLE ASSY 572115 /C

AFTER ASSEMBLY, THIS RIBBON CABLE IS TO BE TAPED TO THE PCB USING MASKING TAPE PART No G30099.

CENTRE P.C.B 410092-5A

5 OFF GOLD RIGHT ANGLED PIN ASSY (PART No 604035) PLACED TOGETHER AS SHOWN BELOW.



DRAWN	CHECKED	DIMENSIONS IN	TOLERANCES	ANGULAR ± °	MATERIAL
B.J.	D.S.	MILLIMETRES	INCH DIMENSIONS		
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED	DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64		FINISH
DATE 25-4-78	DATE		METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED		

datron ELECTRONICS LTD. NORWICH.

TITLE: CENTRE P.C.B ASSEMBLY. 1061/1071/1081

DRAWING SIZE A1

DRAWING No. 400296

SHEET 1 of 2

DRAWING No. 430296

FIRST USED ON 1061 / 1071

THIRD ANGLE PROJECTION

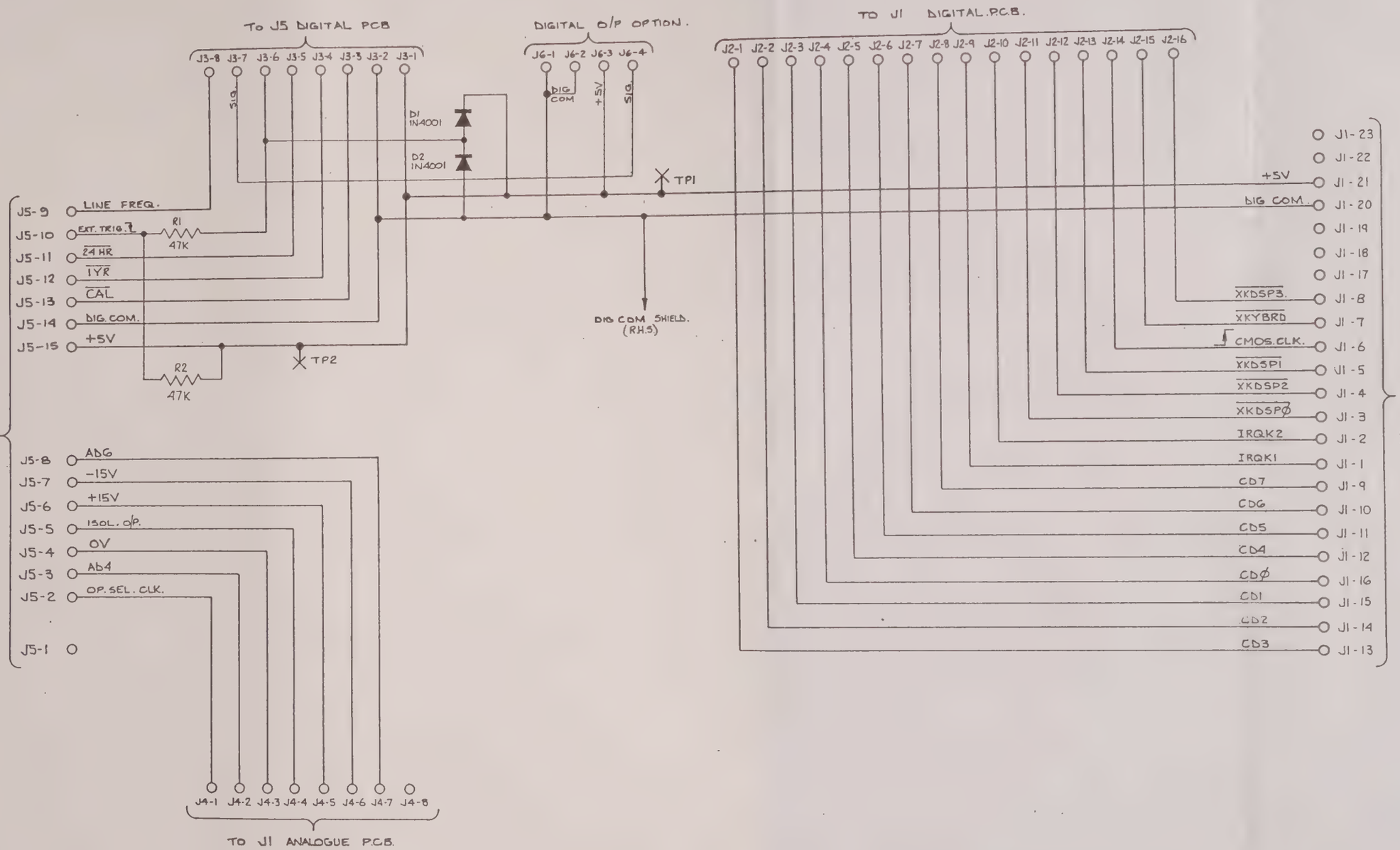
DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. "IN GUARD" TRACKS TO BE ON CCT. SIDE (SHT. 2) & 'OUT OF GUARD' TRACKS TO BE ON THE COMP. SIDE (SHT. 3) OF P.C.B. WHERE POSSIBLE.

ISS.	CHANGES
D	
1	RELEASED TO PRODN 29-9-78.
2	ECO 849. J3-1 SIG JOINED TO J3-7. B.J. 4-5-79.
3	ECO 992. B.J. J3-8 ADDED & J3-1 GU. DELETED 29-10-79



DRAWN BJ.	CHECKED Sch.	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ½° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE CENTRE PCB SCHEMATIC 1061 / 1071 / 1081	DRAWING No. 430296
DATE 30-5-78	DATE					SHEET 1 OF 1

DRAWING No. 400297.
FIRST USED ON 1061/71

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

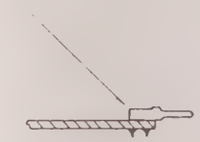
ISS.	CHANGES
C	-
D	NEW ISSUE P.C.B. BJ 24-8-78 MD
1	RELEASED TO PRODN 25-9-78
2	ECO 867 PCB UPDATE BJ 11-6-79 MD

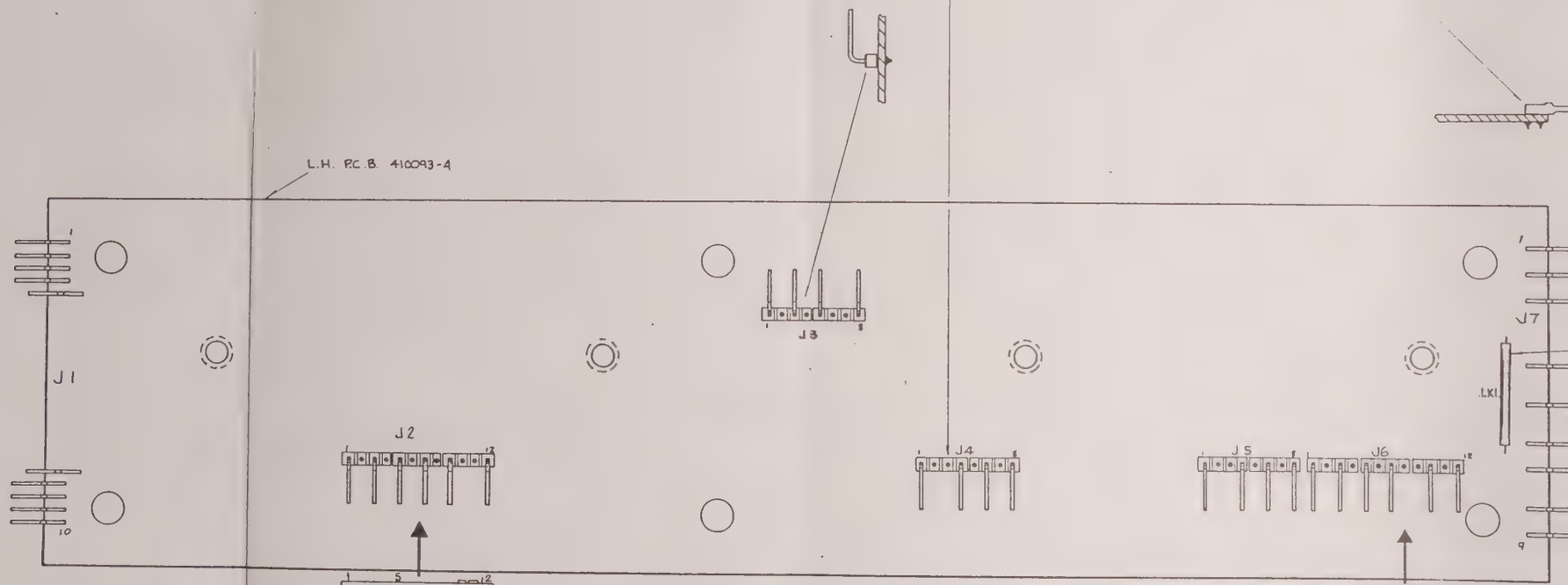
USE THE GOLD 4 EIGHT ANGLED PIN ASSY (PART NO 604035). 12 OFF.
PLACED TOGETHER TO MAKE UP THE REQUIRED AMOUNT OF CONTACTS (SHOWN BELOW).
REMOVE PINS IN PLACES SHOWN BY BLACK DOTS.

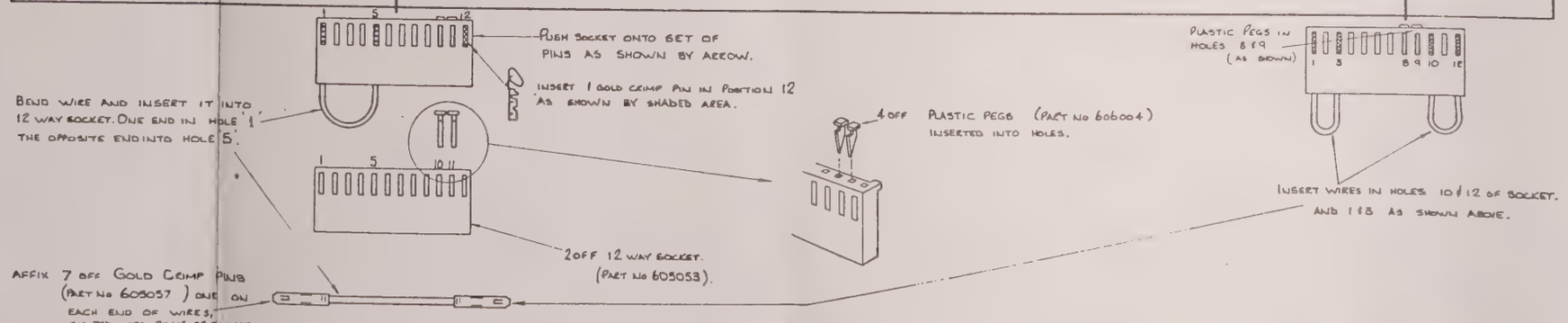
IMPORTANT.

2 OFF. AMP PINS (PART No 604036)
MUST BE AFFIXED FIRST.
ENSURE ALL PINS ARE SEATED TIGHT & FLAT TO P.C.B. BEFORE SOLDERING.

NOTE. PINS ARE HELD TOGETHER BY A PLASTIC STRIP. THIS STRIP IS TO BE CUT TO SUIT THE AMOUNT OF PINS REQUIRED.

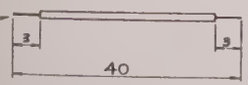





PUSH SOCKET ONTO SET OF PINS AS SHOWN BY ARROW.
INSERT 1 GOLD CRIMP PIN IN POSITION 12 AS SHOWN BY SHADED AREA.
BEND WIRE AND INSERT IT INTO 12 WAY SOCKET. ONE END IN HOLE 1 THE OPPOSITE END INTO HOLE 5.
4 OFF PLASTIC PEGS (PART No 606004) INSERTED INTO HOLES.
PLASTIC PEGS IN HOLES 8 & 9 (AS SHOWN)
2 OFF 12 WAY SOCKET. (PART No 605053).
INSERT WIRES IN HOLES 10 & 12 OF SOCKET. AND 1 & 3 AS SHOWN ABOVE.

AFFIX 7 OFF GOLD CRIMP PINS (PART No 605037) ONE ON EACH END OF WIRES, THE 7TH INTO PIN 12 OF SOCKET.

3 OFF. 40mm OF BROWN WIRE, (PART No 510111) AND STRIP 3mm FROM EACH END (AS SHOWN).



DRAWN	CHECKED
B.J.	DG
TRACED	APPROVED
DATE 21-4-78	DATE

DIMENSIONS IN MILLIMETRES
SCALE 2:1 NOT TO BE SCALED

TOLERANCES ANGULAR ± °
INCH DIMENSIONS
DECIMAL TO 3 PLACES ± .005
DECIMAL TO 2 PLACES ± .010
FRACTIONAL ± 1/64
METRIC DIMENSIONS
DECIMAL TO 2 PLACES ± 1mm
DECIMAL TO 1 PLACE ± 2mm
WHOLE DIMENSIONS ± 4mm
UNLESS OTHERWISE STATED

MATERIAL
FINISH

datron ELECTRONICS LTD. NORWICH.

TITLE 1061/71/81 L.H. P.C.B. ASSEMBLY

DRAWING SIZE A1
DRAWING No. 400297
SHEET 1 OF

DRAWING No. 430297

FIRST USED ON 1061/1071

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

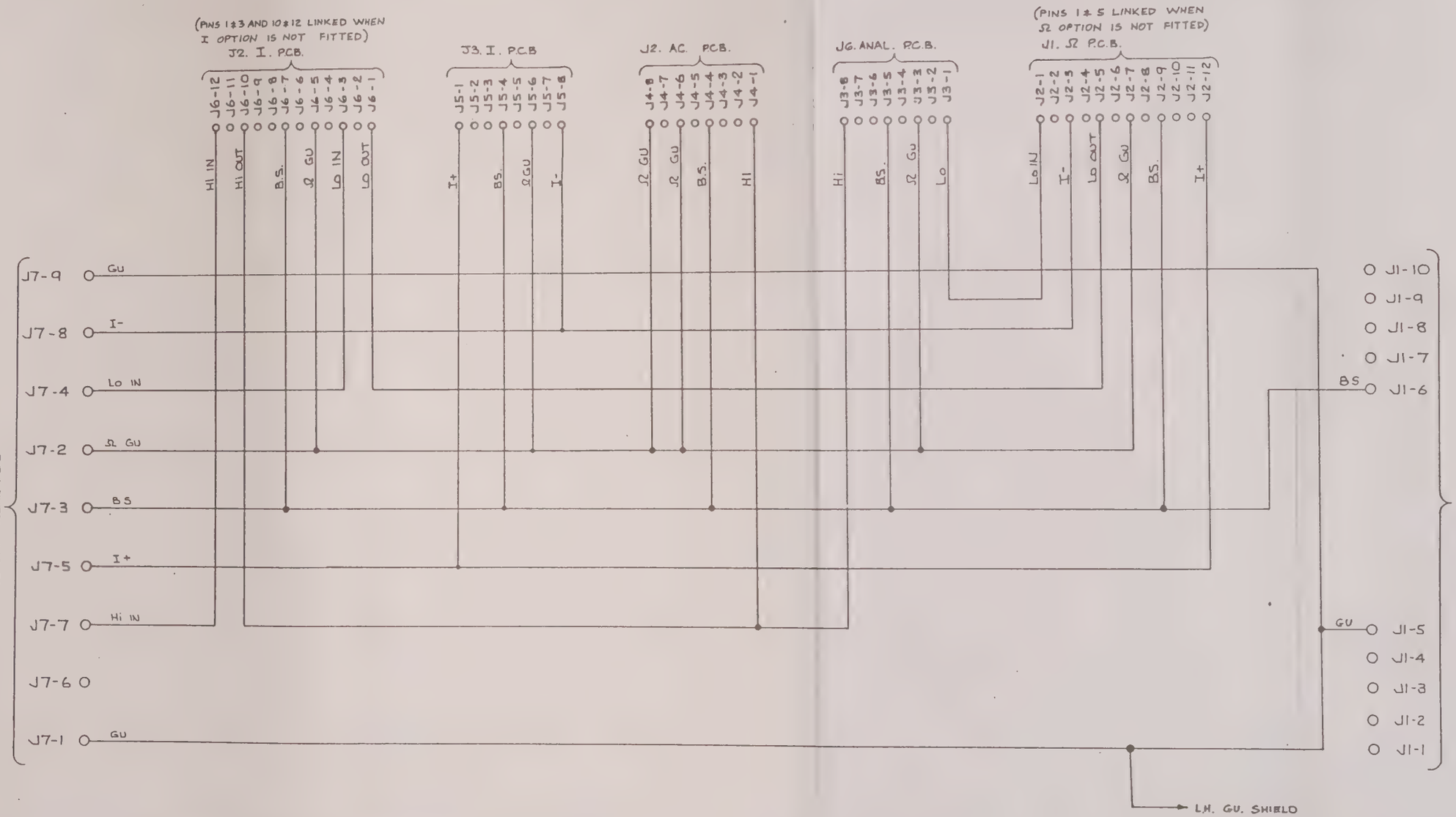
1/ ALL CIRCUIT TO BE ON THE COMPONENT (SHEET 3) SIDE OF P.C.B WHERE POSSIBLE.

2/ INPUT GUARD VIA J7-1 TO PROVIDE ELECTROSTATIC SHIELD ON 'CIRCUIT' (SHEET 2) SIDE OF P.C.B. WHERE POSSIBLE.

3/ B.S, VIA J3-5, TO ENCLOSE Hi AND I+ CIRCUITS ON P.C.B WHERE POSSIBLE.

4/ Ω GUARD VIA J3-3 TO ENCLOSE LO & I- CIRCUITS ON PCB WHERE POSSIBLE.

ISS.	CHANGES
D	
1	RELEASED TO PROD'N. 29-9-78



DRAWN B.J.	CHECKED DCJ	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± 1/2° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE L.H. P.C.B. SCHEMATIC 1061/1071/1081	DRAWING No. 430297 SHEET 1 OF 1
DATE 30.5.78	DATE					

(W116)

DRAWING No	FIRST USED ON
400298	1061/71

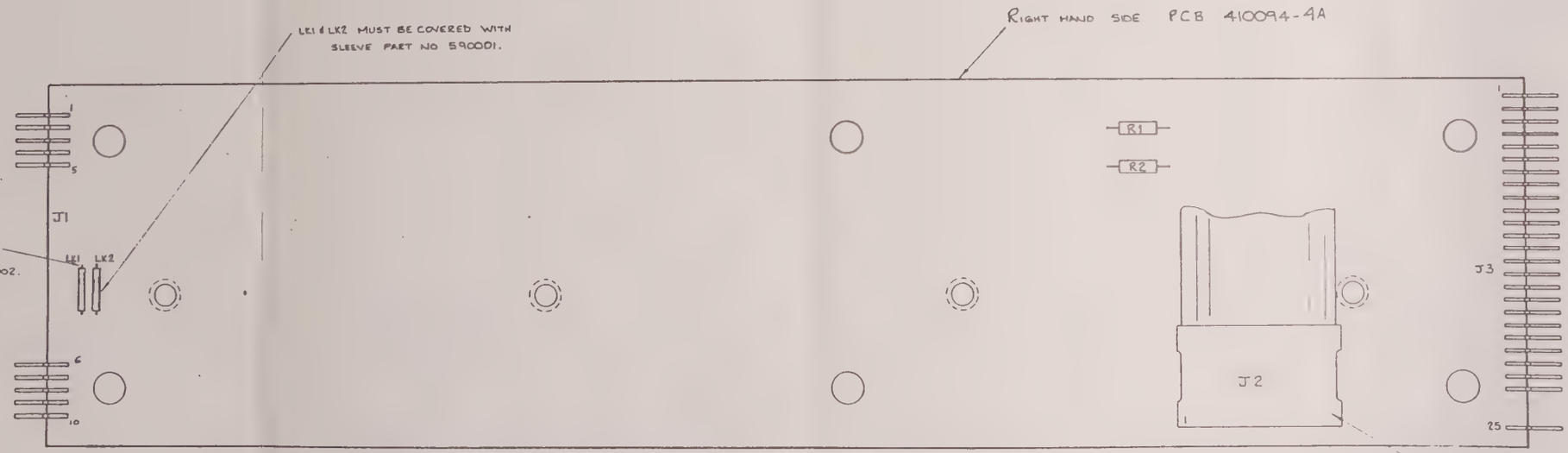
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES	
C		
D	NEW ISSUE P.C.B. B.J. 24-8-78	MD
1	RELEASED TO PRODN 29-5-79.	
2	ECO 850. 4-5-79 B.J. UPDATED PCB. J1-6 WAS REMOVED	MD
3	ECO 8676 5-9-79 B.J. UPDATED PCB CABLE ASSY 400168 CHANGED TO 574270/A	MD
4	ECO 943. 10.9.79 RIBBON CABLE ASSY WAS 574250/A IL	B.J
5	ECO 1217 WE WAS 400365. DR 18-8-81	MD
6	ECO 1474 PCB ISSUE N° WAS 4 IL 3.6.83	MD



RIBBON CABLE ASSY
PART No 574270/C

NOTE. WHEN SOLDERING CABLE PLUG INTO BOARD ENSURE THAT THE PLUG BODY SITS FLAT AGAINST PCB.

AFTER ASSEMBLING P.C.B WRAP THE ABOVE RIBBON CABLE ROUND THE P.C.B AND TAPE DOWN USING MASKING TAPE PART No 630099.

IMPORTANT

AMP PINS (DATRON PART No.604036.) 4 OFF MUST BE AFFIXED TO P.C.B FIRST. ENSURE ALL PINS ARE SEATING TIGHTLY AND FLAT TO P.C.B. BEFORE SOLDERING.

NOTE. PINS ARE HELD TOGETHER BY PLASTIC STRIP. THIS STRIP IS TO BE CUT TO SUIT THE AMOUNT OF PINS REQUIRED.

FOR EXAMPLE. ABOVE 2 BLOCKS OF 5 PINS ARE REQUIRED, THEREFORE STRIP OF 10 PINS TO BE CUT IN HALF.

DRAWN	CHECKED	DIMENSIONS IN	TOLERANCES	MATERIAL
B.J.		MILLIMETRES	INCH DIMENSIONS ANGULAR ± °	
TRACED	APPROVED	SCALE	DECIMAL TO 3 PLACES ± .005	FINISH
DATE 20.4.78.	DATE	2:1 NOT TO BE SCALED	DECIMAL TO 2 PLACES ± .010	
			FRACTIONAL ± 1/64	
			METRIC DIMENSIONS	
			DECIMAL TO 2 PLACES ± 1mm	
			DECIMAL TO 1 PLACE ± 2mm	
			WHOLE DIMENSIONS ± 4mm	
			UNLESS OTHERWISE STATED	

datron ELECTRONICS LTD. NORWICH.

TITLE: 1061/71/81. R.H. P.C.B. ASSEMBLY.

DRAWING SIZE: A1

DRAWING No. 400298

SHEET 1 of 2

DRAWING No.	FIRST USED ON
430298	1061 / 1071

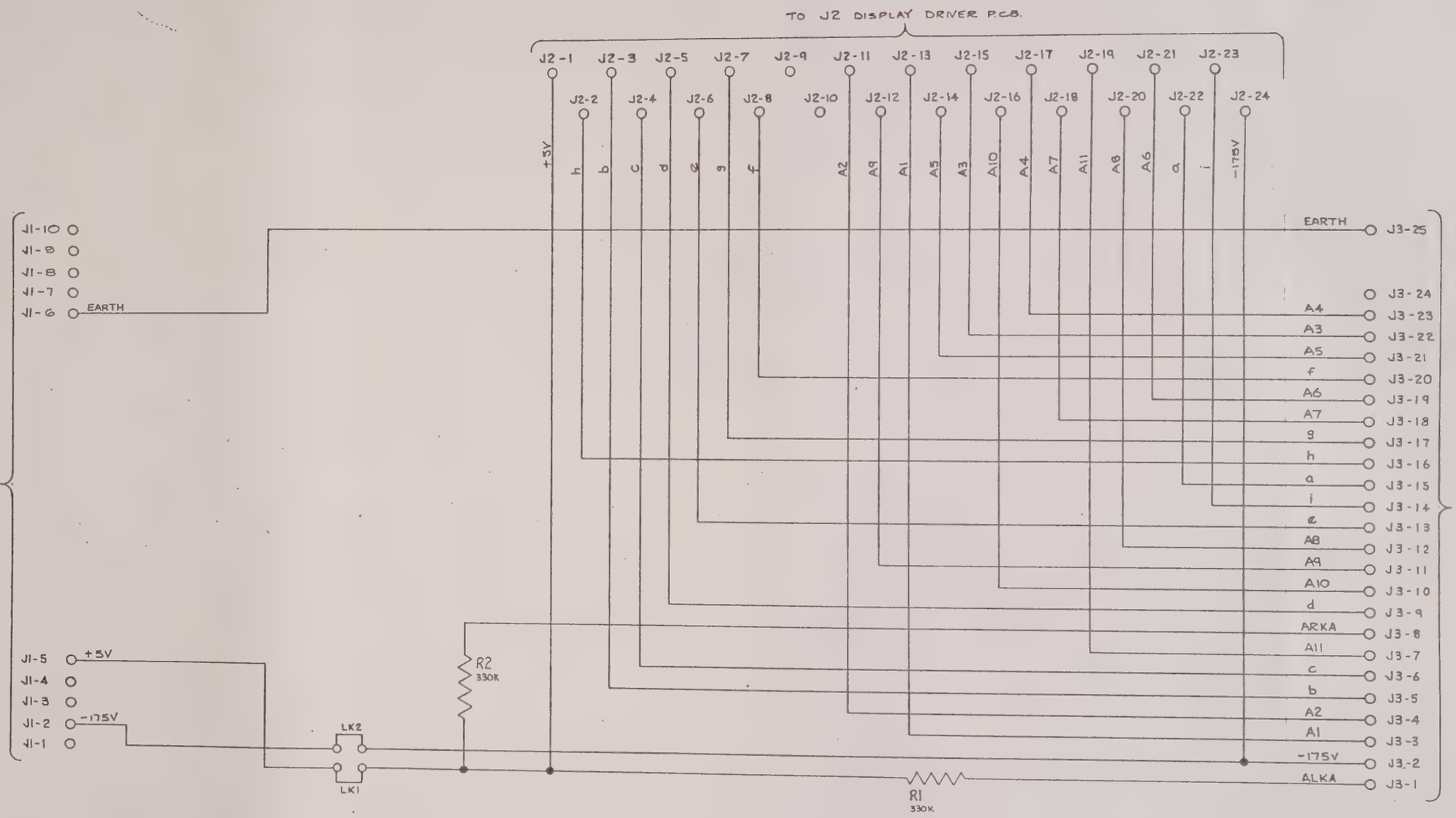
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
D	
1[C]	RELEASED TO PROD 29-5-78
2	DIG COM SCREEN REMOVED. B.J. ECO 850 9-5-79



DRAWN	CHECKED	DIMENSIONS IN	TOLERANCES ANGULAR ± ½°	MATERIAL
B.J.	DCJ	MILLIMETRES	INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	FINISH
TRACED	APPROVED	SCALE		
DATE 26-5-78	DATE	NOT TO BE SCALED		

datron ELECTRONICS LTD. NORWICH.

TITLE: R.H. SIDE P.C.B. SCHEMATIC. 1061/1071/1081

DRAWING SIZE: A2

DRAWING No. 430298

SHEET 1 OF 1

DRAWING No
400304
FIRST USED ON
1061/1071

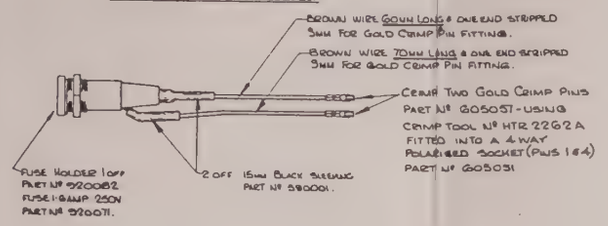
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

CURRENT OPTION FUSE ASSEMBLY

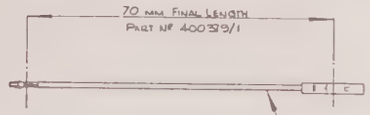

BROWN WIRE 60MM LONG & ONE END STRIPPED 5MM FOR GOLD CRIMP PIN FITTING.
BROWN WIRE 70MM LONG & ONE END STRIPPED 5MM FOR GOLD CRIMP PIN FITTING.
CRIMP TWO GOLD CRIMP PINS PART Nº G05057 - USING CRIMP TOOL Nº HTR 22G2A FITTED INTO A 4 WAY POLARISED SOCKET (PINS 164) PART Nº G05031
FUSE HOLDER 1 OFF PART Nº 920082 FUSE 1·6AMP 250V PART Nº 920071.
2 OFF 15MM BLACK SLEEVING PART Nº 590001.

R25 NOTE

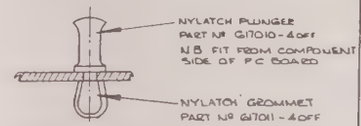
* IMPORTANT NOTE:

R25 RESISTOR TO BE FITTED LAST SO THAT RL3 & RL4 WIRING TO P.C.B CAN BE DONE WITH EASE.

OHMS GUARD LEAD DETAIL



NYLATCH ASSEMBLY
(4 POSITIONS)



SECTION A-A. TYPICAL

MOUNT M1 M3 & M4 ON 16 WAY D.I.L SOCKETS PART Nº G03061 3 OFF

MOUNT M2 ON 14 WAY D.I.L SOCKET PART Nº 603060 1 OFF.

FOR DIFFERENT MAKES OF R19 C7 MUST BE CHANGED ACCORDINGLY SEE SHT 3 FOR FULL DETAILS.

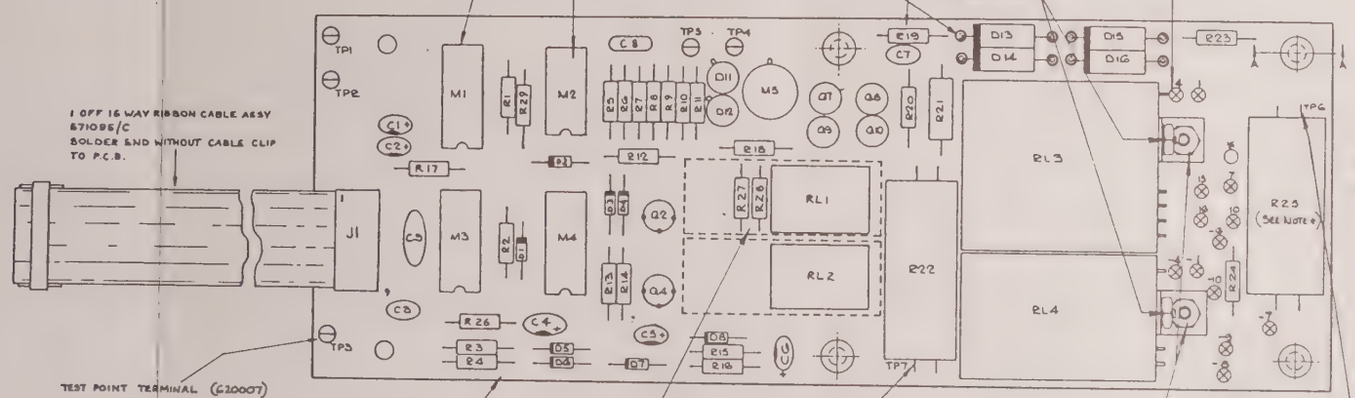
RETAIN RELAY TO BRACKET WITH 3-48UNC NUT PART Nº G15003 & M2·5 WASHER PART Nº 613014 2 OFF EACH (2 POSITIONS)

FIT D13-D16 ON STEATITE BEADS - 3 PER LEAD. G30024, 24 OFF

13 OFF P.C.B TERMINALS PART Nº G20009

12 WAY POLARISED SOCKET PART Nº G05052. 1 OFF.

1 OFF 16 WAY RIBBON CABLE ASSY 571085/C SOLDER END WITHOUT CABLE CLIP TO P.C.B.



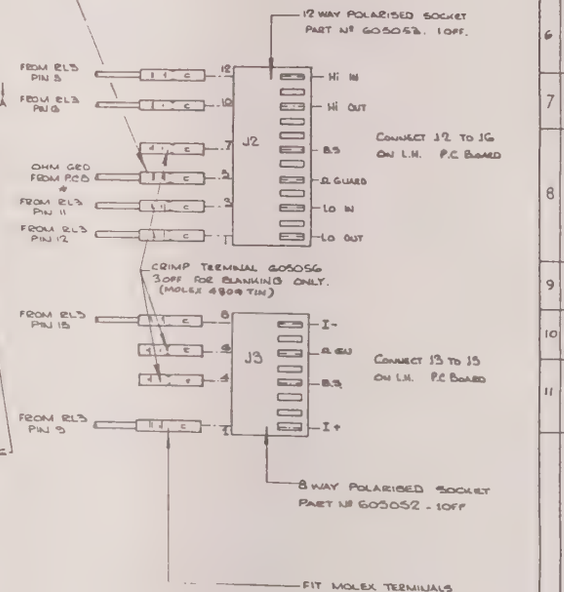
TEST POINT TERMINAL (G20007) 5 OFF

PRINTED CIRCUIT BOARD PART Nº 40104-5A

R27 + 28 ARE NOT USED WITH OMRON ALTERNATIVES

USE R22 LEAD AS TP7

USE R25 LEAD AS TP6



CONNECT J2 TO J6 ON L.H. P.C BOARD

CRIMP TERMINAL G05056 3 OFF FOR BLANKING ONLY. (MOLEX 4804 TIN)

CONNECT J3 TO J5 ON L.H. P.C BOARD

8 WAY POLARISED SOCKET PART Nº G05052 - 1 OFF

FIT MOLEX TERMINALS 4809 GOLD PART Nº G05057. 6 OFF USING CRIMP TOOL Nº HTR 22G2A

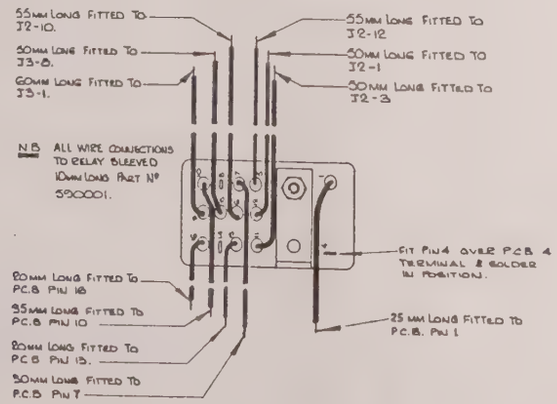
WIRING DETAILS R13

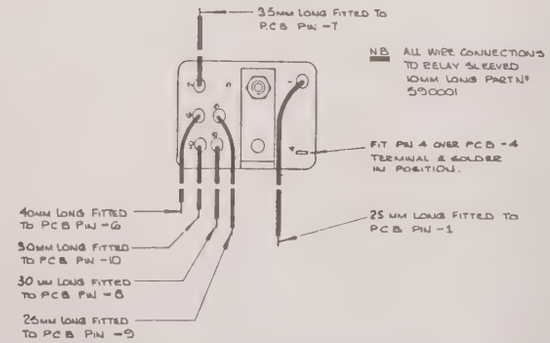
ALL WIRES TO BE 7/2 PTFE COVERED-WHITE PART Nº 540008. LENGTHS AS BELOW.

RETAIN RELAY BRACKETS TO P.C.B WITH 2 × M3 × 6 POZI PAN SCREWS PT Nº G110/34 & 2 × M3 FULL NUT PT Nº G15002 & 2 × M3 SHAKEPROOF WASHERS PART Nº G13008

WIRING DETAILS R14

ALL WIRES TO BE 7/2 PTFE COVERED-WHITE PART Nº 540008. LENGTHS AS BELOW.


55MM LONG FITTED TO J2-10.
50MM LONG FITTED TO J3-8.
60MM LONG FITTED TO J3-1.
55MM LONG FITTED TO J2-12
50MM LONG FITTED TO J2-1
50MM LONG FITTED TO J2-3
N.B ALL WIRE CONNECTIONS TO RELAY SLEEVED 10MM LONG PART Nº 590001.
FIT PIN 4 OVER P.C.B 4 TERMINAL & SOLDER IN POSITION.
80MM LONG FITTED TO P.C.B PIN 18
95MM LONG FITTED TO P.C.B PIN 10
80MM LONG FITTED TO P.C.B PIN 13.
90MM LONG FITTED TO P.C.B PIN 7
25 MM LONG FITTED TO P.C.B. PIN 1


35MM LONG FITTED TO P.C.B PIN -7
N.B ALL WIRE CONNECTIONS TO RELAY SLEEVED 10MM LONG PART Nº 590001
FIT PIN 4 OVER P.C.B -4 TERMINAL & SOLDER IN POSITION.
40MM LONG FITTED TO P.C.B PIN -6
90MM LONG FITTED TO P.C.B PIN -10
30 MM LONG FITTED TO P.C.B PIN -8
25MM LONG FITTED TO P.C.B PIN -9
25 MM LONG FITTED TO P.C.B PIN -1

ISS	CHANGES
A	17 JULY 79 WAS
B	C1 POSITION CHANGED PCB WAS ISSUE 2 R2 & C4 VALUES CHANGED WAS B 21 AUG 79
C	STEATITE BEADS ADDED, TO MOUNT D13 - D16 24 OFF JL 24.9.79
1	RELEASED TO PRODUCTION. 24.9.79
2	ECO 972, 987 RL3 & RL4 WERE ITT RELAY WIRING WAS P.C.B BJ 25-10-79
3	ECO'S 1000, 1012, 1037 & 1038 RIBBON CABLE CLIP WAS 606007 CABLE WAS 571080/A FUSE HOLDER WAS 920047 C7 WAS 7n25 LK1 ADDED R29, R28, D9, D10, Q5 & Q6 DELETED. BJ 4-1-80
4	ECO 1048 R26-R28 ADDED JL 17.1.80
5	ECO 1095, 1110, 1087 J2-7 J3-4 4J3-6 WERE 605057 D17 & D18 REMOVED C18 ADDED JL 18.4.80
6	ECO. 1116 R29 ADDED. CUT TRACK ON PCB BETWEEN M2 & J1-9 PARTS LIST CHANGE TO C2 JL 3.6.80
7	ECO. 1189 OHMS GUARD LEAD WAS 400379/2 JL 17.12.80
8	ECO 1080 R6.9.C9. ADDED R12 WAS 1K R13.7 WERE 12K R5.6.14 WERE 27K R26.27.28 WERE 10K R17 WAS 1M Q1.Q3.AN1.LK1 DELETED JR 25.2.81
9	ECO 1217 J1 WAS 16 WAY SOCKET. JR 18.8.81
10	ECO 1287 R27 + 28 WERE 330 R. JR 24.11.81
11	ECO 1520 D13.D14.D15 AND D16 WERE 200010. PCB WAS 18308.5 JR 10.5.83.

DRAWN W G SMITH	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS: DECIMAL TO 1 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS: DECIMAL TO 1 PLACE ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± .5°	MATERIAL	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE CURRENT P.C BOARD LAYOUT 1061/1071
DATE 18 JULY 79	DATE				DRAWING No. 400304 · SHEET 1 OF 7 · DRAWING SIZE A1

DRAWING No 430 304

FIRST USED ON 1061/1071

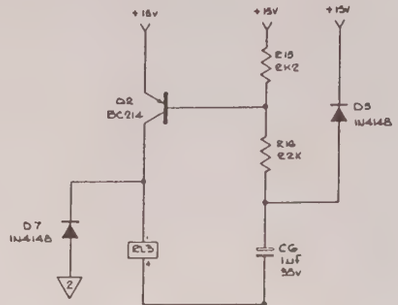
THIRD ANGLE PROJECTION

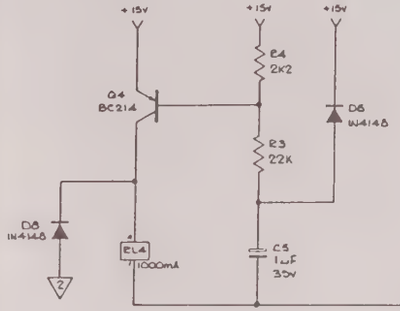
DRAWN IN ACCORDANCE WITH BS 308

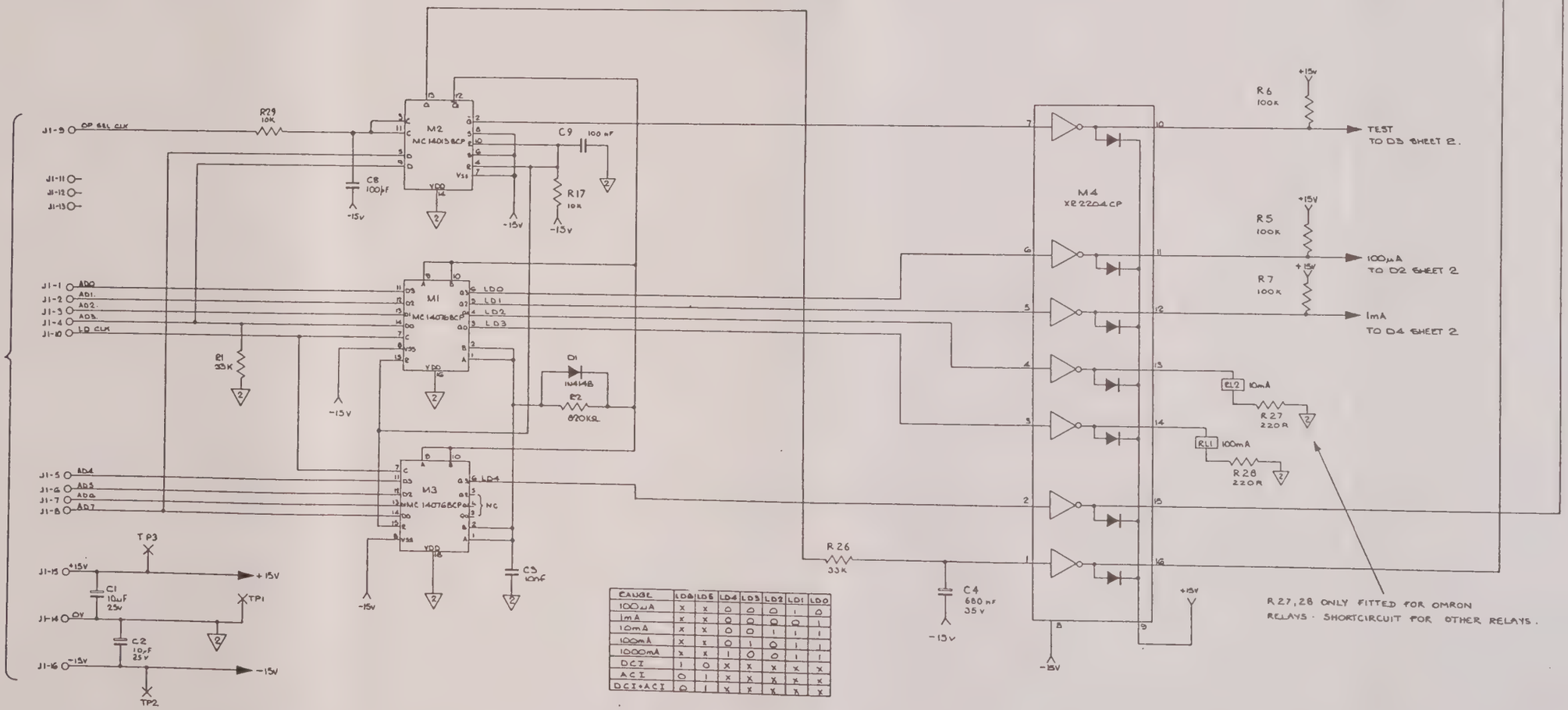
ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES
A	18 JULY 79 W.G.S.
B	R7 was 1MΩ - 000103 C4 was 6nF 250v C28 D6 101103 W.G.S 21 AUG 79
1	RELEASED TO PRODUCTION 24.9.79
2	ECO 1037 & 1038 SEE SHEET 2 BJ
3	SEE SHEET 2 ECO. 1048 IL 18 1.80
4	ECO 1087. 1110 CB ADDED IL 14.4 80
5	ECO 1116 R29 ADDED. CB WAS 1nF. IL 3.6 80
6	ECO 1080 R19, R4, Q1, Q3 + AN1 DELETED. R5-7, C4 MOVED C9, R17, R26-R28 ADDED VR 26 2 81
7	ECO 1257 R27 AND R28 WERE 330R. JR 24-11-81







RANGE	LD6	LD5	LD4	LD3	LD2	LD1	LD0
100μA	x	x	0	0	0	1	0
1mA	x	x	0	0	0	0	1
10mA	x	x	0	0	1	1	1
100mA	x	x	0	1	0	1	1
1000mA	x	x	1	0	0	1	1
DCI	1	0	x	x	x	x	x
ACI	0	1	x	x	x	x	x
DCI+ACI	0	1	x	x	x	x	x

0 = LOGIC '0' = -15V

1 = LOGIC '1' = 0V

x = DONT CARE CONDITION

DRAWN W.G.SMITH	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °
TRACED	APPROVED	SCALE NOT TO BE SCALED	MATERIAL
DATE 20 JULY 79	DATE		FINISH

datron ELECTRONICS LTD. NORWICH.

TITLE CURRENT PC BOARD CIRCUIT DIAGRAM 1061-1071

DRAWING SIZE A1

DRAWING No 430304

SHEET 1 of 2

DRAWING No 430304

FIRST USED ON 1061/1071

THIRD ANGLE PROJECTION

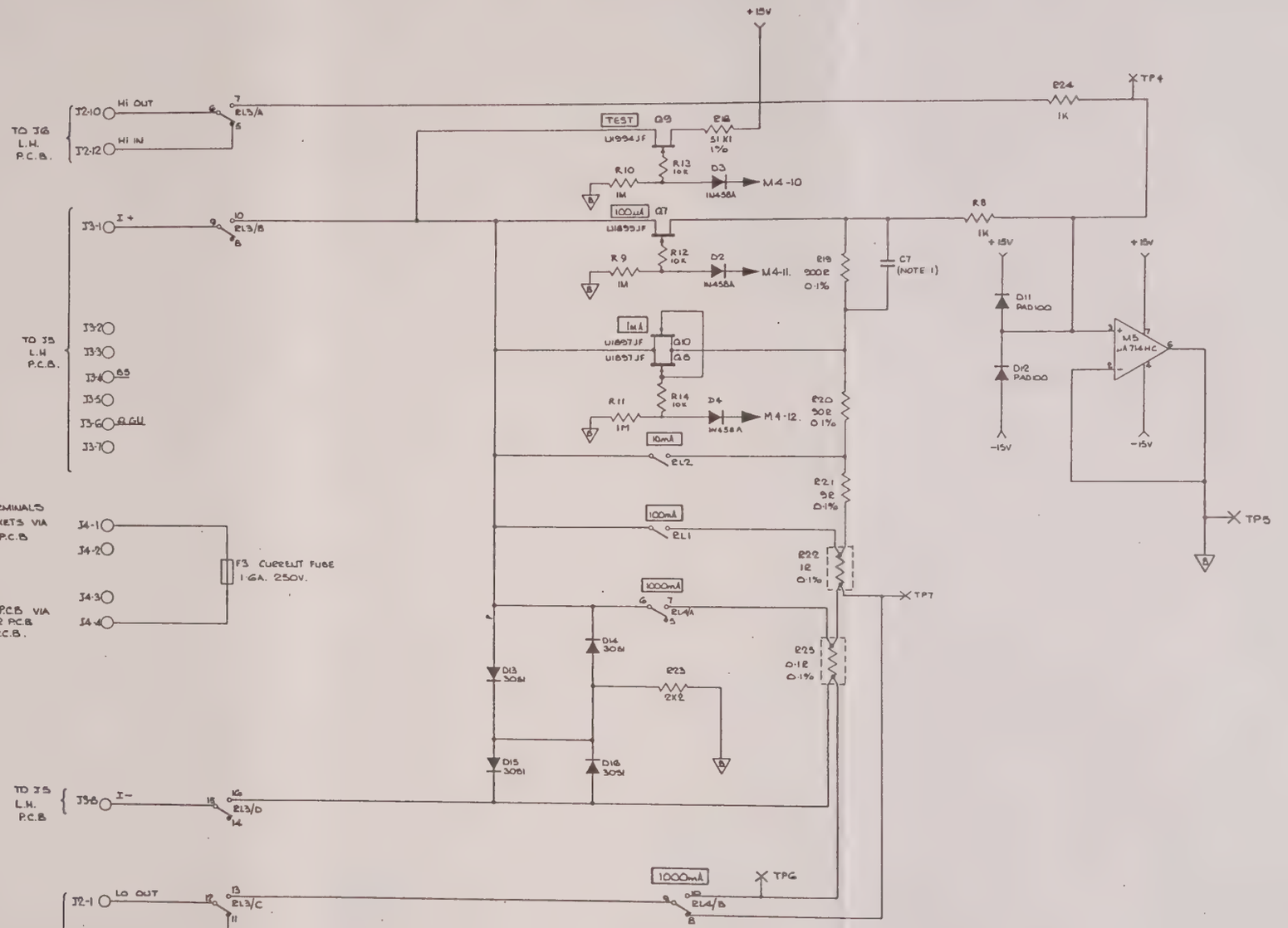
DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. 470pF IF R13 MANUFACTURED BY MANN COMP.
   330pF " " " " KELVIN.

ISS	CHANGES
A	18 JULY 79 W.G.S
B	SEE SHT 1
1	RELEASED TO PRODUCTION 24.9.79
2	ECO. 1037 & 1038. C7 WAS 3n3F. E8,E9,D3,D10,Q5, Q6 DELETED. B.J 4-1-80
3	ECO 1048 R26-R28 ADDED JL 18.1.80
4	ECO 1087 D17, D18 REMOVED JL 14.4.80
5	SEE SHEET 1
6	ECO 1080 R12-14 TRACKED INTO P.C.B. DESIGNATORS REDEFINED. JR 26.2.81
7	SEE SHEET 1



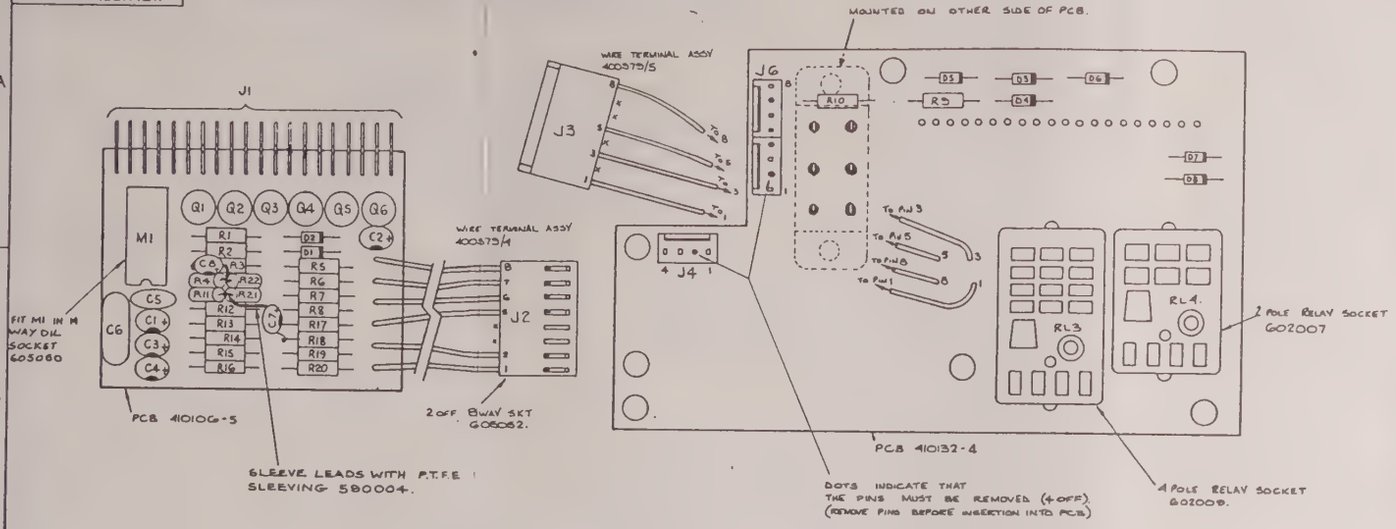
DRAWN W.G SMITH	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm. UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH. DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE CURRENT P.C. BOARD CIRCUIT DIAGRAM - 1061-1071 DRAWING No 430304 SHEET 2 OF 2
DATE 18 JULY 79.	DATE				

DRAWING No 400307

FIRST USED ON 1061/1071

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

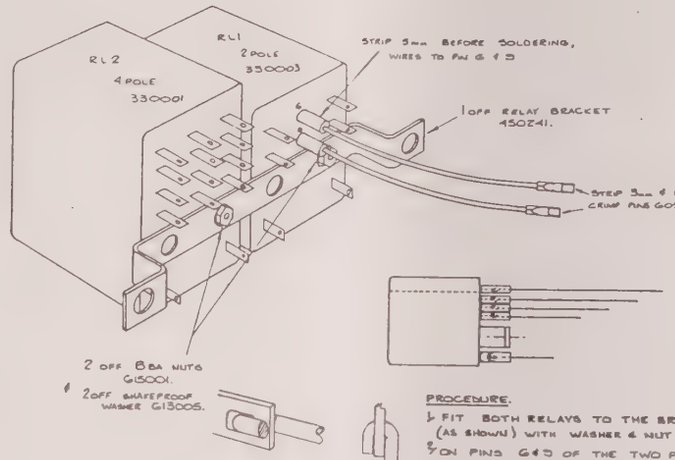

1off SLIDE SWITCH 700069
MOUNTED ON OTHER SIDE OF PCB.
WIRE TERMINAL ASSY 400379/5
WIRE TERMINAL ASSY 400379/4
J1
J2
J3
J4
J6
M1
Q1 Q2 Q3 Q4 Q5 Q6
D1 D2 D3 D4 D5 D6 D7 D8
R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R12 R13 R14 R15 R16 R17 R18 R19 R20 R21 R22
C1+ C2+ C3+ C4+ C5 C6
RL1 RL2 RL3 RL4
To PW3
To PW5
To PW8
To PW1
FIT M1 IN M WAY DIL SOCKET 603060
PCB 410106-5
2off 8way SKT 605062.
SLEEVE LEADS WITH P.T.F.E SLEEVING 580004.
PCB 410132-4
DOTS INDICATE THAT THE PINS MUST BE REMOVED (4off) (REMOVE PINS BEFORE INSERTION INTO PCB)
4 POLE RELAY SOCKET 602008.
2 POLE RELAY SOCKET 602007

ALL BURRS TO BE REMOVED

NOTES

IMPORTANT.

FOR CRIMP PINS USE TOOL Nº HTR 22624.



PROCEDURE.

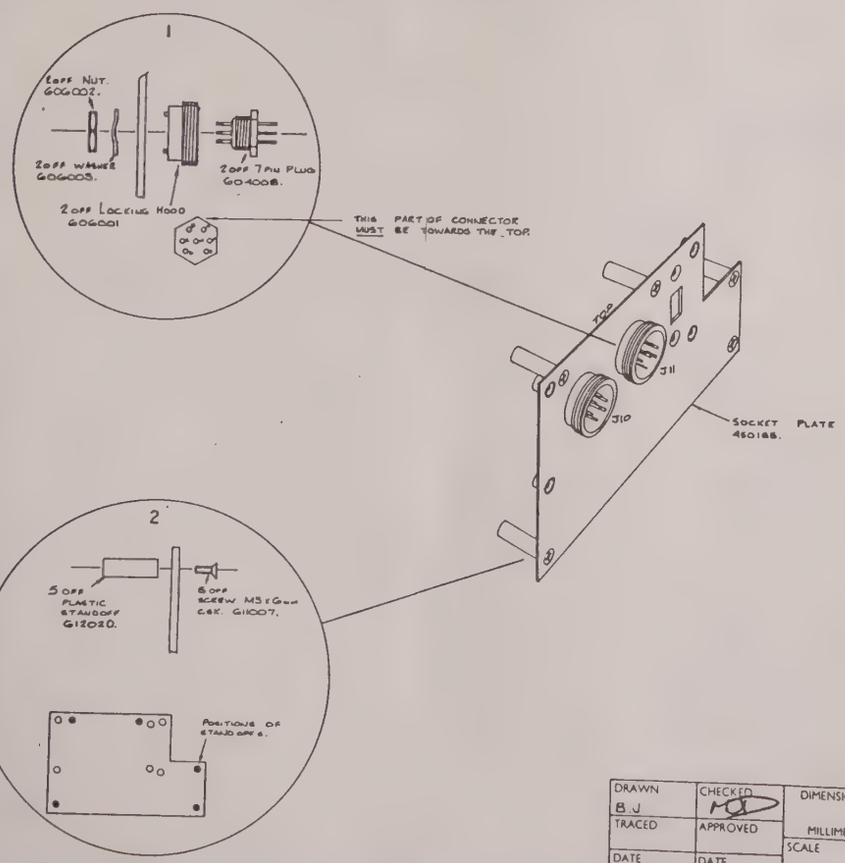
1/ FIT BOTH RELAYS TO THE BRACKET (AS SHOWN) WITH WASHER & NUT.

2/ ON PINS 6 & 3 OF THE TWO POLE RELAY SOLDER 70mm OF WHITE WIRE PART No 540008 SLEEVE WITH ½ PIECE OF 530001 (1st. 10mm).

3/ ALL OTHER CONTACTS ARE TO HAVE APPROXIMATELY 60mm OF 22 SWG TINNED COPPER WIRE PART Nº 540002 & SLEEVE EACH WITH ½ PIECE OF 530001.

4/ STAGGER LENGTHS OF TINNED COPPER WIRE TO ASSIST FITTING TO PCB, AS SHOWN.

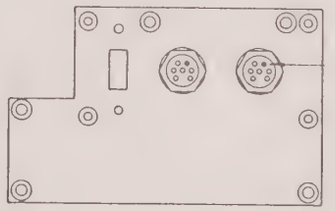
ISS	CHANGES
A	
1	RELEASED EG-3-79
2	LK1 BLACKED BY R10 D00008 DO DELETED E.C.O. 301/1107 3 JAN 79 WBS
3	ECO 972 25-10-79 RL1, RL2, RL5 & RL6 WERE 1TT BJ
4	ECO 981 6-11-79 R9 WAS 33K. BJ R10 WAS 100 R11 & R12 ADDED D7 ADDED D5 DELETED 400379/4 1007 ADDED. PCB5 UP-DATED
5	ECO 1071, 1034 R19 MARKED & WAS 39K NOW 1M. LK1 ADDED. D7 & R10 DELETED R9 WAS 18K. BJ 7-1-80
6	ECO 1085 POLARISING KEY FOR J3 REMOVED J4 WAS 8WAY CONN J5 REMOVED - SEE SHEET 2. IL 22.4.80
7	ECO 1166 1154 R1 WAS 18K R10 R15 R14 R15 R16 R17 R18 R19 R20 C3 C4 C5 C6 D5 D6 D7 Q5 Q6 & LK1 ADDED IN IL 24.9.80
8	ECO 1199 R10 WAS 12K JR 26.2.81
9	ECO 1329 R21, R22, C7 AND C8 ADDED PART L&T CHANGES. JR 8.7.82

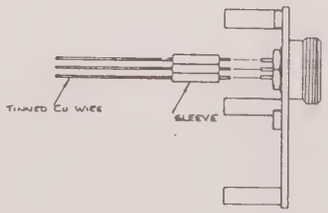


PROCEDURE

1/ ASSEMBLE THE 7 PIN PYE PLUGS. FIRST & ENSURE THAT THE CONNECTORS ARE SECURED IN THE POSITION SHOWN.

2/ ASSEMBLE THE 5 STANDOFF'S IN THE POSITION SHOWN BY THE LARGE DOTS.





PROCEDURE

1/ ALL PINS ON EACH SOCKET EXCEPT PIN 8 (SHOWN SOLID), SOLDER ON 60mm OF TINNED COPPER WIRE, PART Nº 540002 & SLEEVE ALL JOINTS WITH APPROXIMATELY 16mm OF SLEEVING PART No 530001.
THERE SHOULD NOW BE 12 WIRES.

DRAWN B.J	CHECKED MOT	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; ANGULAR ° ' ; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE REAR INPUT / RATIO ASSY
DATE 23-2-79	DATE				DRAWING No. 400307 SHEET 1 OF 8

DRAWING SIZE A1

DRAWING No 400307

FIRST USED ON 10G1/71

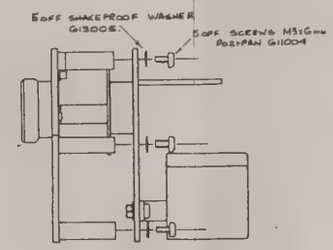
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES	
A		
1	RELEASED 26-3-79	
2	ECO 1080 SHT 1 3 JUNE 79 W.G.S	MD
3	SEE SHT 1 25-10-79 GJ	MD
4	SEE SHT 1. 6-11-79 G.J.	MD
5	SEE SHT. 1. 2-1-80 GJ	MD
6	ECO 1085 J5 WAS 8 WAY CONN. IE 22 & 80	MD
7	SEE SHEET 1 24 3 80	MD
8	SEE SHEET 1 21 2 81	MD
9	SEE SHT 1	MD



3/ BOTTOM VIEW SHOWS THE HOLES IN THE P.C.B (INDICATED BY CROSSES). THESE HOLES ARE FOR THE WIRES FROM THE TWO 7 PIN SOCKETS
IT IS A SUGGESTION THAT THE WIRES ARE STAGGERED TO MAKE ASSY EASIER (AS WIRING THE RELAYS).

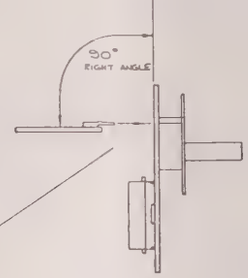
PROCEDURE.

① THE WIRES SHOULD LINE UP WITH THE APPROPRIATE HOLES. IE. WIRE FROM PIN A OF SOCKET SHOULD BE INSERTED INTO HOLE A, WIRE FROM PIN 'C' TO HOLE 'C' AND SO ON.

② WHEN THE WIRES ARE INSERTED IN THE APPROPRIATE HOLES. SECURE THE 2 ASSYS TOGETHER WITH THE M3 x 6mm POZI-PAN SCREWS & SHAKEPROOF WASHERS; SCREWED IN THE 5 STANDOFFS. (AS DETAILED)

③ GENTLY PULL ON THE TIN COPPER WIRES SO THE WIRE IS TAUT; THEN SOLDER & CROP IN THE USUAL MANNER.

2/ INSERT CONNECTORS AS SHOWN.
SOLDER ALL 20 AMP PINS & CROP LEGS IN USUAL MANNER.
THE BOARD IS TO BE AT 90° WHEN SOLDERED IN.



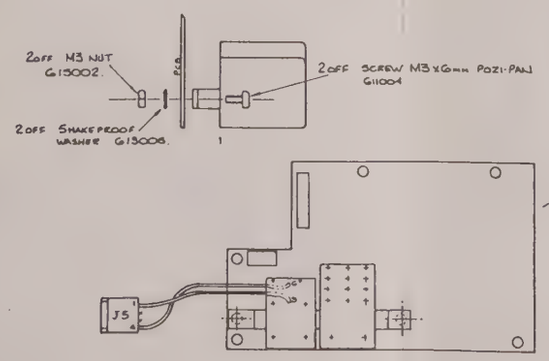
1/ SKETCH BELOW SHOWS THE 2 RELAY & BRACKET ASSEMBLY (FROM SHEET 1). THE CROSSES INDICATE HOLES IN THE PCB INTO WHICH THE WIRES FROM THE RELAYS ARE INSERTED.

PROCEDURE

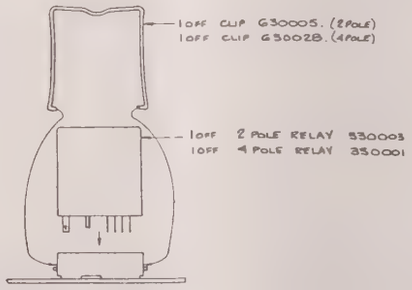
① INSERT THE WIRES INTO THE APPROPRIATE HOLES IN THE PCB (WIRES CUT AT DIFFERENT LENGTHS TO AID ASSEMBLY).

② WHEN THE WIRES ARE ALL IN PLACE SECURE THE BRACKET TO THE PCB USING THE SCREWS, WASHERS & NUTS. SHOWN BELOW.

③ SOLDER & CROP WIRES IN THE USUAL MANNER. INSERT THE CRIMP PINS INTO SOCKET J5 PIN 6 OF RELAY TO PIN 6 OF J5 & PIN 5 TO PIN 1 AS SHOWN BELOW.



FINISHED ASSEMBLY.



4/ THE LAST PROCEDURE IS TO PLUG IN THE 4 & 2 POLE RELAYS & HOLD IN PLACE BY THE CLIPS PROVIDED AS DETAILED IN ABOVE SKETCH & FINISHED VIEW.

DRAWN B.J.	CHECKED MD	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH ——	TITLE REAR INPUT / RATIO ASSY.	DRAWING No. 400307 SHEET 2 OF 8
DATE 2-3-79	DATE					

1	2	3	4	5	6	7

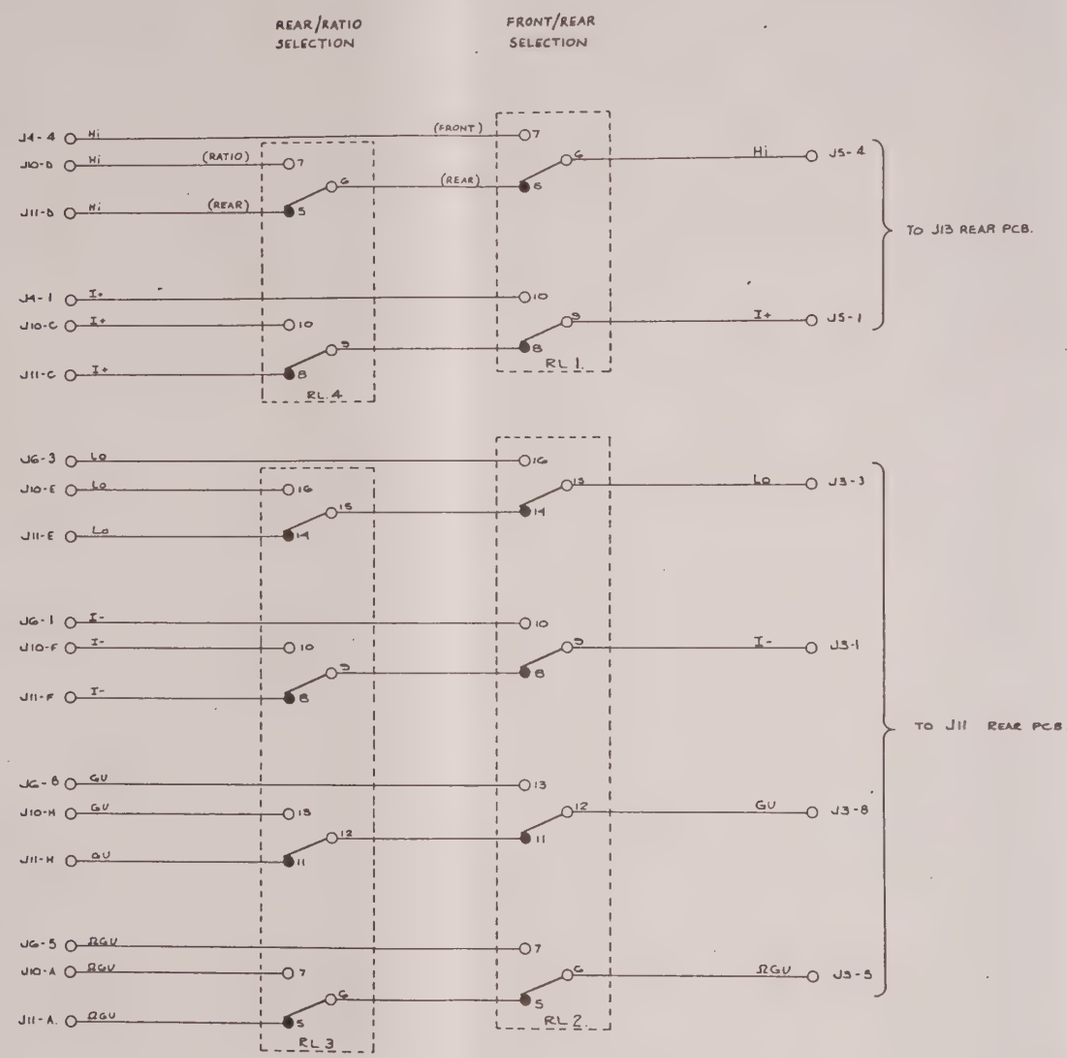
DRAWING No. 430307
FIRST USED ON 1061/1071

THIRD ANGLE PROJECTION

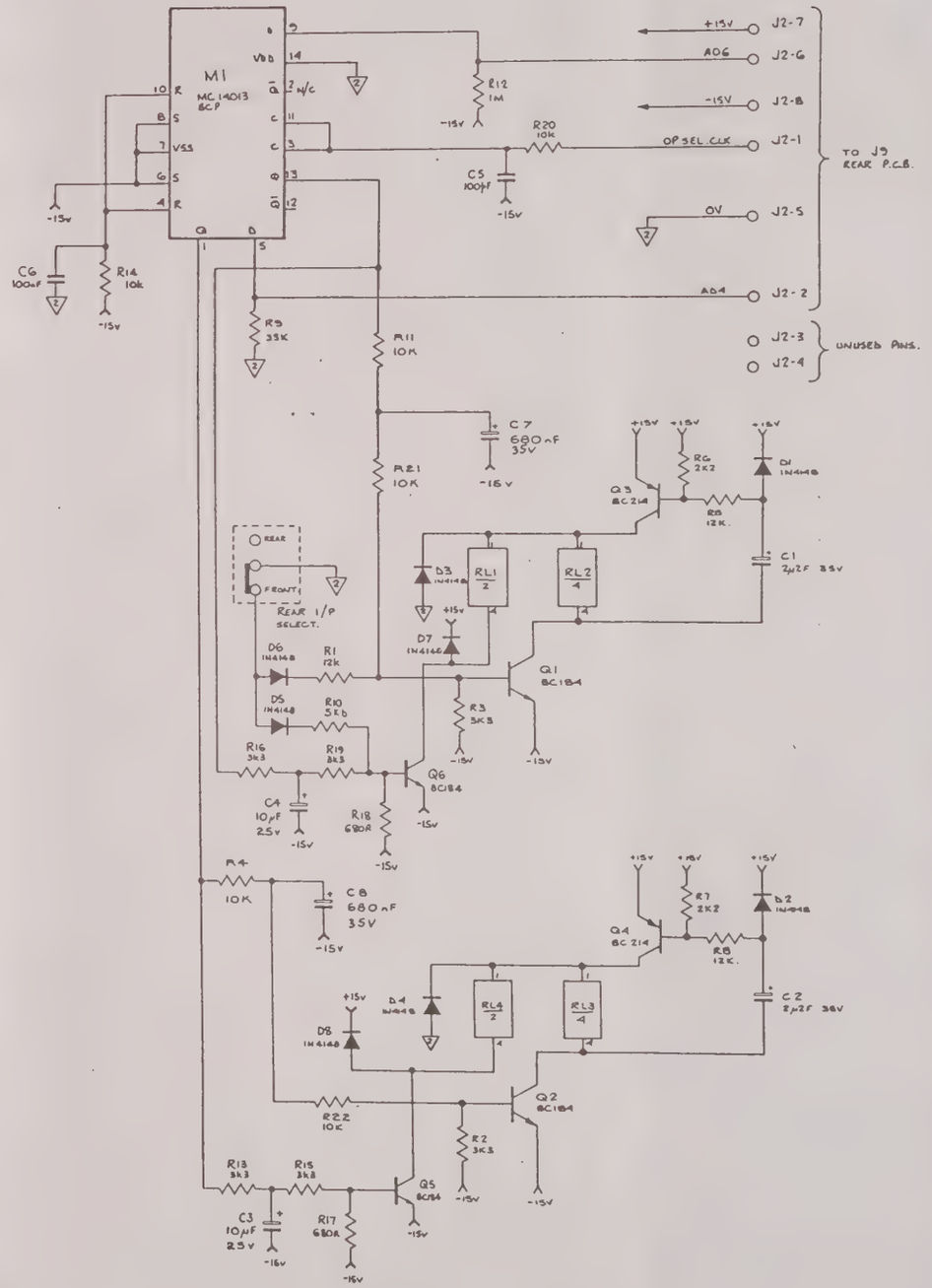
DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES



NOTE: J4 & J5 ARE FROM THE FRONT PANEL TERMINALS.
J10. IS THE RATIO INPUT SOCKET ON REAR PANEL.
J11. IS THE REAR INPUT SOCKET ON REAR PANEL.



ISS.	CHANGES
1	RELEASED 26-3-79.
2	LK1 REPLACED BY R10. 10K. ECO 1002/1007 5 JUNE 79 W.G.S.
3	ECO 981. 6-11-79. G.J. R9 WAS 33K. R10 = 10K. R11 & R12 ADDED D7 ADDED IC#C 4066 U3/4 ADDED. D5 DELETED.
4	ECO 1091 & 1094. G.J. R18 WAS JOINED TO J2-6 & M1-14. LK1 ADDED. R9 WAS 10K D7 & R10 DELETED. 2-1-80
5	ECO 1085 BS. BETWEEN J4-1 & J5-1 REMOVED PINS OF J4 & J5 RE-NUMBERED PCB 4 WAY CONNECTORS IL 22 4 80
6	ECO. 1166, 1154 R1 WAS 18K R10 R13 R14 R15 R16 R17 R18 R19 R20 C3, C4, C5, C6, D5, D6, D7, D8, Q5, Q6 ADDED IL 24 9 80
7	ECO 1199 R10 WAS 12K JR 24 2 81
8	ECO 1329 C3 AND C4 WERE 6µ8F. R4 AND R11 WERE 18K. C7, C8, R21 AND R22 ADDED. JR 8-7-82.

DRAWN	CHECKED	DIMENSIONS IN	TOLERANCES	MATERIAL
B J		MILLIMETRES	INCH DIMENSIONS ANGULAR ± ° ' DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	
TRACED	APPROVED	SCALE		FINISH
DATE 9-3-79	DATE	NOT TO BE SCALED		

datron ELECTRONICS LTD. NORWICH.

TITLE: REAR INPUT / RATIO CIRCUIT.

DRAWING SIZE A1

DRAWING No. 430307

SHEET 1 OF 1

DRAWING No. 400308.

FIRST USED ON 1061 - 1071

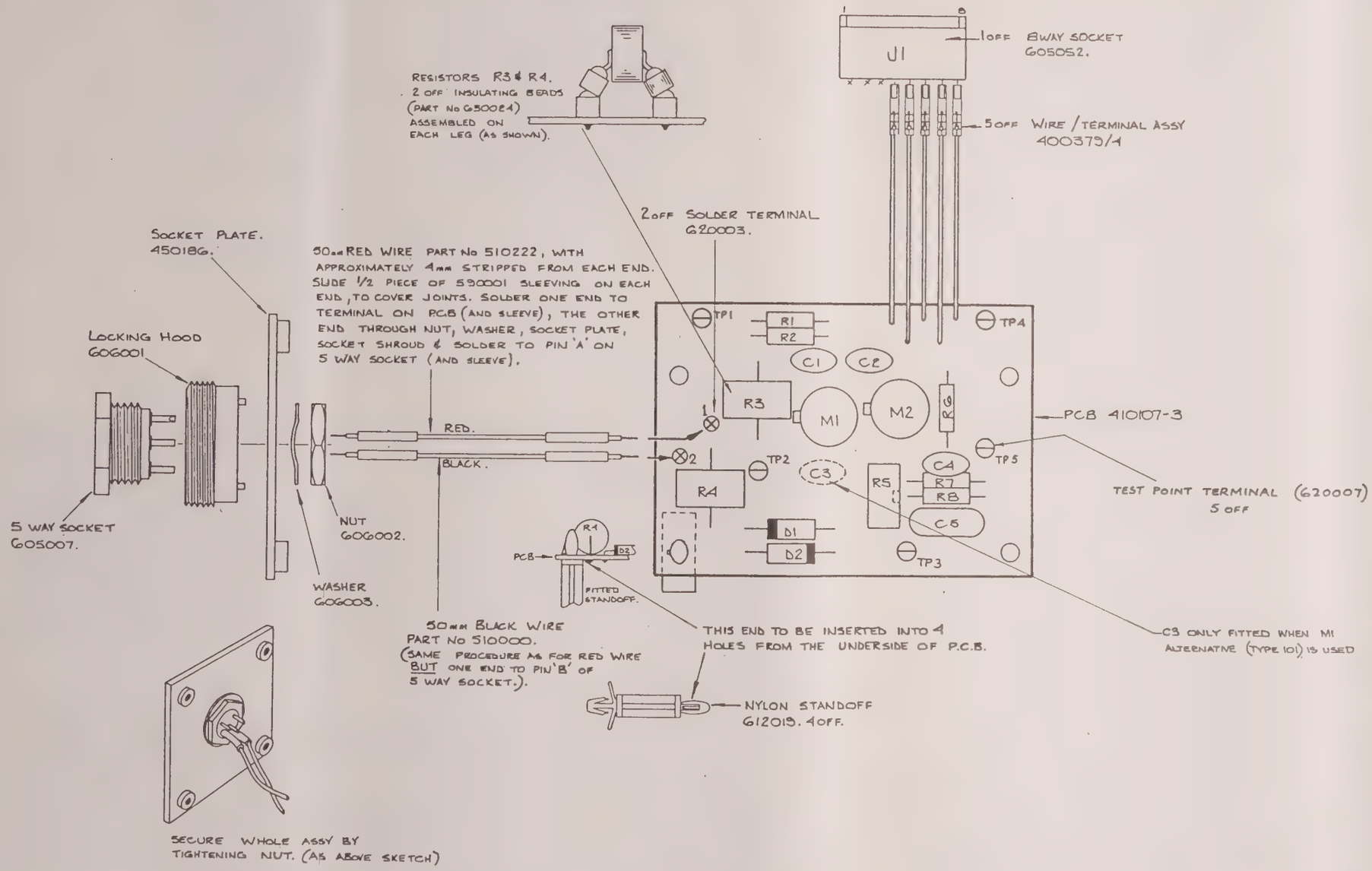
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
1	RELEASED. 27-DEC-78. W.G.S.
2	POSITION OF TAG ON M1 & M2 CORRECTED. D2 POSITION REVERSED. ECO. 907 20.6.79 IL.
3	PARTS LIST CHANGE TO R7 ECO. 945 10.9.79 IL.


RESISTORS R3 & R4.
2 OFF INSULATING BEADS (PART No G30024) ASSEMBLED ON EACH LEG (AS SHOWN).
J1
1 OFF 8WAY SOCKET G05052.
5 OFF WIRE / TERMINAL ASSY 400379/1
2 OFF SOLDER TERMINAL G20003.
SOCKET PLATE. 45018G.
50mm RED WIRE PART No 510222, WITH APPROXIMATELY 4mm STRIPPED FROM EACH END. SLIDE 1/2 PIECE OF 590001 SLEEVING ON EACH END, TO COVER JOINTS. SOLDER ONE END TO TERMINAL ON PCB (AND SLEEVE), THE OTHER END THROUGH NUT, WASHER, SOCKET PLATE, SOCKET SHROUD & SOLDER TO PIN 'A' ON 5 WAY SOCKET (AND SLEEVE).
LOCKING HOOD G06001
RED.
BLACK.
5 WAY SOCKET G05007.
NUT G06002.
WASHER G06003.
TP1
TP2
TP3
TP4
TP5
R1
R2
C1
C2
R3
M1
M2
R6
PCB 410107-3
C4
R7
R8
C3
R5
C5
R4
D1
D2
TEST POINT TERMINAL (G20007) 5 OFF
PCB
R4
D2
FITTED STANDOFF.
50mm BLACK WIRE PART No 510000. (SAME PROCEDURE AS FOR RED WIRE BUT ONE END TO PIN 'B' OF 5 WAY SOCKET.).
THIS END TO BE INSERTED INTO 4 HOLES FROM THE UNDERSIDE OF P.C.B.
C3 ONLY FITTED WHEN M1 ALTERNATIVE (TYPE 101) IS USED
NYLON STANDOFF G12019. 4 OFF.
SECURE WHOLE ASSY BY TIGHTENING NUT. (AS ABOVE SKETCH)

DRAWN B.J.	CHECKED M.W.Farrow	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ½° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ∓ 4mm UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH	TITLE ANALOGUE OUTPUT P.C.B ASSY. 1061/1071/1081	DRAWING No. 400308 / SHEET 1 OF 4
DATE 27-11-78	DATE					

DRAWING No. 430308

FIRST USED ON 1061 - 1071

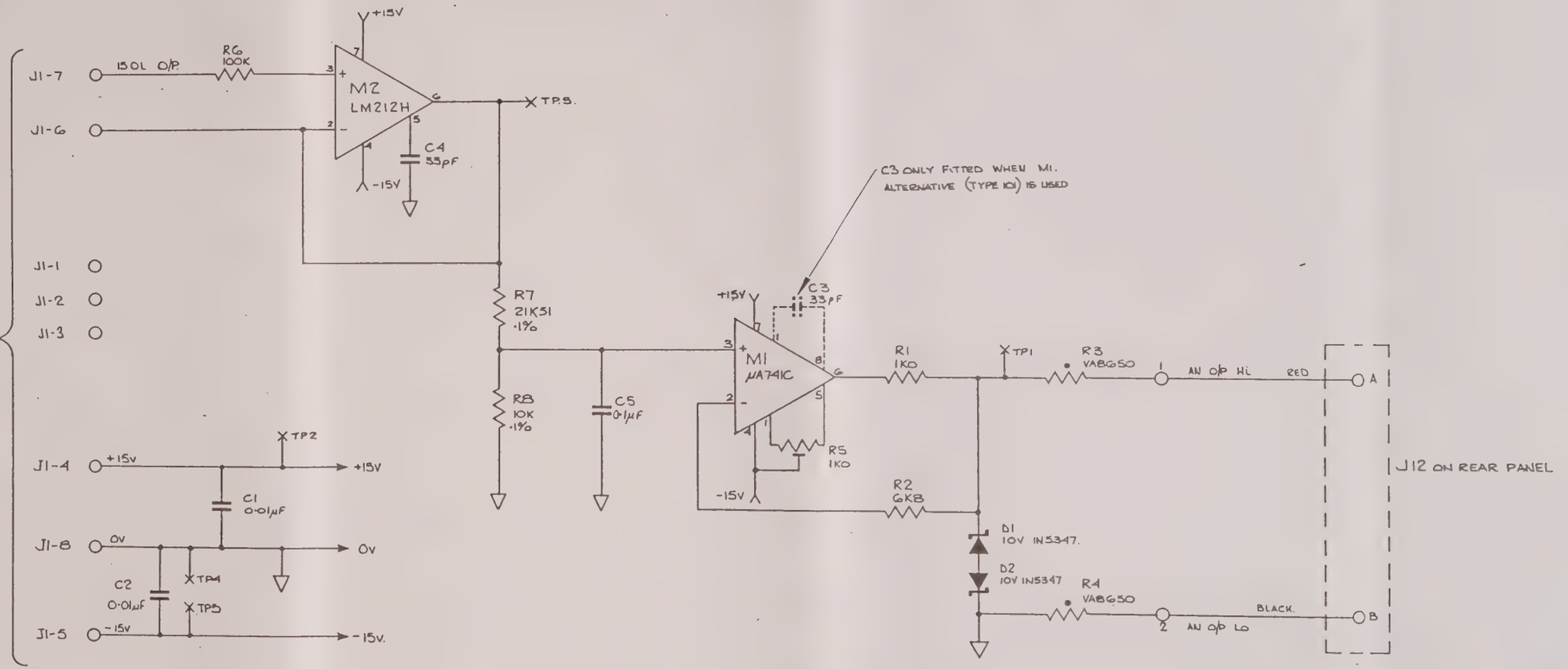
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
1	RELEASED. 27-DEC-78. W.G.S
2	R7 WAS 21k73 ECO. 945 10.9.79 IL BJ



DRAWN B.J.	CHECKED MSD	DIMENSIONS IN — MILLIMETRES	TOLERANCES ANGULAR ± ½° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ∓ 4mm. UNLESS OTHERWISE STATED	MATERIAL —	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE — NOT TO BE SCALED		FINISH —	TITLE ANALOGUE OUTPUT CIRCUIT. 1061/1071/1081	DRAWING No. 430308 / SHEET 1 OF 1
DATE 29-11-78	DATE					

J W 1162

DRAWING No 400328
FIRST USED ON 1061

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. SOLDER PCB TERMINAL LUG PART N° G20005 5 OFF SHOWN AS ⊗
2. FIX RELAY BRACKET 459112 TO PCB WITH SCREW M3 X 6mm POZI-PAN G11004. 1 OFF. SHAKE PROOF WASHER G13005. 1 OFF NUT M3 FULL HEX BTREL G15002. 1 OFF.

ALL SINGLE FET LEGS IN THE ORDER SHOWN

ALL TRANSISTOR LEGS IN THE ORDER SHOWN.

MOUNT ALL OTHER IC'S ON 16 WAY DIL SOCKETS 605061 . 12 OFF

1 OFF 16 WAY RIBBON CABLE ASSY PART NO 871075/C SOLDER END WITHOUT CABLE CLIP TO P.C.B.

NOTE ALL TEST LINKS TO BE AS TEST POINTS.

MOUNT M12, M16, M17, M18, M19, M26, M27, M29 ON 14 WAY DIL SOCKETS 605C60. 8 OFF

1mm P.T.F.E SLEEVING 550004

WIRE/TERMINAL ASSY 400379/1 7 OFF.

4 OFF RED SOCKET CLIPS PART No G06005

TEST POINT TERMINAL 620007 25 OFF.

6 OFF CERAMIC BEADS PART NO 630036

ALL LINKS AND TEST POINTS USE TINNED COPPER WIRE — PART No 540002

NOTE :- FIT E50G AS SHOWN ON DRG FIT TCR50G AS SHOWN WITH FLAT ON THE LEFT.

TCR 50G

* NYLATCH - GROMMET 617011 - 8 OFF

* NYLATCH - PLUNGER 617010 - 8 OFF NB FIT FROM COMPONENT SIDE OF PCB.

THIS NYLATCH MUST BE FITTED AFTER ATE.

SECTION A-A

FSV TERMINAL PART No G02001 6 OFF

COLOUR DOTS TO BE ON TOP

3mm APPROX.

PCB 410095-8A

LOOP D37 & D60 LEADS AS ABOVE.

MOUNT M22, M35, Q35, Q36 AND Q37 ON 8 WAY DIL SOCKET No G05059 - 5 OFF

ENSURE THAT ALL RESISTORS MARKED * ARE MOUNTED 3mm OFF PCB

10 OFF UNSLEEVED LINKS A, B, C, D & E. FIT LINKS B & C AFTER ATE.

CLOVERLEAF TERMINALS PART No G20005 7 OFF.

NOTE ! ALL RELAY WIRES USE 7/12 PTFE INSULATED WIRE 540008. STRIP 5mm FROM EACH END.

TO SOLDER TERMINAL A WIRE LENGTH 25 mm

TO STANDOFF PIN AT B. WIRE LENGTH. 35 mm

WIRE 25 mm LONG JOINING THESE TERMINALS TOGETHER SLEEVED. (AS SHOWN)

TO SOLDER TERMINAL E. WIRE LENGTH 40 mm

VIEW IN DIRECTION OF ARROW

SLEEVE ALL JOINTS WITH ½" PIECE OF 550004

TO STANDOFF PIN AT C. WIRE LENGTH 35 mm

1 OFF NUT G15005

1 OFF WASHER M3 INT SHAKEPROOF. G13014

JOIN BOTTOM TERMINAL OF RELAY INTO HOLE IN PCB WITH TINNED COPPER WIRE. 540002

WIRE/TERMINAL ASSY 400379/2 4 OFF

8 WAY POLARISED SOCKET 605052 - 2 OFF

30 mm 7/2 PTFE WIRE 540008. NOT FITTED WITH LARGE ALTERNATIVE RESISTORS - SEE BELOW.

REMOVE THIS RELAY PIN AND MOUNT RELAY ABOVE PCB SO THAT STUB DOES NOT MAKE CONTACT

VIEW SHOWING POSITION FOR LARGE ALTERNATIVE RESISTORS.

ISS	CHANGES
A	
B	UPDATED PCB.
1	RELEASED TO PRODN 23-11-78
2	R46 WAS 10K (1000R) R54 & R55 WAS 330R (330R) MT WAS 140R (280R) ECO 807 26-FEB-79 BJ.
3	COMPONENT VALUES CHANGED. REFER TO ECO 826 & ECO 834 6.4.79. IL.
4	COMPONENT VALUES CHANGED. REFER TO ECO 883. 23-4-79. BJ.
5	D57 REMOVED & R54, LT CHANGE TO R24. ECO. 886/810/804 IL. 19. 6. 79
6	ECO 925. D40 WAS BEAN RE-INSERTED. BJ. 16-7-79.
7	ECO. 937 PCB. RAISED To 410.6A IL. 4.9.79
8	ECO. 959 C59 ADDED ACROSS M16-8 & M18-4 IL. 20.9.79
9	ECO 963, 972 & 984. C 30 REMOVED. CHANGES R182, R183, C58, C56, M36 & RL1 BJ 12-10-79
10	ECO 987, 993 & 997. RELAY WIRES WERE PVC INSULATED. R57 WAS 47R. R127 WAS 10M (1+4mm) R219, 220, 221 & 222 WERE 1M C213, 4 R227 & C60 ADDED BJ 20-10-79
11	ECO 1000 SOCKET CLIP G06005 WAS G06007 IL 9.1.80
12	ECO 1049 1072. 1077 PARTS LIST CHANGED IL 27.2.80
13	ECO. 1098. 1107 CLOVERLEAFS WERE 18 OFF. D58 REMOVED & REPLACED BY LK3 R217, C594 LINKS 1-4 'S1' DELETED. PARTS LIST CHANGED IL 16.4.80
14	ECO 1050. 1117. 1128 PARTS LIST CHANGED C8 RELOCATED. D68 ADDED. R41/R88 DELETED. IL 2.6.80
15	ECO. 1168. 1144 Q35 Q36 WERE 2N3856 R18, R35, R36. Q24 & Q25 ADDED IL 24.9.80
16	ECO 1141 1182 1184 1188 1190 1191 1194 1192 Q35 Q36 NOW MOUNT ON DIL SOCKET 605059 R236 & R237 ADDED. WIRE TERMINAL 1/38 TO J1-7 WAS PITTED TO J1-8 TRACK FROM WHERE J1-7 WAS JOINS PCB CUT ON COMP SIDE. LK21 ADDED. FURTHER PARTS LIST CHANGES. IL 18.12.80
17	ECO 1217 D3 WAS 16 WAY SOCKET. UK 16-8-81
18	ECO 1244. 1248 PARTS LIST CHANGED IL 2.11.81
19	ECO 1245. 1273 M7 WAS 8200R3 UK 5-1-82 RM7
20	ECO 1282. PCB WAS 155 B.A. RL2 WAS 8300R8. M36 WAS 2.1E. C38 WAS 47 PF. R23 WAS 2.7K. R26 WAS 8.2K. R27 WAS 680R. M23 WAS LAN TYPE R233, 234, 238, 239, 230, 224, 228, 229, 231. D71, D72, C64, Q37 AND 1 MOUNTING PIN ADDED. 3 CLOVERLEAVES DELETED. UK 22.7.82
21	ECO 1460 R240 ADDED IL 25.5.83
22	ECO 1512 R241 ADDED UK 17-8-83
23	ECO 1645, 1649. PCB WAS 1568. R18E = PSV ONLY. Q36 = 2N3906 R18 = 2N3906 NOTE FOR NYLATCH SEE ABOVE. IL 27.2.84
24	ECO 1655 D73, D74, R242 ADDED IL 19.9.84

DRAWN	CHECKED	DIMENSIONS IN	TOLERANCES	ANGULAR ±	MATERIAL
BJ		— MILLIMETRES	INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64	
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED	METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm		FINISH
DATE 12-7-78	DATE		UNLESS OTHERWISE STATED		

datron ELECTRONICS LTD. NORWICH.

TITLE 1061 ANALOGUE PCB ASSEMBLY

DRAWING No. 400328

DRAWING SIZE A1

SHEET 1 OF 24

DRAWING No 430328
FIRST USED ON 1001

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

= SIGNAL COMMONS

= ANALOGUE COMMON

SS	CHANGES
3	RE-DRAWN 17-4-79 ECO 876, 0344 BJ ECO 933
4	SEE SHEETS 2+3
5	SEE SHEET 5
6	ECO 958 SEE SHT. 5 IL 20-8-79
7	ECO 965 M36 WAS M714. ECO 986 C30 OMITTED BJ 12-10-79.
8	ECO 1035 (SHT 1) R127 WAS 10M (SHT 3) R220 221K 222 WERE 1M (SHT 4) R219 WAS 1M R57 = 47K ECO ADDED (SHT 3) R227 " IL 10-12-79
9	ECO 1049 R123 WAS 2M17 1% R125 WAS 2M37 1% R134 WAS 470K R135 WAS 5M32 1% R136 WAS 4M75 1% R138 WAS 1M82 1% IL 27 2 80
10	ECO 1098 R219 & 220 CHANGED IL 16 4 80
11	ECO 1050, 1117, 1128 SEE SHTS 2, 3 & 4 IL 2 6 80
12	ECO 1168 SEE SHEET 3 IL 23 8 80
13	ECO 1184 1188 1186, 1190 1192 J1-8 WAS 6V. J1-7 ADDED (ADG). R150 WAS 100K SEE ALSO SHTS 2+5 IL 16 12 80
14	ECO 1244 1248 SEE SHEETS 3 & 4 IL 3 11 81
15	ECO 1273 R127 WAS A CR25 UP 7-1-82
16	ECO 1282 R224 R225 R226 R233 R238 C64 AND Q37 ADDED M36 WAS 212. TL8 WAS TP13. UP 23-7-82
17	ECO 1460 R240 ADDED, SHT 2 IL 25 5 83
18	ECO 1512 R241 ADDED. UP 17-8-83
19	SEE SHEET 2.
20	ECO 1659 D13 ADDED RP 19.9.84

FROM SHT 2 +BS -BS

TO J3 L.H. PCB — JG-1 JG-3 JG-5 — R122 22K, R121 27K, R120 27K, R119 27K, R149 3M3, R150 3M3, R145 10K, RL1 FRAME, R148 100K

FROM R127 — R123 2M2, R124 121K 1%, R125 2M2, R126 3K65 1%, R138 1M2, R136 3M9, R135 2M7, R134 390K, R151 2M, TL2, M30 LM124, R137 22K, C27 0.01µF, R128 TSP102, R153 10K, R152 FSV (100R NOM), RB4 13R7 1%

FROM SHT 5: SEL CLK J1-1, AD4 J1-2, 130% OP J1-4, AD6 J1-7, TP1, +15V, C1 10µF 25V, J1-5, MECCA, TP70, TP71, D13 TVS 5·5, C2 10µF 25V, J1-10, J1-8, -15V, TP2, Ib COMP → ANZ-1 SHT 5. To J4 CENTRE PCB. To J5-12 SHT 3 0V

TP22, R233 1K, C47 0.1µF, M3 741HC, D39 IN4584A, R241 330M, M23 (CA3080) 3300 Ω, R138 10K 1%

D20 IN4584A, R150 200K, R141 18M, R140 18M, M31 LF411, C29 47pF, C28 47pF, TP13

R128 22M1 1%, R129 22M1 1%, R133 33M, R147 7K5, R146 2K3, R144 10K, R238 100R, RL2, R143 100M, C31 1nF

Q12 LM394, Q13 U1954E, Q14 U1954E, Q16 U1954E, Q15 U1954E, D18 6V8 BZY88C, D19 6V8 BZY88C, TP34, R142 2K7, R127 10M 1%, TO R123, TO R154 SHT 2

R192 1M, D17 IN4584A, R130 3K6, Q11 U5114, R131 3K6, R133 1M, D16 IN4584A

Q18 U1954E, Q19 U1954E, TL1, R135 220R, D22 8V2 BZY88C, D23 11V BZY88C, C41 0·15µF, R194 121K 1%, R195 61K1 1%, C40 0·15µF, C35 0·15µF, R193 100K, R192 1M5, C30 1nF, +15V, R224 22K1 1%, R225 22K1 1%, M36 741HC, TP28, R180, R179, R178, Q37 SUB56M, C39, C64 1nF, R226 22K, -15V

* THESE COMPONENTS ARE ALL PART OF INPUT FILTER KIT 440067.

Q8 U5114, R116 1M, FROM D19 SHT 2, R108 10K 0·1%, R109 10K 0·1%, R114 2M21 1%, R112 6K8, TO J1-4, TL8, ISOL O/P R34 SHT 3, C58 3300pF, Q9 U5114, R117 1M, FROM D23 SHT 2, Q10 U5114, R118 1M, FROM D38 SHT 2, R110 9K, R115 1M 1%, Q6 U5114, R113 1M, FROM D23 SHT 2, R111 1K, Q5 U5114, R96 MTK 12, R95 10M, D11 IN4584A, TEST → M20-5 SHT 5

DRAWN BJ	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES (ANGULAR + ,°) INCH DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm. UNLESS OTHERWISE STATED	MATERIAL —	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH —	TITLE D.C. ISOLATOR. 1001.	DRAWING No 430328. SHEET 1 OF 5
DATE 5-4-79.	DATE					

DRAWING No 430328

FIRST USED ON 1061.

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

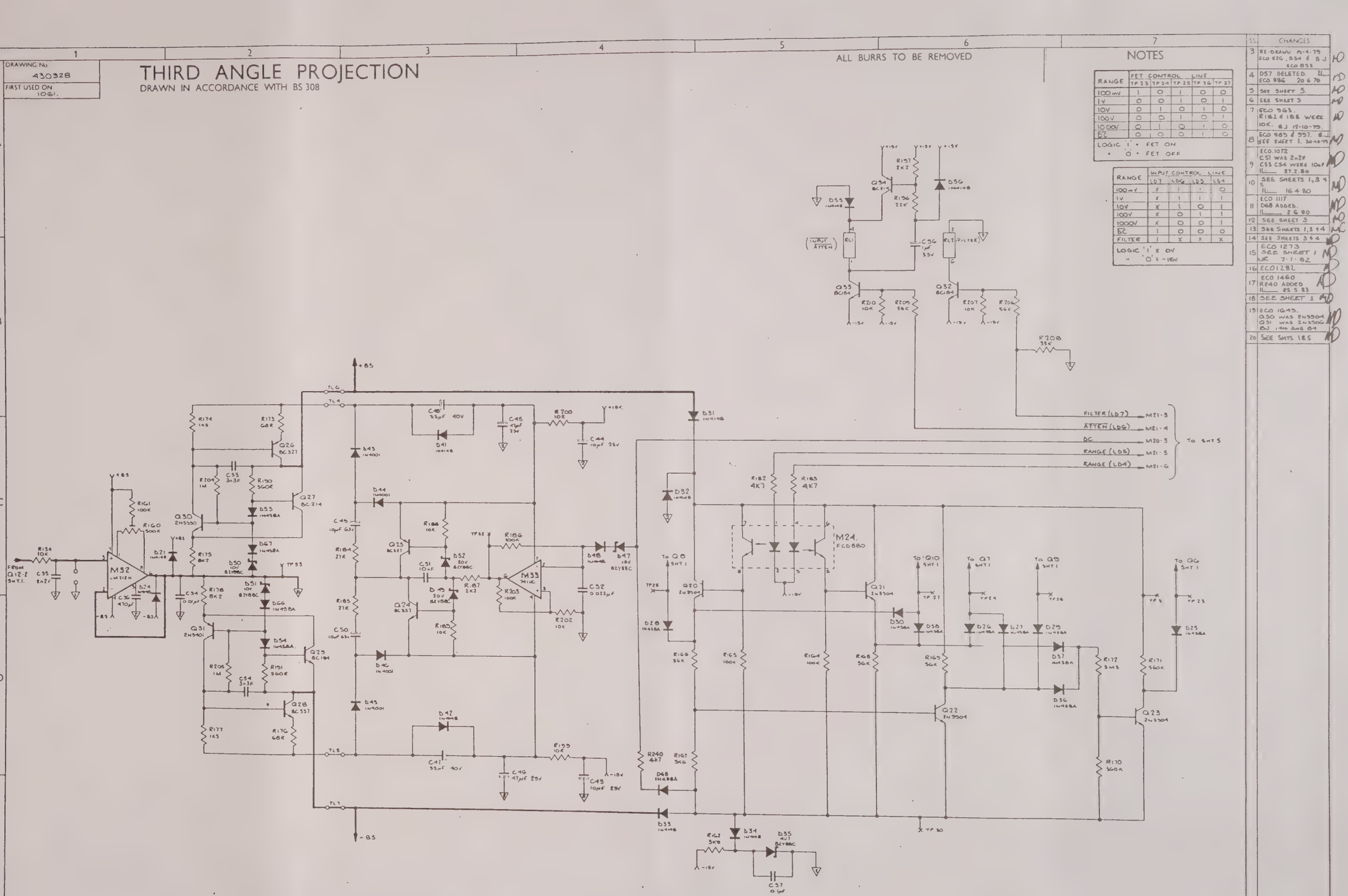
RANGE	FET CONTROL LINE				
	TP 23	TP 24	TP 25	TP 26	TP 27
100 mV	1	0	1	0	0
1V	0	0	1	0	1
10V	0	1	0	1	0
100V	0	0	1	0	1
1000V	0	1	0	1	0
DC	0	0	0	1	0

LOGIC '1' = FET ON
'0' = FET OFF

RANGE	INPUT CONTROL LINE			
	LD7	LD6	LD5	LD4
100 mV	X	1	1	0
1V	X	1	1	1
10V	X	1	0	1
100V	X	0	1	1
1000V	X	0	0	1
DC	1	0	0	0
FILTER	1	X	X	X

LOGIC '1' = 0V
'0' = -15V

SS	CHANGES
3	RE-DRAWN 8-4-79 ECO 826, BS4 & BJ ECO 853
4	DS7 DELETED. IL ECO 886 20 6 80
5	SEE SHEET 5.
6	SEE SHEET 3
7	ECO 963. R182 & 183 WERE 10K. BJ 12-10-79
8	ECO 985 & 997. IL SEE SHEET 1. 30-10-79
9	ECO 1072 C51 WAS 2n2F C55 C54 WERE 10nF IL 27.2.80
10	SEE SHEETS 1, 3 & 5 IL 16 4 80
11	ECO 1117 D68 ADDED. IL 2 6 80
12	SEE SHEET 3
13	SEE SHEETS 1, 3 & 4
14	SEE SHEETS 3 & 4
15	ECO 1273 SEE SHEET 1 IL 7-1-82
16	ECO 1281
17	ECO 1460 R240 ADDED IL 25 5 83
18	SEE SHEET 1
19	ECO 1645. Q30 WAS 2N3304 Q31 WAS 2N3906 BJ 14th JUNE 84
20	SEE SHTS 1&5



DRAWN BJ	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE BOOT STRAPPED SUPPLIES & RANGE LOGIC 1061	DRAWING No 430328 SHEET 2 OF 5
DATE 17-4-79	DATE					

DRAWING No 430328

FIRST USED ON 1081

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
3	RE-DRAWN 13-4-79 ECO 826, 834 & 835 ECO 855
4	R24 WAS 820k IL ECO. 940 20.6.79
5	SEE SHEET 3.
6	SEE SHEET 3
7	SEE SHEET 1 & 2
8	ECO 989 & 997. BJ SEE SHT. 1. 30-10-79
9	SEE SHEETS 1 & 2
10	ECO 1098 Q35 Q36 WERE SU2656M. R46 WAS 1k5. DSB DELETED & REPLACED WITH LK3 IL 16.4.80
11	ECO. 1050. R58 WAS 1k. C8 WAS ACROSS R24. C11 WAS 1nF. C57 WAS 68pF. M22 WAS LF356H. IL 2.6.80
12	Q34 & Q35 WERE 2N3356 R4 WAS 10M R5 WAS 18k R12 & R14 WERE 8K6 R15 & R35 WERE ADDED R36 R24 WAS 680k C12 WAS 0·47µF. Q24 Q3 ADDED. LK1 & LK2 REMOVED IL 25.9.80
13	R236 & R237 ADDED R57 WAS 200R IL 16 12 80
14	M1 NOW SELECTED SEE ALSO SHT 4 IL 3 11 81
15	ECO 1273 SEE SHEET 1 JR 7-1-82
16	ECO 1282 R231 D71, D72 ADDED R26 WAS 2M2 R27 WAS 82DR JR 23 1 82
17	SEE SHEET 2
18	SEE SHEET 1
19	SEE SHEET 2
20	SEE SHTS 1&5

CONDITION	MULTIPLEXER a	b	c	RESET
RESET.	1	1	1	1
SIG	1	1	0	0
-Ve BIAS	0	1	0	0
+Ve REF 1	1	0	0	0
+Ve REF 2	0	0	0	0
+Ve BIAS	0	1	1	0
-Ve REF 1	1	0	1	0
-Ve REF 2	0	0	1	0

DRAWN B J	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °	MATERIAL ——
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH ——
DATE 11-4-79	DATE			

datron ELECTRONICS LTD. NORWICH.

TITLE A-D CONVERTOR. 1081

DRAWING SIZE A1

DRAWING No. 430328

SHEET 3 of 5

DRAWING No. 430328

FIRST USED ON 1061

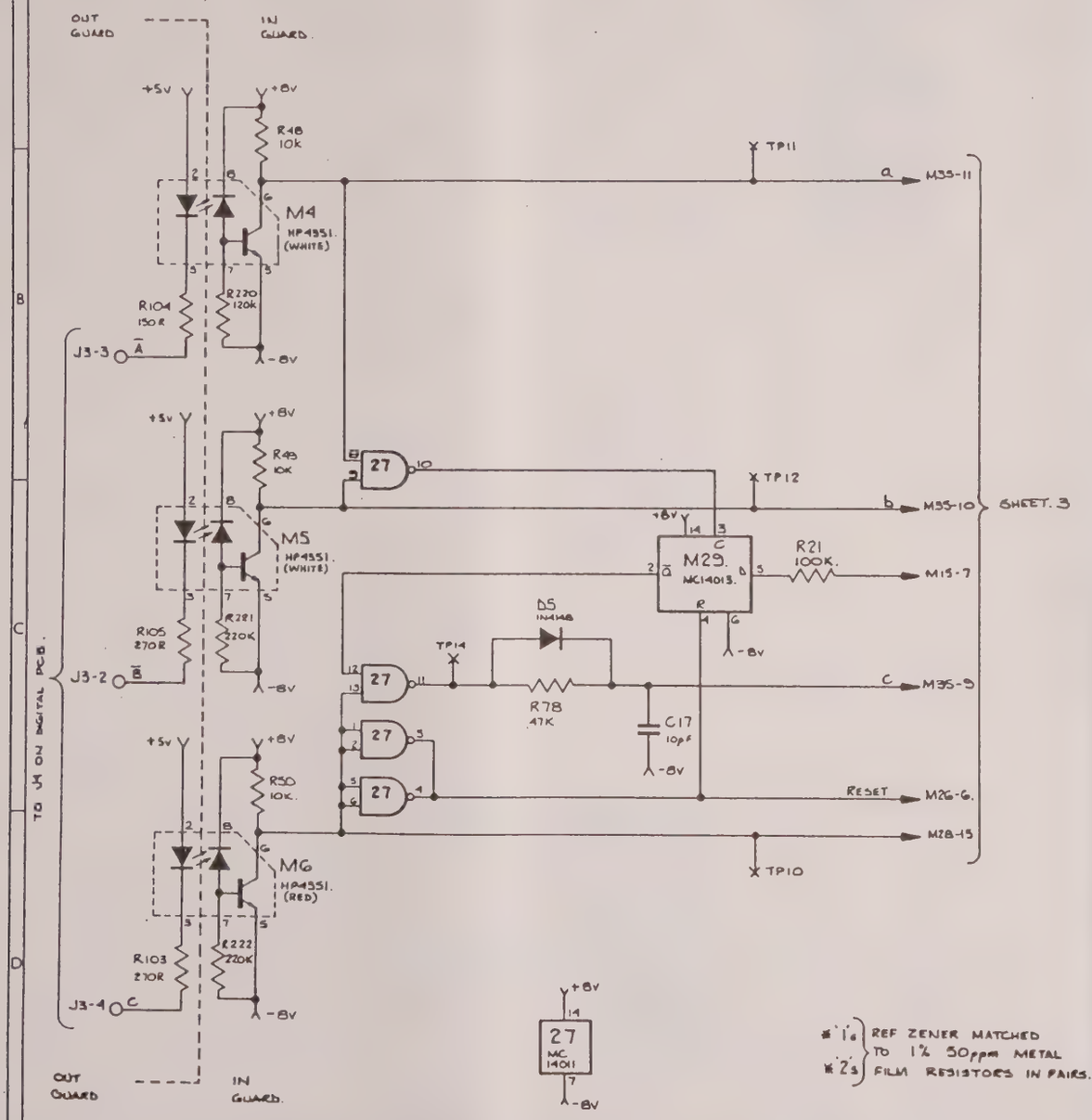
THIRD ANGLE PROJECTION

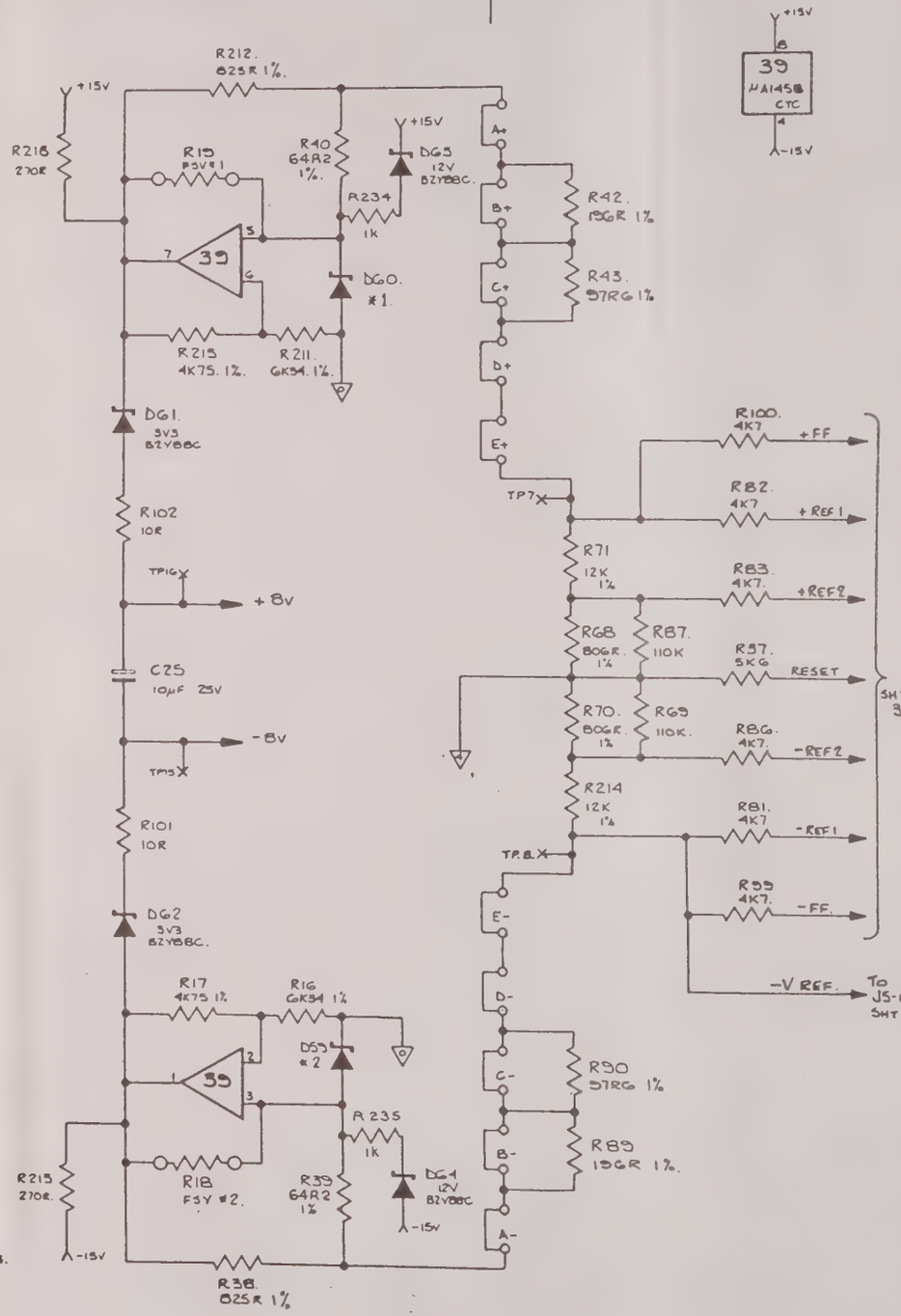
DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES
3	RE-DRAWN B.J. 15-4-79. ECO 826, 834 & ECO 853.
4	SEE SHEETS 2&3.
5	SEE SHEET 5.
6	SEE SHEET 5
7	SEE SHEET 1&2
8	ECO 853 & 987. SEE SHT 1. B.J. 30-10-79
9	SEE SHEETS 1&2
10	SEE SHEETS 1 3&5 IL 16.4.80
11	ECO.1128. R41 & R88 DELETED IL 9.6.80
12	SEE SHEET 3
13	R48 WAS 5KG R105 WAS 150R R220 WAS 220k IL 16.12.80
14	R39 & R40 WERE 40R2. M4-M6 SELECTED SEE ALSO SHT.3 IL 3.11.81
15	ECO 1273 SEE SHEET 1 JR 7·1·82
16	ECO 1282. R234, R235 ADDED JR 23·7·82
17	SEE SHEET 2
18	SEE SHEET 1
19	SEE SHEET 2.
20	SEE SHTS 1&5





*1 & *2 } REF ZENER MATCHED TO 1% 50ppm METAL FILM RESISTORS IN PAIRS.

DRAWN B.J.	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ½° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm. UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE A-D CONTROL & REFERENCES. 1061	DRAWING No. 430328
DATE 6-4-79.	DATE					SHEET 4 OF 5

DRAWING No 430328

FIRST USED ON 1061

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

SH	CHANGES
3	RE-DRAWN 19-4-79 ECO 870, BJW & BJ ECO 830
4	SEE SHEETS 2+3
5	ECO 929 D40 WAS DELN RE-INSTATED BJ 16-7-79
6	ECO 959 C59 ADDED ACROSS MIG-8 & MIG-4 IL 20 9 79
7	SEE SHEET 1 & 2.
8	ECO 985 & 997. BJ SEE SHEET 1 10-10-79
9	SEE SHEETS 1 & 2
10	ECO 1098. 1107 LINK 'F' DELETED MIG WAS 14069 C59 DELETED C26 WAS 1nF R83 WAS 100k R104/R107 WERE 3k3 IL 17 4 80
11	SEE SHEETS 2+3
12	SEE SHEET 3
13	SEE SHEETS 1, 3 & 4
14	SEE SHEETS 3 & 4
15	ECO 1273 SEE SHEET 1 CR 7-1-82
16	ECO 1282 R239 ADDED CR 23.7.82
17	SEE SHEET 2
18	SEE SHEET 1
19	SEE SHEET 2
20	ECO 1659 D74 R242 ADDED RP 19.9.84

TO J4 DIGITAL PCB

OUT OF GUARD | IN GUARD

J3-1 +5V
J3-16 ID7
J3-15 ID6
J3-14 ID5
J3-13 ID4
J3-12 ID3
J3-11 ID2
J3-10 ID1
J3-9 ID0
J3-6 COND VAL
J3-7 IA1
J3-8 IA0

M8-A, M8-B, M9-A, M9-B, M10-A, M10-B, M11-A, M11-B, M2-B, M7-B, M7-A, M2-A

AN1 5K3

D13 1N4148, D12 1N4148, D14 1N4148, D9 1N4148

R52 330K, R53 330K, R54 330K, R55 330K, R58 5K3, R72 4K7, R107 2K2, R106 2K2, R93 120K, R239 1K, C26 470pF

AD7, AD6, AD5, AD4, AD3, AD2, AD1, AD0, HF FLAG, OP SEL CLK, LOCLK

M20. 14076 — C33 0.1µF, D74 TVS 515, R137 10K

M21. 14076 — R201 820K, D40 1N4148, C32 0.01µF

TEST → TO D11 SHT 1
AVE → TO D8 SHT 3
DC → TO D47
LD4 → TO R183
LD5 → TO R182
LD6 → TO R205
LD7 → TO R206
TO SHT 2

M13. M14. — C/P DMS

AN2. 200K, 100K

IA COMP → TO M3-2. SHT. 1.
TL3, R242 10K

TO M11-9
TO M11-4
TO M11-10

TO SHT 1
AD4 & J1-7
OP SEL CLK & J1-1

TO J2. OHMS PCB

A D0	J5-1
A D1	J5-2
A D2	J5-3
A D3	J5-4
A D4	J5-5
A D5	J5-6
A D6	J5-7
A D7	J5-8
OP SEL CLK	J5-9
LD CLK	J5-10
	J5-11
R REF COM	J5-12
-V REF	J5-13
0V	J5-14
+15V	J5-15
-15V	J5-16

To TP8 SHT 4
To J1-3 SHT 1

To J1 CURRENT PCB

AD0	J2-1
AD1	J2-2
AD2	J2-3
AD3	J2-4
AD4	J2-5
AD5	J2-6
AD6	J2-7
AD7	J2-8
OP SEL CLK	J2-9
LD CLK	J2-10
	J2-11
	J2-12
	J2-13
0V	J2-14
+15V	J2-15
-15V	J2-16

To J1 AC. PCB

HF FLAG	J4-1
AD0	J4-2
AD2	J4-3
AD3	J4-4
AD4	J4-5
AD5	J4-6
AD6	J4-7
AD7	J4-8
OP SEL CLK	J4-9
LD CLK	J4-10
HF COMP	J4-11
O/P COM	J4-12
LOCL O/P	J4-13
0V	J4-14
+15V	J4-15
-15V	J4-16

DRAWN BJ	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 3 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE ANALOGUE INTERFACE LOGIC 1061
DATE 18-4-79	DATE				DRAWING No. 430328 SHEET 5 OF 5

DRAWING SIZE A1

DRAWING No. 400329

FIRST USED ON 1061

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. LINKS(LK) MADE FROM 22SWG BTC WIRE (540002) AND SLEEVED (EXCEPT FOR LK19–LK34) 590004
2. MOUNT MI. 2. 21 17. 22. 28 29. 33. 34. 37–40, 44–46 48 49 52. 54 55. 56 ON 14 WAY DIL SOCKETS. PART Nº 605060 — 22OFF
3. MOUNT M3–14. 16. . 24–27 32 41 42 43/60 47. 50 58 59 57 M61 ON 16 WAY DIL SOCKETS. PART Nº 605061 — 28 OFF
4. MOUNT M18, M30 & 35 ON 24 WAY D.I.L. SOCKETS PART Nº 605064
5. MOUNT M19 20 ON 22 WAY DIL SOCKETS. PART Nº 605063
6. MOUNT M23 ON 28 WAY D.I.L. SOCKET PART Nº 605065
7. MOUNT M53 ON 40 WAY DIL SOCKET PART Nº 605050
8. MOUNT M62 ON 8WAY D.I.L. SOCKET PART Nº 605059
9. MOUNT M31. 36 ON 18 WAY DIL SOCKET PART No 605068.

ISS	CHANGES
A	
1	RELEASED TO PRODN 24 11 78
2	ECO 811 4.1.79 / 1SS 4 PCB UPDATE IL
3	ECO 865 CAB 100nF(104J0) ADDED C25 was 100nF (104J0) C24 was 100nF (104J0) C23 was 47nF (473J0) 29 JAN 79 IMGS
4	ECO 835/852 1SS 5 PCB UPDATE - SEE ECOs FOR FULL DETAILS IL 17.4.79
5	ECOs 863/872, 904 /905/911 /912 PARTS LIST CHANGES IL 19.6.79
6	ECO 928 C33 ADDED PTS LI. CHANGE to R85 IL 17.7.79
7	ECO. 941/949 Pts. LI. CHANGE TO R2 IL 4.9.79
8	ECO 939 941 998 1004 PTS LI CHANGE IL 29.10.79
9	ECOs 998 1003 1042 1045 1067 1596 PCB UPDATE 1582 ECOs FOR FULL DETAILS IL 4.1.80
10	ECO. 1057 PARTS LIST CHANGE ONLY IL 21.1.80
11	ECO 1082 WIRE TERMINAL ASSY 400379/1 to be 8 OFF IL 13.2.80
12	ECO 1044 1086 1094 1092 PCB UPDATE & 1SS 7A PARTS LIST CHANGES. IL 28.3.80
13	ECO 1108 C103 ADDED. PARTS LIST CHANGES IL 18.4.80
14	ECO. 1133 R104 ADDED. + PARTS LIST CHANGES IL 18.6.80
15	ECO 1137. 1140. 1142 PARTS LIST CHANGES C82 CONNECTED TO R85 R86 '+' SIGN ADDED TO TANT. CAPS IL 28.7.80
16	ECO 1147 D21 ADDED IL 30.9.80
17	ECO 1188 R28 WAS 1k - PARTS LIST CHANGE IL 6.1.81
18	ECO 1214 PC 8 UPGRADED TO 8B PARTS LIST CHANGES LK 39 ADDED R 11–6–81
19	ECO 1241 Nº OF 24 WAY SOCKETS IS 8. IL 4.11.81
20	ECO 1281, 1283 & 1243 R86 WAS 100K. R46 WAS 330K. M21, 26 WERE 28 PIN 8B6 W 4 BIT RAM PCB WAS 1SS 6B LK36 ADDED. J3 WAS 608064 JJR 2.12.81
21	ECO 1365 M18 NOW USED - SEE PARTS LIST M30 M33 WERE 16S 9 IL 9.5.82
22	ECO 1431 C32 NOW MOUNTED ON CERAMIC BEADS JJR 16.12.82
23	ECO. 1481 EARTH WIRE ADDED. LINK (DIS.COM. LOW IMPEDANCE) BETWEEN BUS STRIP & C56 ADDED IL 31.5.83
24	ECO 1515 M18, M30–M35 WERE 16S 17A. JJR 18.8.83.
25	ECO 1559 HEATSINK 320140 ADDED TO M23 IL 11.11.83
26	ECO 1648 & 1588 C56 & 57 ADDED. EARTH WIRE REMOVED PCB UPDATE SOFTWARE UPDATE NYLATCH REMOVED BJ 27-2-84
27	ECO 1603 M18, M30 & M35 WERE 16S 18 IL 6.4.84
28	ECO 1633 1637 PCB WAS 1SS 11A M20 DELETED M13 WAS 8K1 RAM IL 25.6.84
29	ECO 1864 M23 WAS 270053 NOW 270053-5 BJ 27-3-84
30	ECO 1748, 1750 NV RAM EXTN PCB DELETED. M19, M20 ADDED. C40 WAS 100nF. IL 1.11.84

8 WAY POLARISED SOCKET 605052 - 1OFF

WIRE/TERMINAL ASSY 400379/1 8 OFF

COMPONENT CLIP — FIT BETWEEN UNSCREENED LINES ON PCB. 630098

FIT CLIP 606003 - 3OFF TO J1, J2 & J4, AS SHOWN.

22 SWG TINNED COPPER WIRE PIN TO STAND OFF PCB BY 12mm 540002.

P.C. BOARD 410096 - 11B

5 off SOLDER TERMINAL 620003 PINS MARKED A, B, C etc.

BUS STRIP 920048

HEATSINK 920140 TO BE FITTED OVER M23 BY SLIDING DEVICE OVER BODY OF M23 BEFORE FITTING IN SOCKET

N.B. FOR 400Hz OPERATION, LK7 TO BE FITTED AND LK5 50Hz OPERATION TO BE REMOVED

R31 TO BE MOUNTED ON END 1mm. ABOVE PCB.

N.B. FREE END OF R31 WILL BE SOLDERED TO BATTERY B1 AT CHASSIS ASSY STAGE.

WHEN FITTING 8k ROM, (M30/M35) REMOVE LK3 AND LK4. FIT LK10 AND LK13

MOUNT C32 ON CERAMIC BEADS 2 OFF 630036

TEST POINT TERMINAL 620007 32 off.

MOUNT JL3 ON 16 WAY D.I.L. SOCKET 605061

DRAWN IL	CHECKED P.R.H.	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS: DECIMAL TO 3 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED. ANGULAR +/-	MATERIAL	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH	TITLE 1061 DIGITAL PCB. ASSY.
DATE 26 6 78	DATE				DRAWING No. 400329 SHEET 1 OF 15

DRAWING SIZE A1

DRAWING No 430329

FIRST USED ON

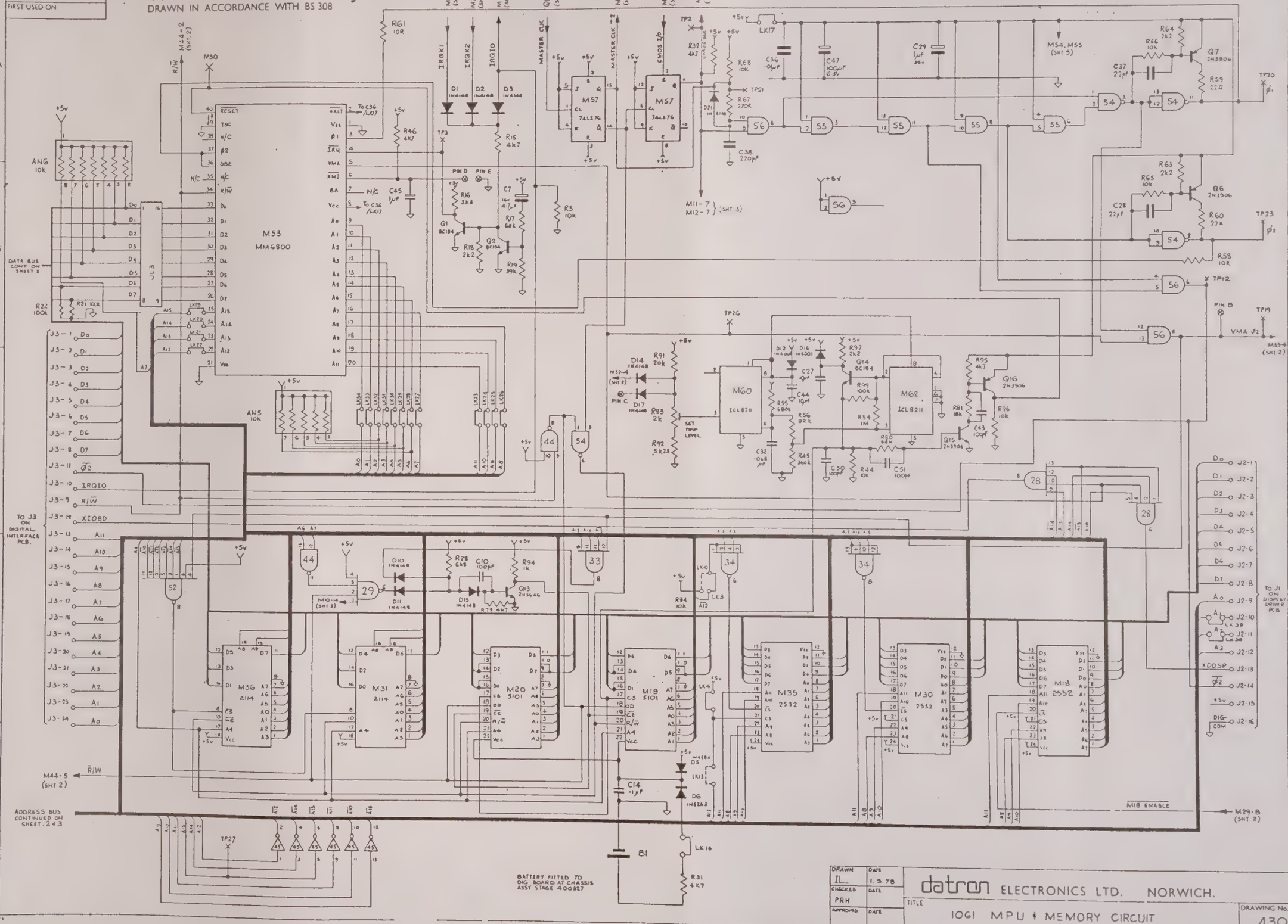
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES
A	
1	RELEASED TO PRODN 24 11 78
2	ECO 811 PCB UPDATE TO 1554 IL 8.1.79
3	ECO 815 SHT 4 W (C49 100PF (10210) ADDED) C23 WAS 1000PF (1500611) C36 WAS 100PF (150061) C25 WAS 47PF (150066) 29 JAN 79 WLS
4	ECO 815/852 1554 S UPDATE TO PCB IL 4-4-79
5	ECO 912/911/863 R91 WAS 100K R92 WAS 27K R93 WAS 10K LK17 WAS R68 (0.47Ω) IL 19 6 79
6	ECO 928 17.7.79 SEE SHEET 3
7	ECO 941/849 SEE SHEET 3 4.9.79
8	ECO 1004 SEE SHEET 3 IL 29 10 79
9	ECO 858 JL1 - JL2 REMOVED & REPLACED WITH WIRE LINKS. SZ-S3 REMOVED. R53 REMOVED. BATTERY ASSY NOW OBSOLETE. J7 & LK14 DELETED. TP30 ADDED. SEE ALSO SHTS 2-4 IL 8.1.80
10	ECO 1046, 1032 SEE SHEETS 3, 4 & 5 IL 24.3.80
11	ECO 1108 R15 WAS 22K R18 WAS 10K IL 18.4.80
12	ECO 1135 SEE SHEET 4 IL 18.6.80
13	ECO 1147 D21 ADDED IL 30.9.80
14	ECO 1198 SEE SHEET 3 IL 6.1.81
15	ECO 1214 LA 39 ADDED JR 18.6.81
16	ECO 1243, 1851 R56 WAS 100K R45 WAS 330K M31, 36 WERE 256 X 4 BIT RAMS LK 38 ADDED. PCB WAS 1554 B JR 2.12.81
17	ECO 1542 SEE SHT 2 & 5 BJ 28-2-84
18	ECO 1637 M19 WAS 5101 RAM M20 DELETED 23 5 84
19	ECO 1748 M19 & M20 WERE PART OF NV RAM EXT. PCB ASSY UNDER M19 REF IL 1.11.84



DRAWN IL	DATE 1.3.78	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
CHECKED PRH	DATE	TITLE 1061 MPU & MEMORY CIRCUIT	DRAWING No. 430329
APPROVED	DATE		SHEET 1 OF 5

DRAWING No 430329

FIRST USED ON

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

No.	CHANGES
A	
1	RELEASED TO PRODN 24 11 78
2	ECO 811 PCB UPDATE TO 1534 8.1.79
3	ECO 815 K48 100M (1020) ADDED K23 WAS 100M (1000) K24 WAS 10M (1000) K25 WAS 47M (3000) 29 JAN 79
4	153 5 UPDATE TO PCB ECO 835/852 6-6-79
5	SEE SHEETS 1 & 3
6	SEE SHEET 3
7	SEE SHEET 3
8	SEE SHEET 3
9	ECO 958 SI4 LK15 - LK16 DELETED. ANT. R102 ADDED 8.1.80
10	SEE SHEETS 3,4 & 5 24 3 80
11	SEE SHEETS 1,3 & 5 18 4 80
12	SEE SHEET 4
13	SEE SHEET 3
15	ECO 1214 2A0 AND 1A1 DRIVES INCREASED 17 8 81
16	ECO 1243 1251 SEE SHEET 1
17	ECO 1542. BJ M32 WAS 74LS155. SEE SHT 3. 28-2-84
18	SEE SHEET 1
19	SEE SHEETS 1 & 4

SIGNATURE ANALYSIS PROBE

J6-1 NC, J6-4 NC, J6-5 NC, J6-2 NC

M11-9,10 (SHT 2) M12-9,10 (SHT 2)

XADCTL, +5V, R87 100k, AN3 10K

M43 555 — TIME GUARD → M10-12 (SHT 3)

CMOS CLK ← M57-11 (SHT 1)

C34 .01µF, C35 10µF

R39 10K

M8 1407G, M9 1407G — DATA DISABLE, CLOCK, RESET, O/P DISABLE

J4-1 … J4-16 → TO J3 ANALOGUE PCB

M21-11 (SHT 3), M21-8, M21-6, R23 NULL DET, R2 M10-2 COND VAL, IA1, IA0, ID7 … ID0

AN7 10K, R102 10K

CD0 … CD7

AN3 10K, AN1 10K

M44-8 (SHT 1), M53-34 (SHT 1) R/W, M57-7 (SHT 1) CMOS I/O

DATA BUS CONTINUED ON SHEET 1

NE-F (SHT 3), M25-4 (SHT 3), M18-18 (SHT 1), M18 ENABLE

R20 100K, R1 10K

M16 74LS155, M32 74LS42N

TP18

To D14(k) (SHT 1)

XKDSP3 J1-16, XKYBRD J1-15, CMOS CLK J1-14, XKDSP1 J1-13, XKDSP2 J1-12, XKDSPØ J1-11, IRQK2 J1-10, IRQK1 J1-9, CD7 J1-8, CD6 J1-7, CD5 J1-6, CD4 J1-5, CD0 J1-4, CD1 J1-3, CD2 J1-2, CD3 J1-1 → TO J2 CENTRE PCB

VMA Ø2 ← M56-11 (SHT 1)

ADDRESS BUS CONTINUED ON SHEET 1 & 3

CMOS DATA BUS CONTINUED ON SHEET 3

XADSTA → M10-1, 15 (SHT 3)

R89 10k, R3 10k, R4 10k

IRQK2, IRQK1 → D2, D1 (SHT 1)

J5-5 ZAHR, J5-4 YDAC → TO J3 CENTRE PCB

DRAWN IL	CHECKED P.R.H.	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ° ' INCH DIMENSIONS DECIMAL TO 3 PLACES ± .001; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS DECIMAL TO 3 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm. UNLESS OTHERWISE STATED	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE ——		FINISH ——	TITLE 1061 CMOS ADDRESS DECODE + I/O CIRCUIT	DRAWING No 430329
DATE 13 9 78	DATE	NOT TO BE SCALED				SHEET 2 OF 5

DRAWING No. 430329

FIRST USED ON

NOTES

ISS.	CHANGES
A	
1	RELEASED TO PRODN 24 11 78
2	ECO 811 PCB UPDATE TO 1554 IL 8 1 79
3	ECO 845 C48 100P (0805) ADDED C23 was 100pF (1206) C24 was 100N (1206) C25 was 47N (1206) 29 JAN 79 W G S
4	R3 WAS BC184 ECO 933 1553 S UPDATE TO PCB. ECO 952 6-6-79 IL
5	ECO 896 R10 WAS 680k 19 6 79 IL
6	ECO. 928 CS8 ADDED. PLS IL CHANGE TO R85 17 7 79 IL
7	ECO 941/949 R2 WAS 15k D18 & D19 ADDED IL 4.9.79
8	ECO. 1004 R86 DELETED IL 28.10.79
9	ECO 998 D18/D19 DELETED TP31-TP35 ADDED IL 8180
10	ECO 1092, 1086 R5 WAS 27k R33 WAS 4k7 R01 WAS 10k SEE ALSO SHTS 4 & 5 IL 24 2 80
11	ECO 1108 R103 ADDED IL 18 4 80
12	SEE SHEET 4
13	SEE SHEET 1
14	ECO 1188 R23 WAS 1k IL 6.1.81
15	ECO 1214 M21 WAS MC14030BCP IL 12 6 81
16	ECO 1273, 1251 SEE SHEET 1
17	ECO 1542 SEE SHEET 2&3 BJ. 28.2.84.
18	SEE SHEET 1
19	SEE SHEETS 1 & 4

CMOS DATA BUS CONTINUED ON SHEET 2.

ADDRESS BUS CONTINUED ON SHEET 1, 4 2

M23 ZNA2035

OUTPUT DATA MULTIPLEXER

7 BIT COUNTER (BIAS PERIOD AND REF 2)

18 BIT COUNTER (BIG PERIOD, REF 1 AND REF 2 CAREY)

CLOCK FREQUENCY ERROR DETECTOR

R35, R34, M26-1 (SHT 4), M39-8, M27-11

M11 14076 · M12 14076 · M13 14516 · M14 14516 · M1 14013 · M15 555 · M61 555 · M22 14013 · M26 14027 · M47 14022

J4-6 (SHT 2) · D3 (SHT 1) · M43-3 (SHT 2) · J5-3 RUN CAL · J5-6 EXT TRIG · M29-1 (SHT 1) · M16-12 XADDLY (SHT 2) · J6-7 2HZ FROM M26-15 · J4-5 (SHT 2) NULL DET. · J4-3 · J4-2 · J4-4 · TO A50 SHT 4 · MASTER CLOCK ÷ 2 M57-13 (SHT 1)

DRAWN IL	CHECKED P.R.H.	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 1 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± 1°	MATERIAL	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE		FINISH	TITLE 1061 A-D CONVERTER
DATE 13.7.78	DATE	NOT TO BE SCALED			DRAWING No. 430329 · SHEET 3 OF 5 · DRAWING SIZE A1

DRAWING No. 430329

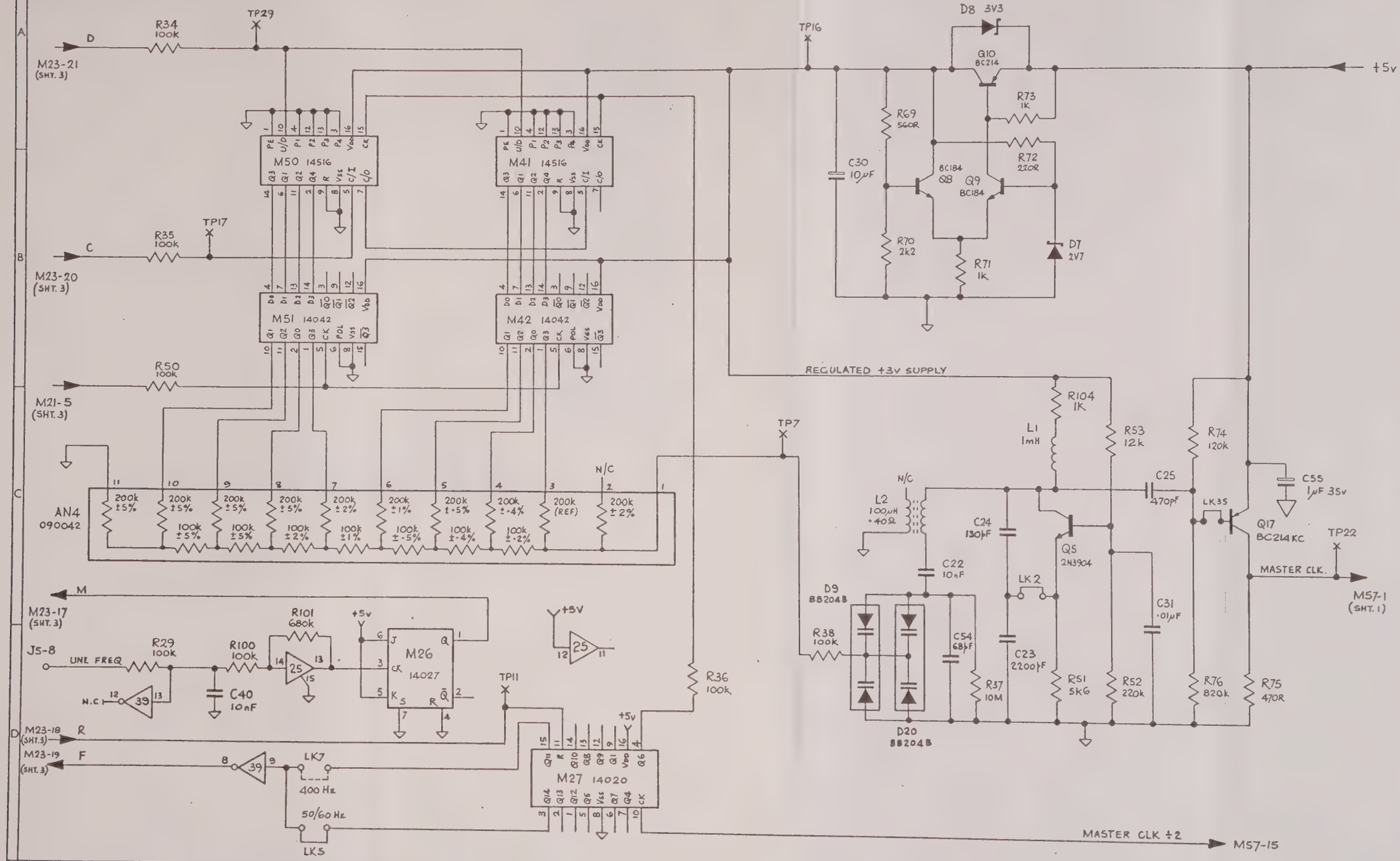
FIRST USED ON 1061

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES



ISS	CHANGES
A	
1	RELEASED TO PROD 24.11.78
2.	ECO B11 PCB UPDATE TO ISSUE 4 IL. 8.1.79
3	ECO B15 C48 1000pF(102A01) ADDED. C23 WAS 1000pF(300B01) C24 WAS 100pF (300G01) C25 WAS 47pF(300G21) 29 JAN 79 W.G.G.
4	ISS 5 UPDATE TO PROD ECO 835/852 4-4-79 IL
5	SEE SHEETS 1 & 3
6	SEE SHEET 3
7	SEE SHEET 3
8	SEE SHEET 3
9	ECO.998 M25 PINS 14 & 13 WERE M39 PINS 13 & 12 RESP. R100. R101 ADDED IL 8.1.80
10	ECO. 1046. R98 REPLACED BY LK35 Q11 DELETED. D9 WAS MVAM-2 D20, C54, C55 & Q17 ADDED. C22 WAS 470pF R76 WAS 83k R52 WAS 12k R51 WAS 100R IL 24.3.80
11	SEE SHEETS 1 3 AND 5 IL 18.4.80
12	ECO. 1135 R51 WAS 12k Q17 WAS 2N3906 R104 ADDED. IL 18.6.80
13	SEE Sheet 1
14	SEE SHEET 3
15	ECO 1214 SEE SHEETS — 1, 3, 5.
16	ECO 1243, 1251 SEE SHEET 1
17	ECO 1542 SEE SHEET 2 & 5 BJ. 28-2-84
18	SEE SHEET 1
19	ECO. 1750 C40 WAS ·1µF IL 1.11.84

DRAWN	CHECKED	DIMENSIONS IN	TOLERANCES ANGULAR ± ½°	MATERIAL
IL	P.A.H.	MILLIMETRES	INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED	
TRACED	APPROVED	SCALE —— NOT TO BE SCALED		FINISH ——
DATE 18.9.78	DATE			

datron ELECTRONICS LTD. NORWICH.

TITLE 1061 LINE LOCKING CIRCUIT

DRAWING SIZE A2

DRAWING No. 430329

SHEET 4 OF 5

DRAWING No. 430329

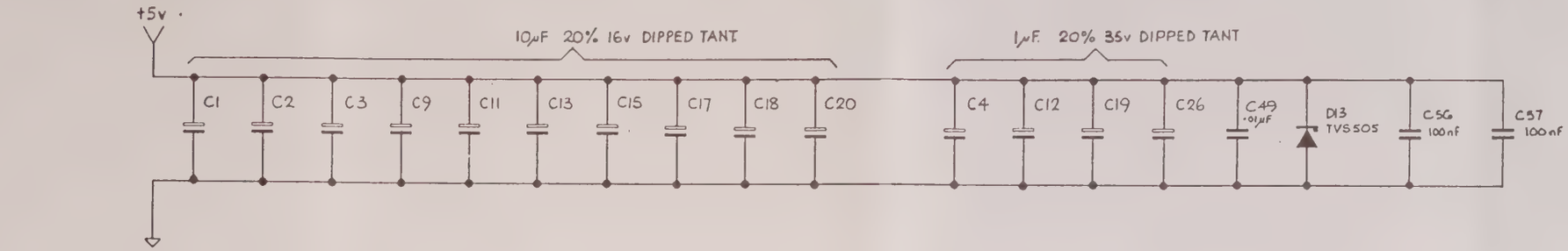
FIRST USED ON

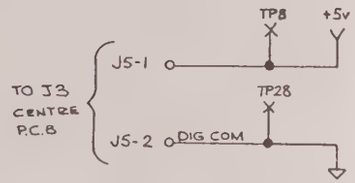
THIRD ANGLE PROJECTION

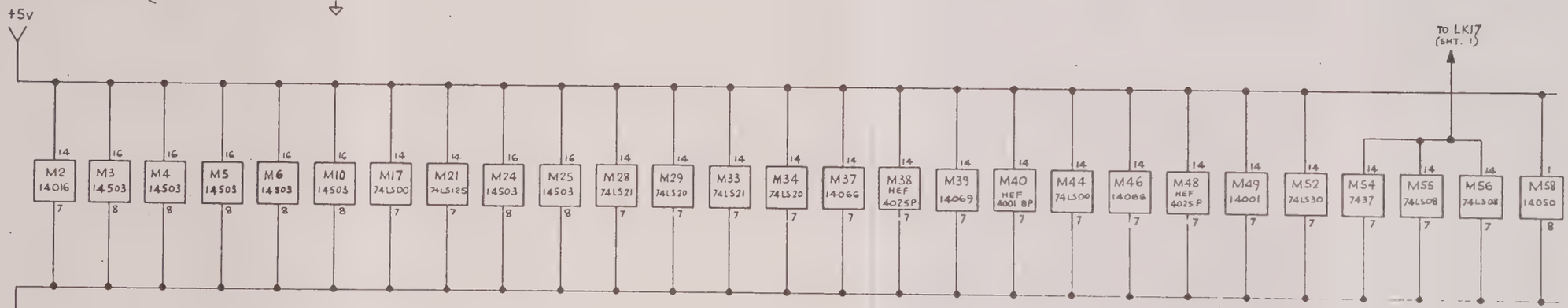
DRAWN IN ACCORDANCE WITH BS 308

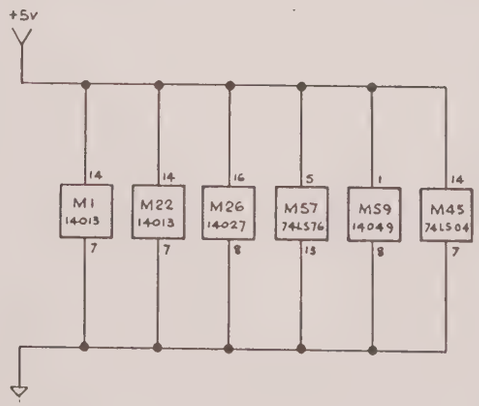
ALL BURRS TO BE REMOVED

NOTES









ALL RESISTORS : 5% ¼ WATT CARBON

ISS	CHANGES
A	
1	RELEASED TO PROD 24.11.78
2	ECO 811 PCB UPDATE TO ISSUE 4 IL 8.1.79
3	ECO 816 C48 100nF (602101) ADDED C23 was 1000pF (1300862) C24 was 100pF (1300861) C25 was 47pF (1300823) 29 JAN 79 W.O.S.
4	ISS 5 UPDATE TO PCB ECO 835/852 4-4-79 IL
5	SEE SHEETS 1 & 3
6	SEE SHEET 3
7	SEE SHEET 3
8	SEE SHEET 3
9	ECO. 998, 1047 SEE SHTS 1-4 M38 WAS 14025 IL 8.1.80
10	ECO. 1048 D13 WAS ICTE-5 M37+ M46 WERE 14016. IL 24.3.80
11	ECO. 1108 M40 WAS 14001 IL 18.4.80
12	SEE SHEET 4.
13	SEE SHEET 1
14	SEE SHEET 3
15	ECO 1214 M21 WAS 14050 JR 12.6.81
16	ECO 1243, 1251 SEE SHEET 1
17	ECO 1542 C57 & C56 ADDED SEE SHEET 2. BJ. 28-2-84.
18	SEE SHEET 1
19	SEE SHEETS 1 & 4

DRAWN IL.	CHECKED P.R.H.	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ½° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE —— NOT TO BE SCALED		FINISH	TITLE 1061 DIGITAL PCB. ASSY.	DRAWING No. 430329 SHEET 5 OF 5
DATE 12.10.78	DATE					

DRAWING No. 400330

FIRST USED ON

THIRD ANGLE PROJECTION

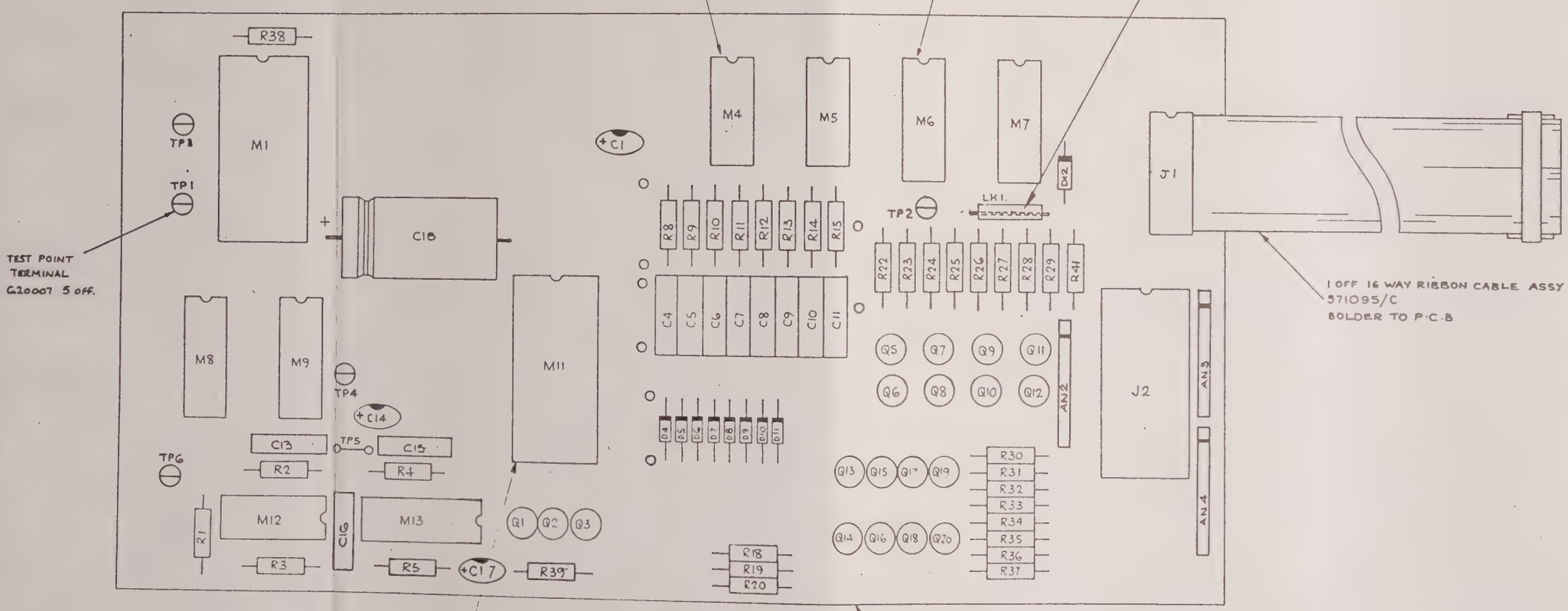
DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. MAKE TP1-TP6 FROM 22 SWG. BTC. WIRE - PART Nº 540002.

ISS	CHANGES
C	
1	RELEASED TO PRODUCTION 29.9.78
2	C18 ADDED & R40 ECO 789. 17 Nov 78. W.G.S.
3	AN2-AN4 ADDED R41 & D12 ADDED ECO 822 20-FEB 79 W.G.S.
4	R40 WAS 180Ω 0005% NOW WIRE LINK ECO 854 10 MAY 79 W.G.S.
5	CLIP FOR J1 WAS 606005. ECO.904 21.6.79 IL.
6	ECO 1000 & 1025 RIBBON CABLE CLIP WAS 606007. BJ D12 WAS BZY.
7	ECO 1217 J1 WAS 16 WAY SOCKET. JR 17·8·81
8	ECO 1253 PARTS LIST CHANGE TO J2 JR 2·12·81
9	ECO 1588 NYLATCH DELETED PCB WAS 1355A BJ. 1st MAR 84.



DRAWN IL	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± 1° DECIMAL TO 1 PLACES ± .1 DECIMAL TO 2 PLACES ± .01 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 0.1mm DECIMAL TO 1 PLACE ± 0.2mm WHOLE DIMENSIONS ± 0.5mm UNLESS OTHERWISE STATED	MATERIAL SEE PARTS LIST	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH	TITLE 1061 DISPLAY DRIVER PCB ASSY	DRAWING No. 400330
DATE 3 5 78	DATE					SHEET 1 OF 7

DRAWING No. 430330

FIRST USED ON 1061

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
1	RELEASED TO PRODUCTION 30-10-78
2	C18 ADDED & R40 E.C.O. 7809 17 NOV 78 W.G.S.
3	AN7-AN16 ADDED R41 & D12 ADDED. E.C.O. 8112 20 FEB 79 W.G.S.
4	R40 WAS 180R 2000 NOW 470R 1/4W E.C.O. 8514 10 MAY 79 W.G.S.
5	E.C.O. 10135 D12 WAS BZY 88 B.J. 3-1-80

TO J2 DIG PCB

TO J2 DIGITAL PCB

TO J2 R.H.S. PCB

TO J2 R.H.S. PCB

M1 MC6810A

M8 14520

M9 SN74LS157

M11 14515

DISPLAY SEGMENT DESIGNATIONS

UNSPECIFIED DEVICES:
M13 = MC14572.
M4, M5 = MC14001BCP.
M6, M7 = MC14503BCP.
M12 = SN74LS00.
R1–R37 = 5% 1/4W CARBON.
C1, C15 = 20% 25v DIP TANT.
C2–C14, C16 = 10% 250v POLYESTER.

DRAWN IL	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES NON DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS: DECIMAL TO 3 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 6mm. UNLESS OTHERWISE STATED. ANGULAR +/-	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE —— NOT TO BE SCALED		FINISH	TITLE 1061 DISPLAY DRIVER PCB	DRAWING No. 430330
DATE 30 10 78	DATE					SHEET 1 OF 1

DRAWING No. 400331
FIRST USED ON 1061

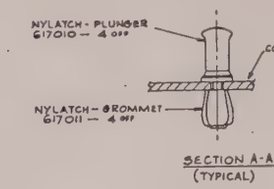
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

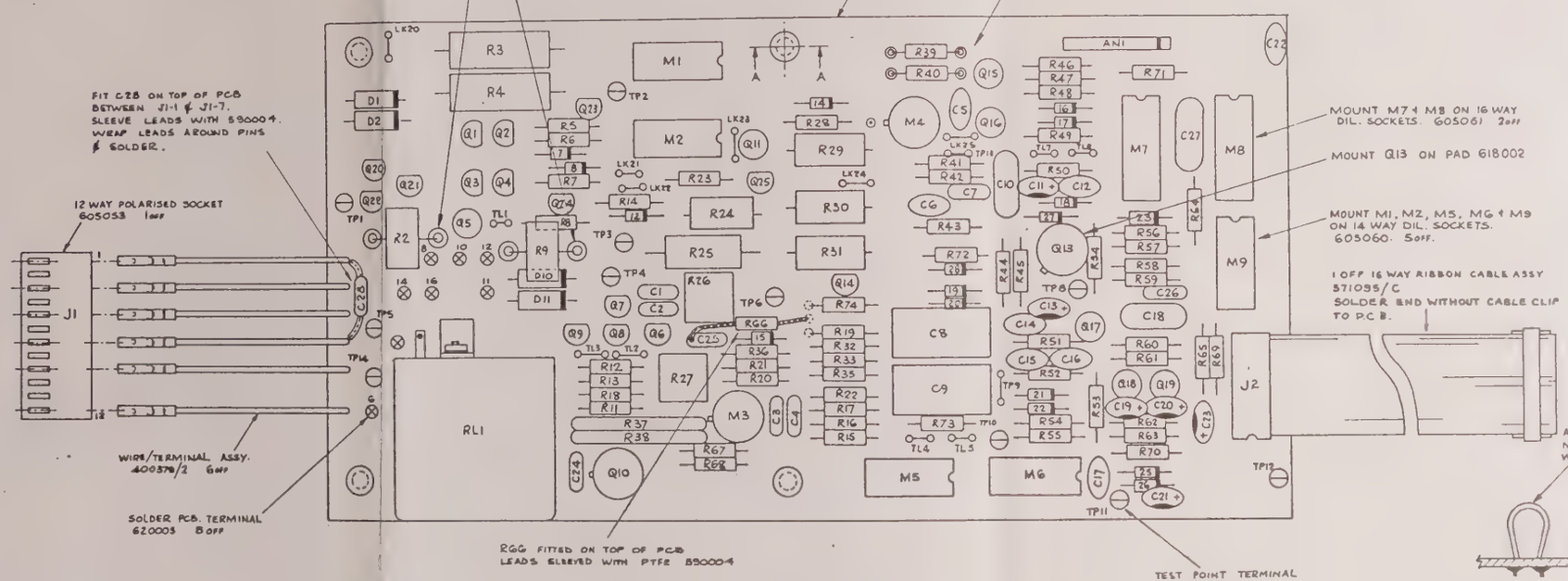
ALL BURRS TO BE REMOVED

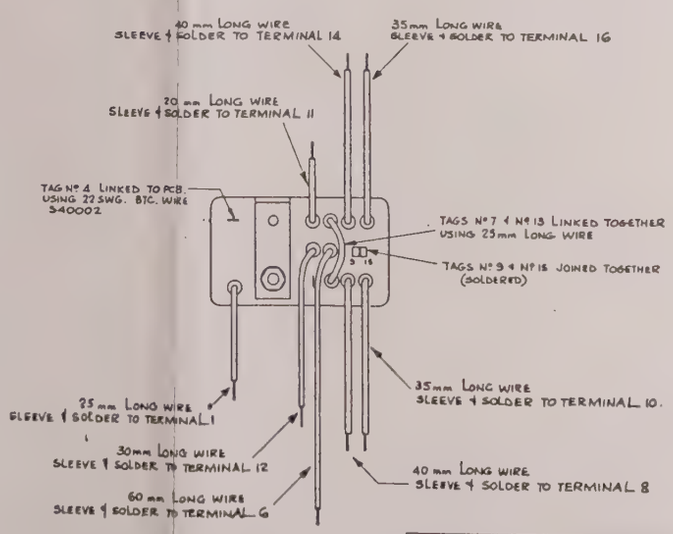
NOTES

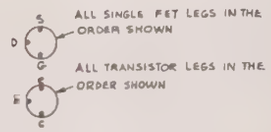
ISS.	CHANGES
12	ECO 1176 ASSY COMPLETELY RE-DRAWN. PCB WAS 1330/8 4:1. R71 – R74 ADDED R66/R70 TRACKED-IN. R90 DELETED C27 D28 ADDED C26 D27 TRACKED-IN OTHER PARTS LIST CHANGES - SEE ECO 28 11 80
13	ECO 1217 J2 WAS 18 WAY SOCKET J2 16-8-81
14	ECO 1300 R34, R48 & R66 VALUE CHANGE E66 POSITION CHANGE 30th MARCH 81
15	ECO 1367. C28 ADDED. RGG NOW ON TOP OF PCB 7th AUG 81



SECTION A-A (TYPICAL)


MOUNT R2 & R9 WITH 2off PORCELAIN BEADS 620024 ON EACH LEG, AS SHOWN
R2 & R9
4 off F.S.V. TERMINAL 602001
PCB. 410099-6
FIT C28 ON TOP OF PCB BETWEEN J1-1 & J1-7. SLEEVE LEADS WITH 590004. WRAP LEADS AROUND PINS & SOLDER.
12 WAY POLARISED SOCKET 605053 1off
J1
WIRE/TERMINAL ASSY. 400378/2 6off
SOLDER PCB. TERMINAL 620003 8off
RGG FITTED ON TOP OF PCB LEADS SLEEVED WITH PTFE 590004
MOUNT M7 & M8 ON 16 WAY DIL. SOCKETS. 605061 2off
MOUNT Q13 ON PAD 618002
MOUNT M1, M2, M3, M6 & M9 ON 14 WAY DIL. SOCKETS. 605060. 5off.
1 OFF 16 WAY RIBBON CABLE ASSY 571095/C SOLDER END WITHOUT CABLE CLIP TO P.C.B.
J2
ALL TEST POINTS TO BE MADE FROM 22 SWG BTC WIRE. 540002
N.B. SOME LINKS SHOWN AS TEST POINT LOOPS SHOULD ALSO BE FORMED THUS, FOR EASE OF PRODUCTION (eg. LK20)
TEST POINT TERMINAL 620007 11 off.


40 mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 14
35mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 16
20 mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 11
TAG Nº 4 LINKED TO PCB. USING 22 SWG. BTC. WIRE 540002
TAGS Nº 7 & Nº 13 LINKED TOGETHER USING 25mm LONG WIRE
TAGS Nº 9 & Nº 15 JOINED TOGETHER (SOLDERED)
35mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 10.
25 mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 1
30mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 12
60 mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 6
40 mm LONG WIRE SLEEVE & SOLDER TO TERMINAL 8



NOTE

ALL WIRE IS 540008 PTFE INSULATED WHITE
ALL LENGTHS OF WIRE SHOWN ON RELAY ARE TOTAL LENGTHS, THEN 5mm IS TO BE STRIPPED FROM EACH END.
SLEEVE WIRES WITH 590001 1/2 PIECE FOR EACH CONNECTION. ie. 1 PIECE FOR EACH WIRE.

DRAWN IL	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 6mm. UNLESS OTHERWISE STATED	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH ——	TITLE 1061 OHMS PCB. ASSY.
DATE 30.7.80	DATE				DRAWING No. 400331

DRAWING SIZE A1

SHEET 1 OF 12

DRAWING No
430331.
FIRST USED ON
1061.

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES
1*	RELEASED
2	D13 WAS WRONG BJ R56 - 1M 4-6-79 C13 - 0·1μF. MC
3	ECO 896. D24. BJ DELETED 20-6-79. ECO 908 D7, C8 NOW C8V7. R6 10K NOW 5K6 R15 15K NOW 24K. ME
4	ECO 930 RE-INSTATE D24 ECO 953 Q21, Q22 & Q23 U1394E's NOW U309's. BJ 13-7-79. MD
5	ECO 1054 & 1062 Q12 DELETED D32 WAS ERO 000178 C30 WAS 0·1μF 0805 50 V R36 WAS 10K, 0·1% BB R67 ADDED PCB NOW 1064 TP11 & TP7 DELETED D32, 33 & 34 DELETED R35 WAS 1M5 0001168 R64 & R68 ADDED C24 & C25 ADDED W.B.S. 6-FEB 80 MD
6	SEE SHEET 2 MD
7	ECO. 1124 C9 WAS 3n3F IL 3 6 80 MD
8	ECO 1134 D27 ADDED IL 19 6 80 MD
9	ECO 1176 R72 R73, R74, T028 ADDED. R10 DELETED R26 WAS 20K R35 WAS 1M2 SEE ALSO SHEET 2 IL 12 12 80 MD
10	ECO 1300 R34 WAS 680R R48 - 82K EGG WAS ADDED TO Q16, R19 NOW to ⏚ SOH, MAE 82 BJ MD
11	ECO 1667. C28 ADDED. 7th AUG 81. MD

To J2 LEFT HAND SIDE PCB.

J1-1 LO INJ · J1-2 & GW · J1-3 I- · J1-12 I+ · J1-13 BS · J1-15 LO OUT

TP1 · C28 0·1μF. · H/C · TP14

R3 VA8G60 · RL1 · RELAY FRAME · R2 VA8G50

+15V · R35 10M · TP6 · C23 100pF · D13 IN4148A · R36 10M 1% CF · R26 200K POT · R38 18M MATCHED PAIR · RE7 200K POT. · R37 18M MATCHED PAIR · R18 100M

C4 47pF · M3 LF411 · -15V · C3 47pF · R34 560R · R22 2K2 · Q13 2N3063 · R17 10K · Q6 U3114E · Q7 U3114E · R16 6K8 · D10 10V IN5347 · D11 10V IN5347 · TP4

Q10 LM334 · TP5 · R12 56K · R13 56K · R11 10M · TL3 · TL2 · C24 100pF · R21 15M 1% CF · R20 15M 1% CF. · R88 120K · R15 24K.

C1 4700pF · C2 680pF · R8 5K76 1% MF · +15V · R73 82K · D28 IN4148 · R72 39K · LD7 · TL1 · Q14 U3114E · R20K 1M · R19 100K · Q8 U3594E · Q9 U1594E · R74 10M · Q5 U1594E · TEST (FROM M9-1) SHT3

R32 5K3 · R67 470K · R33 150K · -15V

R3 NF 1% 4K40 · Q1 U3594E · R5 10M · R4 3K04 1% NF · Ω REF COM · Q21 J309 · Q23 U1594E. · LD3

D1 10V IN5347 · D2 10V IN5347 · Q20 U1594E · D8 IN4148 · D7 BZY88C2V7 · R23 330R · R7 10M · Q2 U3114E · Q22 J309 · Q24 U1594E · R14 100K · D12 IN4148 · Q4 U3114E · Q3 U3114E · LD3 · RG 5K3 · -15V · TP3

TP2 · Q25 J309 · Q11 J309 · D14 IN4148A · LD6 · R28 1M · R31 1K2B39 0·1% WW · R30 1K2B39 0·1% WW · R29 5K777 0·1% WW · R24 57K77 0·1% WW. · R25 577K7 0·1% WW

M1B · M1C · M1D · M1A · M2B · M2C · M2A · LD0 · LD1 · LD2 · LD5 · LD4 · LD3

R27 1K2B39 0·1% WW · R48 48K · R47 47K · D17 BZY88C6V · TL7 · C11 22pF 5% 160V · C12 0·01μF · C14 0·01μF · C13 22μF 20% 16V · C10 0·1μF 10% 250V POLY · R44 1K · R45 1K · +ΩBS · -ΩBS · D20 IN4148 · D15 IN4148 · TO R39 · D27 IN4148 · TL8 · R50 6K6 · TO C5 & IN4/4 · D18 IN4148 · EGG & C18

R40 F5V · R39 F5V · R46 6K6 · FROM R50 · R43 47K · C7 1n5F · M4 LM212 · C5 2n2F · DIG 7V5 · Q15 BC214 · FROM Q16 · Q16 BC327 · R41 47K · C6 0·01μF · R42 6780R · TP13 · C8 2·2μF · MS-10 SHT2 · MS-2 SHT2

Ω · M2D · R49 3K3 · -15V · -15V

M1 · M2 · -15V

SUPPLY CONNECTIONS FOR M1 & M2

DRAWN BJ	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± .°	MATERIAL —	datron ELECTRONICS LTD.. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH —	TITLE OHMS PCB CIRCUIT 1061.	DRAWING No. 430331	SHEET 1 OF 2
DATE 20-4-79.	DATE						

DRAWING No 430331

FIRST USED ON 1061

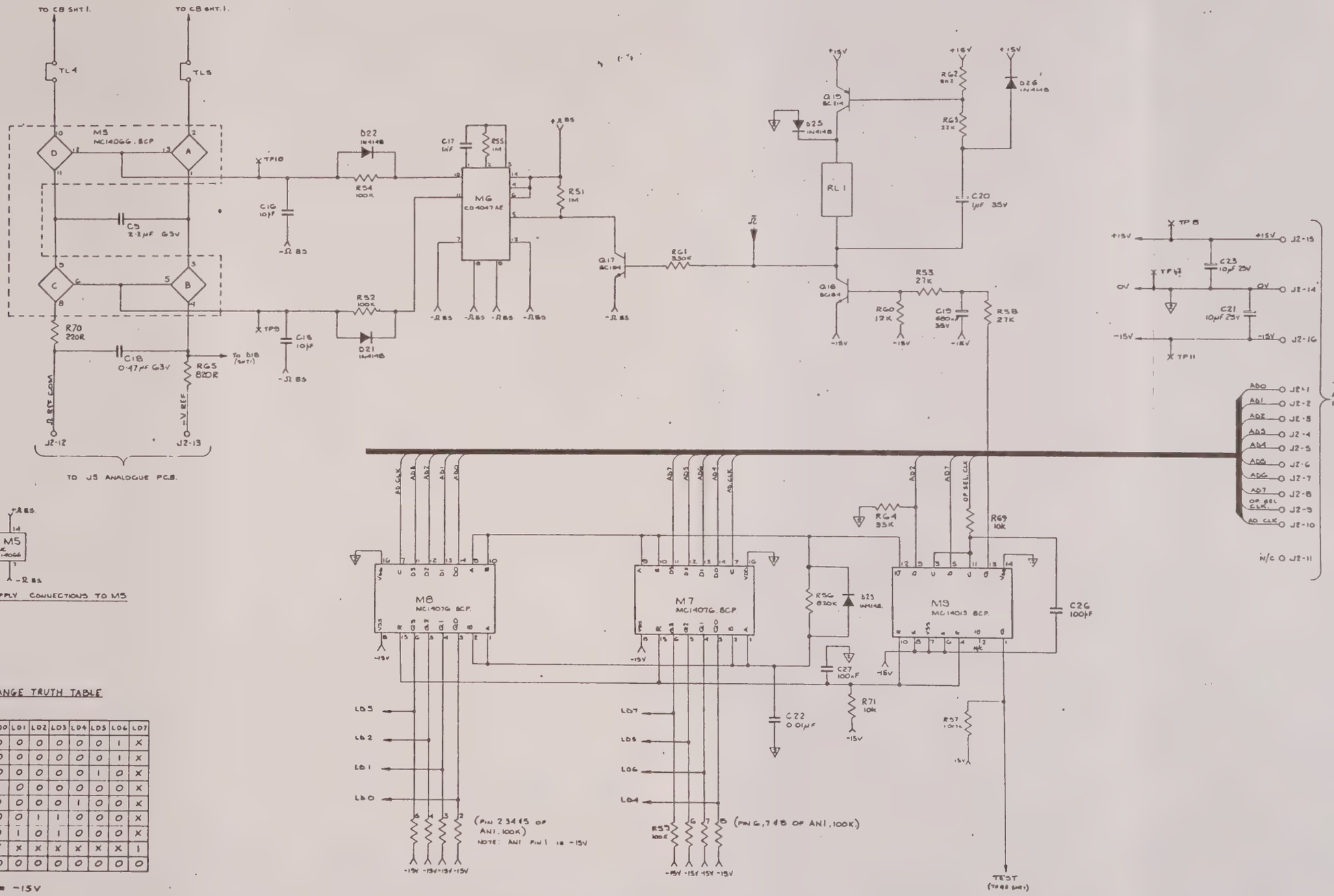
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS	CHANGES
1	RELEASED.
2	D23 WAS IN4148 BJ. RS6 WAS 1M. 5C0851 C13 = 04μF 11.3.79
3	ECO 1098 D24 BJ DELETED. 20-8-79 ECO 1098. Q7, CR12 NOW C2V7 R5, 10K NOW 3K3. R13, 18K NOW 24K
4	ECO 1130 RE-INSTATE D25 EC0933 Q21, Q22 & R74 UNSTABLE NOW U309's BJ 13-7-79
5	ECO 1054 & 1062. SEE SHT 1 FOR DETAILS. W.G.S. 6 PCB 80
6	ECO. 1109 C26 ADDED 11.___ 21.4.80
7	ECO. 1115. C26 WAS 1nF R69 ADDED. 11.___ 3.6.80
8	ECO. 1134 R70 ADDED 11.___ 19.6.80
9	ECO. 1127G R71 & C27 ADDED 11.___ 12.12.80
10	ECO 1300 RG5 WAS 2K7 30th MAY 82 BJ
11	SEE SHEET 1.



SUPPLY CONNECTIONS TO M5

RANGE TRUTH TABLE

RANGE	LD0	LD1	LD2	LD3	LD4	LD5	LD6	LD7
10Ω	0	0	0	0	0	0	1	X
100Ω	0	0	0	0	0	0	1	X
1KΩ	0	0	0	0	0	1	0	X
10KΩ	1	0	0	0	0	0	0	X
100KΩ	0	0	0	0	1	0	0	X
1MΩ	0	0	1	1	0	0	0	X
10MΩ	0	1	0	1	0	0	0	X
FILTER	X	X	X	X	X	X	X	1
Ω	0	0	0	0	0	0	0	0

LOGIC '0' = -15V
LOGIC '1' = 0V
LOGIC 'X' = 'DONT CARE' CONDITION

DRAWN BJ	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ° ' INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	MATERIAL ___	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH ___	TITLE OHMS PCB CIRCUIT. 1061.
DATE 20-4-79	DATE				DRAWING No. 430331

DRAWING SIZE A1

SHEET 2 OF 2

DRAWING No
400332
FIRST USED ON

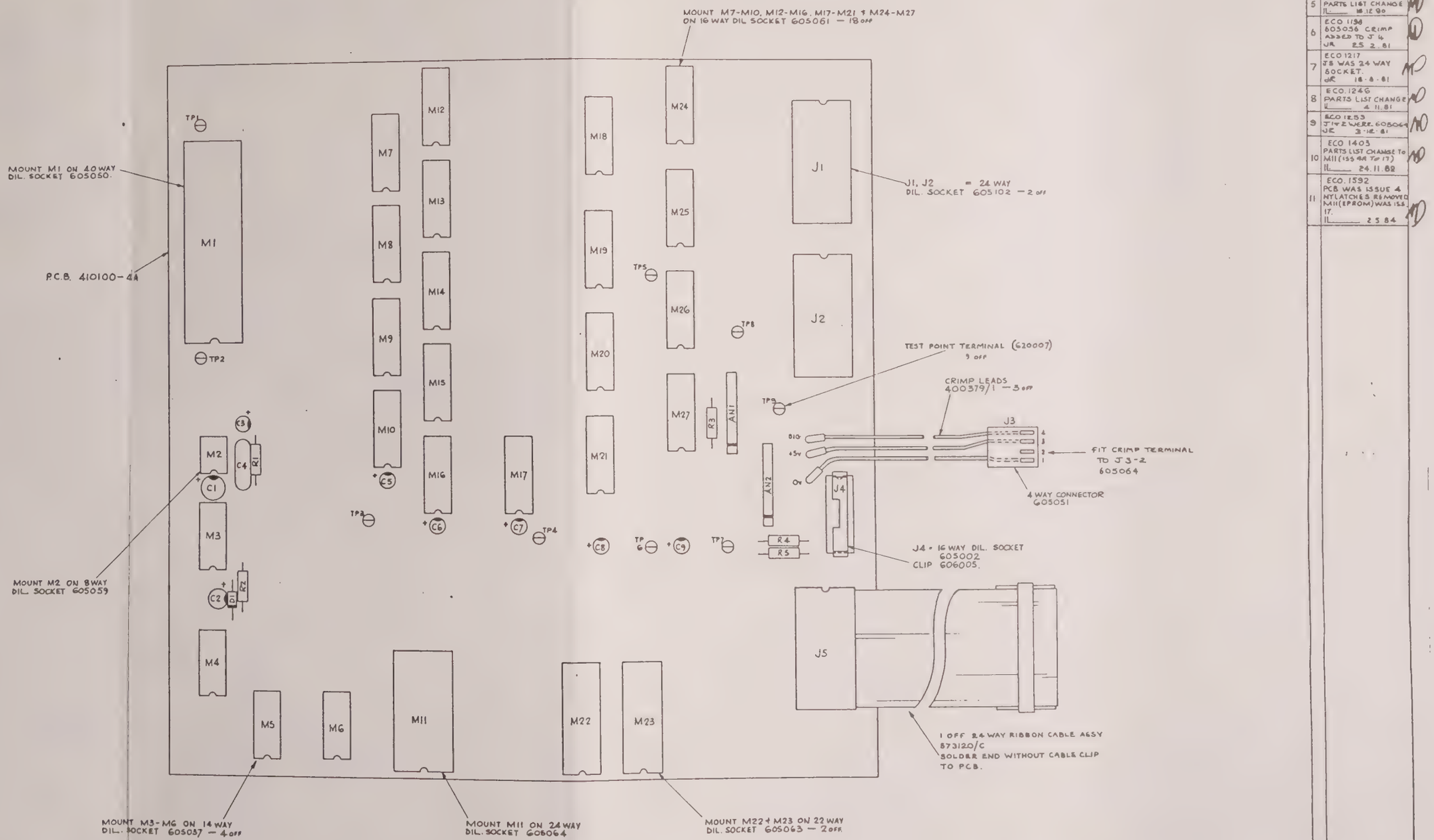
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
1	RELEASED TO PROD" IL 12.80
2	ECO 1078 TEST POINTS TP1- TP9 ADDED J3 PINS RE-ORGANISED PCB WAS ISS 3 IL 12.2.80
3	ECO 1093 PARTS LIST CHANGE IL 25.3.80
4	ECO 1137 PARTS LIST CHANGE IL 29.7.80
5	ECO 1168 PARTS LIST CHANGE IL 18.12.80
6	ECO 1198 605036 CRIMP ADDED TO J4 JR 25.2.81
7	ECO 1217 J3 WAS 24 WAY SOCKET. JR 18.3.81
8	ECO 1246 PARTS LIST CHANGE IL 4.11.81
9	ECO 1253 J1+2 WERE 605064 JR 3.12.81
10	ECO 1403 PARTS LIST CHANGE TO M11 (ISS 9A TO 13) IL 24.11.82
11	ECO 1592 PCB WAS ISSUE 4 NYLATCHES REMOVED M11(EPROM) WAS ISS 17. IL 25.8.4



CIRCUIT DIAGRAM = 430332-2

DRAWN IL	CHECKED MJA	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °	MATERIAL —	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH —	TITLE BCD/PROGRAM PCB ASSY	DRAWING No. 400332 SHEET 1 OF 4
DATE 31.1.80	DATE					

DRAWING No. 430332

FIRST USED ON 1061

NB M7-M10 & M12-M16 = MC14076 BCP

ASSEMBLY 400305

ISS.	CHANGES
1	
2	ECO 1078 TEST POINTS ADDED PRINT INHIBIT LINE ADDED. IL 25 2 80
3	ECO. 1165 M1 WAS MC68A21 IL 16 12. 80

ASSEMBLY 400305

ASSEMBLY 400312

PRINT INHIBIT

M1 MC6821P

M2 555

M11 2716

M23 5101L-1

M22 5101L-1

M7 M8 M9 M10 M12 M13 M14 M15 M16

TO J3 DIGITAL ASSY 400328

DRAWN IL	CHECKED A. J.	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR + °
TRACED	APPROVED	SCALE —	MATERIAL —
DATE 11. 2. 80	DATE	NOT TO BE SCALED	FINISH —

datron ELECTRONICS LTD. NORWICH.

TITLE BCD / PROGRAM

DRAWING SIZE A1

DRAWING No. 430332

SHEET 1 OF 2

DRAWING No. 430332

FIRST USED ON 1061

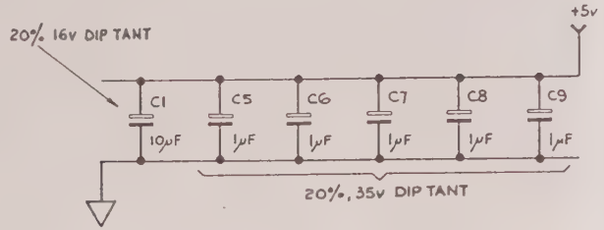
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

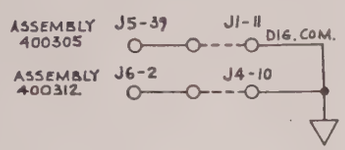
ALL BURRS TO BE REMOVED

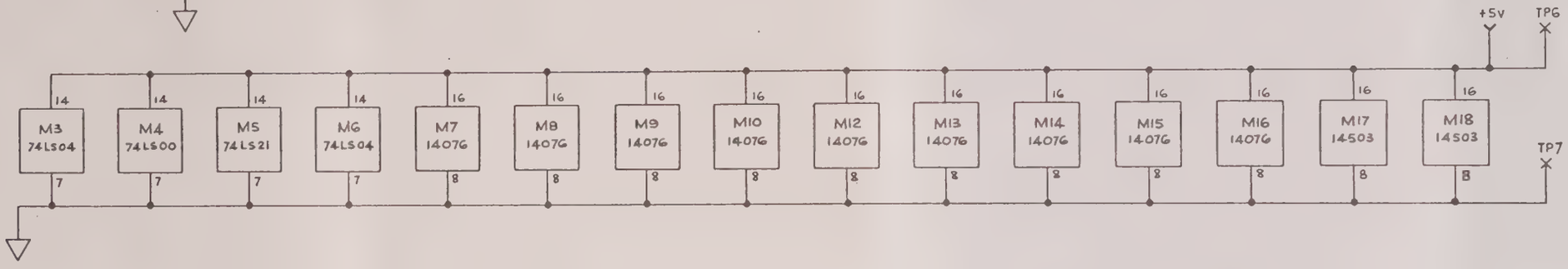
NOTES

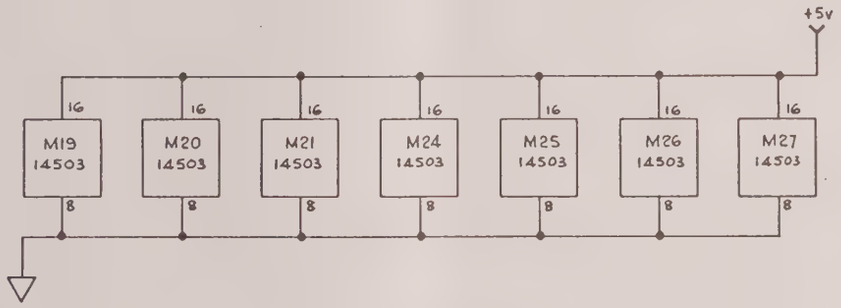
ISS.	CHANGES
1	
2	TP6 & TP7 ADDED IL 22.2.80 AJ.
3	SEE SHEET 1

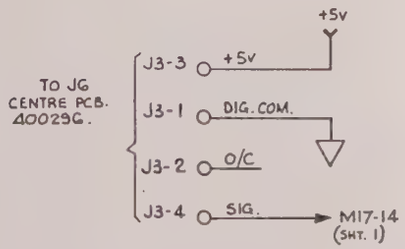


N.B. ALL RESISTORS SHOWN ON SHEET 1 = 5%, ¼W, CARBON FILM.









DRAWN IL	DATE 11.2.80	DIMENSIONS IN MILLIMETRES	METRIC DIMENSIONS ANGULAR ± ½° DECIMAL TO 2 PLACES ± ·1mm DECIMAL TO 1 PLACE ± ·2mm WHOLE DIMENSIONS ± ·4mm UNLESS OTHERWISE STATED	MATERIAL ——	DRAWING SIZE A2
CHKD. A.J.	DATE 15.2.80	SCALE —— NOT TO BE SCALED		FINISH ——	
APPD.	DATE			TITLE BCD/PROGRAM	DRAWING No. 430332

datron ELECTRONICS LTD. NORWICH.

SHEET 2 OF 2

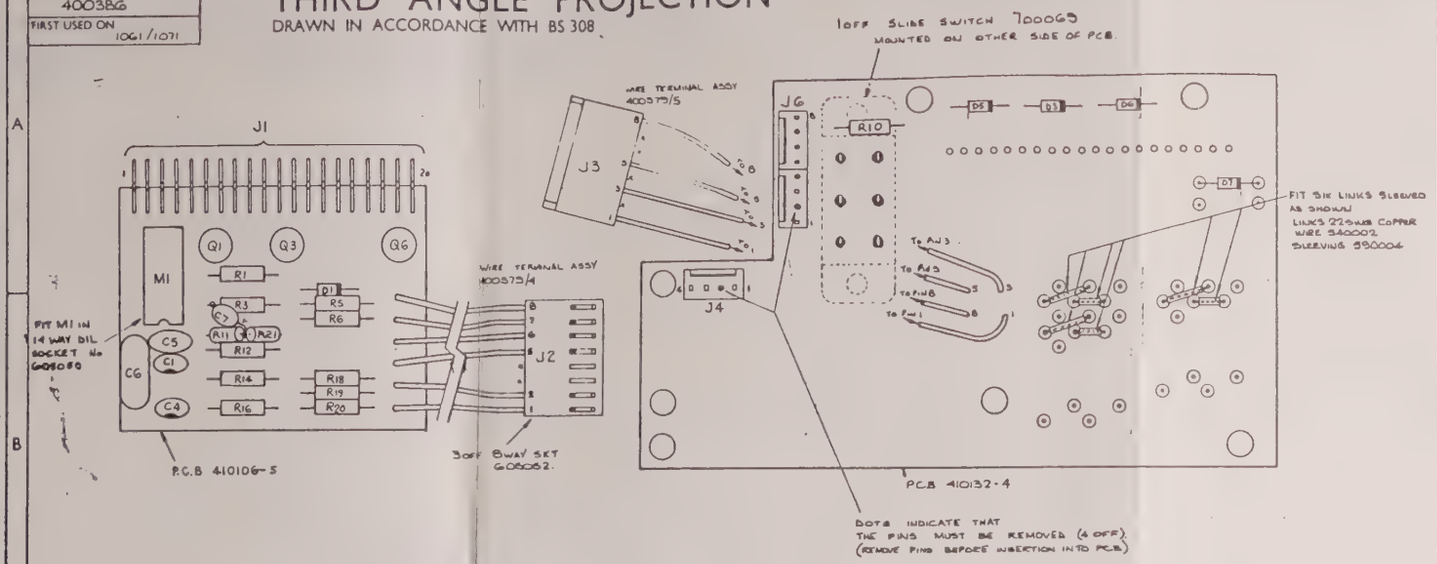
J.W. 2042

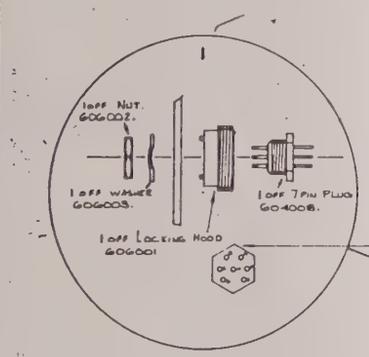
DRAWING No. 4003BG

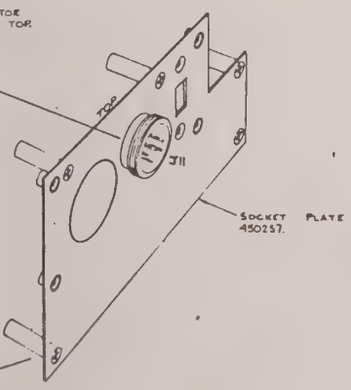
FIRST USED ON 1061/1071

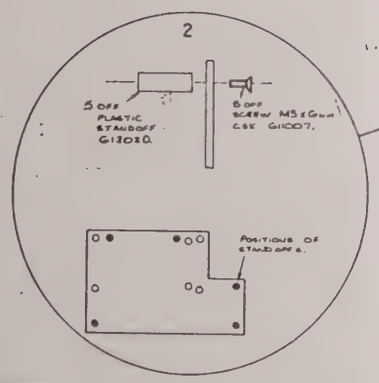
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308




1off NUT. 606002.
1off WASHER 606003.
1off 7PIN PLUG 604008.
1off LOCKING HOOD 606001
THIS PART OF CONNECTOR MUST BE TOWARDS THE TOP.





PROCEDURE.

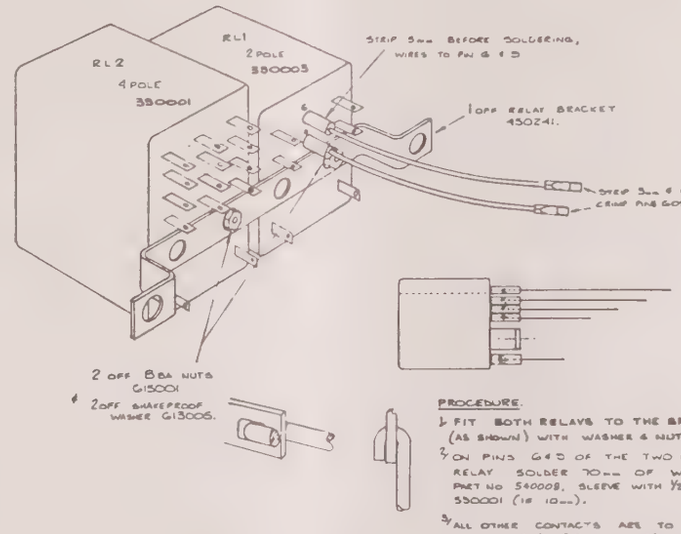
1. ASSEMBLE THE 7 PIN PYE PLUG. FIRST & ENSURE THAT THE CONNECTOR IS SECURED IN THE POSITION SHOWN.
2. ASSEMBLE THE 5 STANDOFF'S IN THE POSITION SHOWN BY THE LARGE DOTS.

ALL BURRS TO BE REMOVED

NOTES

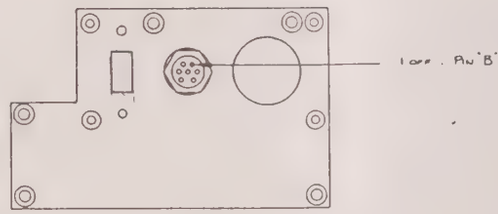
IMPORTANT.

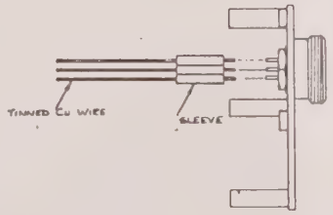
FOR CRIMP PINS USE TOOL N° HTR12G2A.



PROCEDURE.

1. FIT BOTH RELAYS TO THE BRACKET (AS SHOWN) WITH WASHER & NUT.
2. ON PINS 6 & 5 OF THE TWO POLE RELAY SOLDER 70mm OF WHITE WIRE PART NO 540008, SLEEVE WITH ½ PIECE OF 550001 (IS 10mm).
3. ALL OTHER CONTACTS ARE TO HAVE APPROXIMATELY 60mm OF 22 SWG TINNED COPPER WIRE PART N° 540002 & SLEEVE EACH WITH ½ PIECE OF 550001.
4. STAGGER LENGTHS OF TINNED COPPER WIRE, TO ASSIST FITTING TO PCB, AS SHOWN.





PROCEDURE

1. ALL PINS ON THE SOCKET EXCEPT PIN B (SHOWN SOLID), SOLDER ON 60mm OF TINNED COPPER WIRE PART N° 540002 & SLEEVE ALL JOINTS WITH APPROXIMATELY 10mm OF SLEEVING PART No 550001.

THERE SHOULD NOW BE 6 WIRES.

CHANGES

1		Released to Production 8.6.79
2		ECO 872 & 895 RL1 & RL2 WERE 1TT. 450267 was 450188 Male Plug 604001 DELETED EJ 25.10.79
3	ND	ECO 961 6.11.79 R8 & R21 ADDED M16 Socket Addod 1off 400379/4 added. DELETE D6. PCB 5 UPRATED. EJ
4	AD	ECO 1034 R17 MOVED & WAS 33K 2.1.80
5	AD	ECO 1085 POLARISING KEY FOR J3 REMOVED. J5 REMOVED - S&L SHEET 2 11.2.80
6	AD	ECO 1167, 1154 R10, R14, R16, R18, R19, R20, C4, C5, C6, D3, D6, D7 & D8 ADDED. 23.9.80
7	AD	ECO 1193 R10 WAS 12K VR 8.12.80
8	AD	ECO 1328 R11 WAS 18K, C4 WAS 6n8. R21 AND C7 ADDED. VR 12.8.82

DRAWN W.G Smith	CHECKED MJD	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS DECIMAL TO 3 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; UNLESS OTHERWISE STATED; ANGULAR ± °	MATERIAL	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH	TITLE REAR INPUT ASSY	DRAWING No. 4003BG / SHEET 1 OF 8
DATE 10-MAY-79.	DATE 17 MAY 79.					

DRAWING No 400386

FIRST USED ON 1061/71

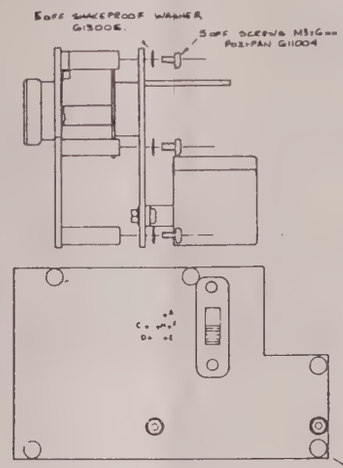
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1	PLANNED TO PRODUCTION 11 MAY 79 WGS
2	SEE SHEET 1 E & J 25-10-79. AD
3	SEE SHEET 1 & 2 TYPED AB
4	SEE SHEET 1. 2-1-80 AB
5	ECO 1085 J5 WAS 9 WAY CONN 22.4.80 AD
6	SEE SHEET 1 23.9.80 AB
7	SEE SHEET 1 22.2.81 AB
8	SEE SHEET 1 12.5.82 AB



3./ BOTTOM VIEW SHOWS THE HOLES IN THE P.C.B (INDICATED BY CROSSES). THESE HOLES ARE FOR THE WIRES FROM THE 7 PIN SOCKET

IT IS A SUGGESTION THAT THE WIRES ARE STAGGERED TO MAKE ASSY EASIER (AS WIRING THE RELAYS).

PROCEDURE.

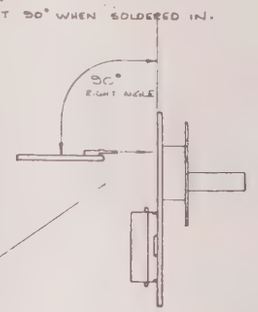
① THE WIRES SHOULD LINE UP WITH THE APPROPRIATE HOLES. IE. WIRE FROM PIN A OF SOCKET SHOULD BE INSERTED INTO HOLE A, WIRE FROM PIN C TO HOLE C AND SO ON.

② WHEN THE WIRES ARE INSERTED IN THE APPROPRIATE HOLES. SECURE THE 2 ASSY'S TOGETHER WITH THE M3 x G mm POZI PAN SCREWS & SHAKEPROOF WASHERS, SCREWED IN THE 5 STANDOFFS. (AS DETAILED)

③ LIGHTLY PULL ON THE TIN COPPER WIRES SO THE WIRE IS TAUT; THEN SOLDER & CROP IN THE USUAL MANNER.

2./ INSERT CONNECTORS AS SHOWN. SOLDER ALL 20 AMP PINS & CROP LEGS IN USUAL MANNER

THE BOARD IS TO BE AT 90° WHEN SOLDERED IN.



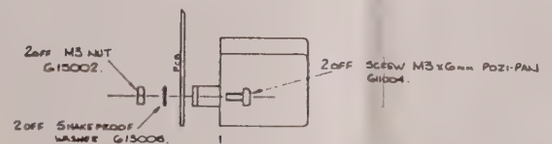
1./ SKETCH BELOW SHOWS THE 2 RELAY & BRACKET ASSEMBLY (FROM SHEET 1). THE CROSSES INDICATE HOLES IN THE PCB INTO WHICH THE WIRES FROM THE RELAYS ARE INSERTED.

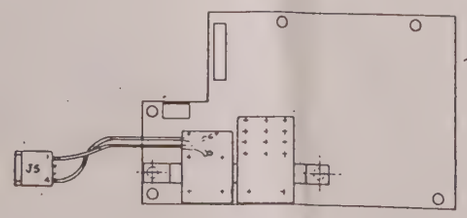
PROCEDURE

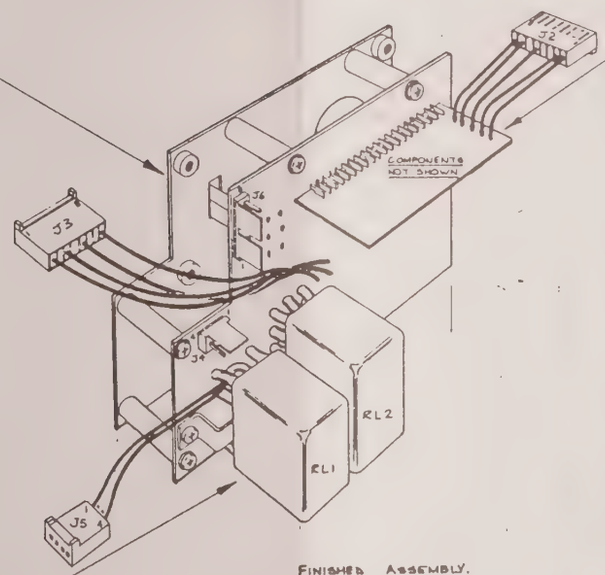
① INSERT THE WIRES INTO THE APPROPRIATE HOLES IN THE PCB (WIRES CUT AT DIFFERENT LENGTHS TO AID ASSEMBLY).

② WHEN THE WIRES ARE ALL IN PLACE SECURE THE BRACKET TO THE PCB USING THE SCREWS, WASHERS & NUTS SHOWN BELOW.

③ SOLDER & CROP WIRES IN THE USUAL MANNER. INSERT THE CRIMP PINS INTO SOCKET J5 PIN 6 OF RELAY TO PIN 4 OF J5 & PIN 5 TO PIN 1 AS SHOWN BELOW.







FINISHED ASSEMBLY.

DRAWN W G SMITH	CHECKED MJD	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS: DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64; METRIC DIMENSIONS: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; ANGULAR ±.° UNLESS OTHERWISE STATED	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A1
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH ——	TITLE REAR INPUT ASSY	DRAWING No. 400386
DATE 10 MAY 79.	DATE 11 MAY 79.					SHEET 2 OF 8

DRAWING No. 430386

FIRST USED ON 1061/1071

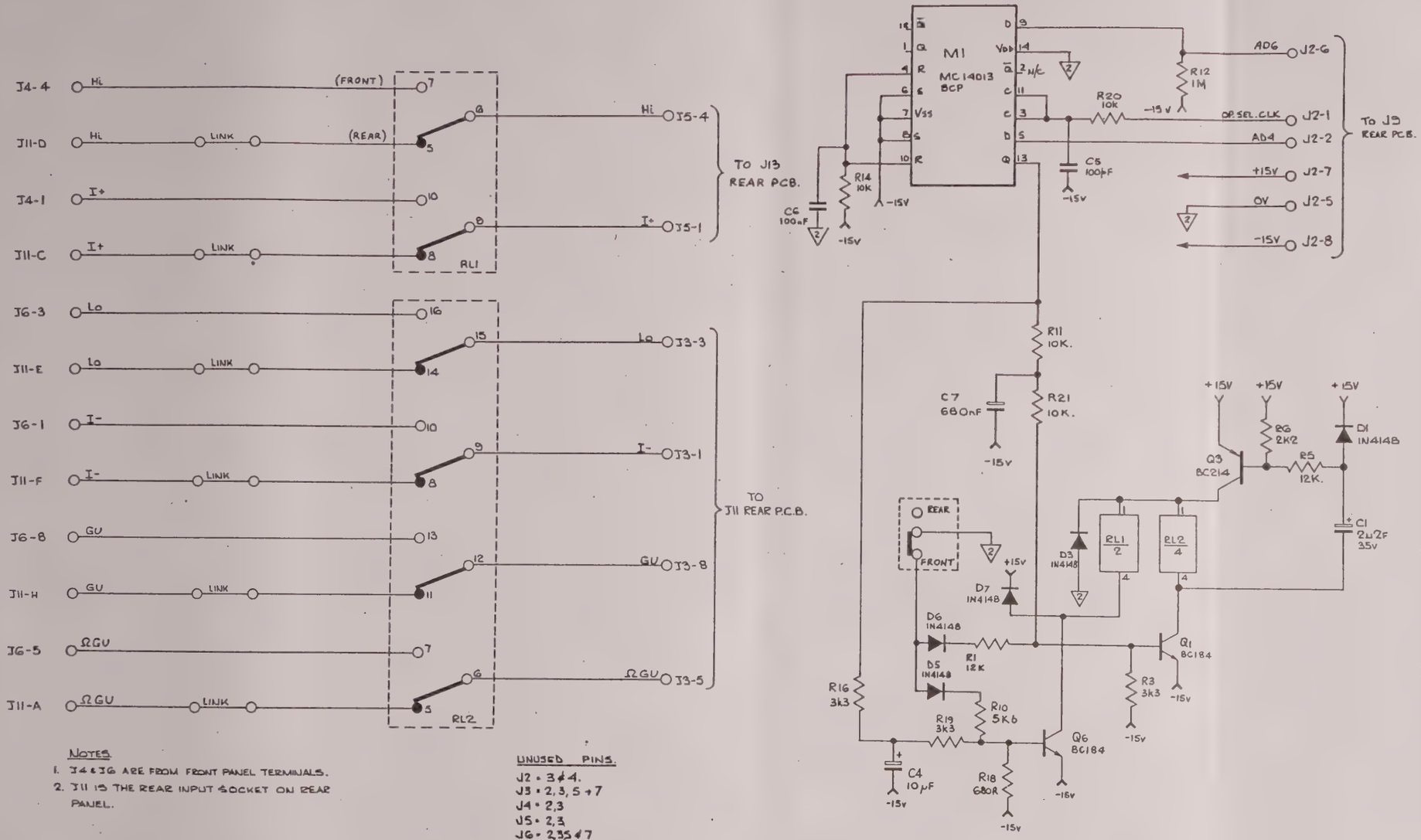
THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

ISS.	CHANGES
A	
1	RELEASED TO PRODUCTION 17 MAY 79 W.G.S.
2	ECO 1081. B.J. M1 ADDED R11 " R12 " D5 DELETED. 6-11-79
3	ECO 1084. B.J. R12 WAS JOINED BETWEEN J2-6 & M1-14. 2-1-80.
4	ECO.1085 BS- BETWEEN J4-1 & J5-1 REMOVED. PINS OF J4 & J5 RE-NUMBERED FOR 4 WAY CONNECTORS. JL 22.4.80
5	ECO. 1167. 1154, R1 WAS 18K. R10. R14. R16. R18 R20. C4. C5. C6. D5. D6. D7 & Q6 ADDED. JL 23.9.80
6	ECO 1199 R10 WAS 12K VR 24.2.81
7	ECO 1328 C4 WAS 6μ8F R11 WAS 18K R21 AND C7 ADDED. JR 13.5.82



NOTES

1. J4 & J6 ARE FROM FRONT PANEL TERMINALS.
2. J11 IS THE REAR INPUT SOCKET ON REAR PANEL.

UNUSED PINS.

J2 • 3 & 4.
J3 • 2, 3, 5 & 7
J4 • 2, 3
J5 • 2, 3
J6 • 2, 3, 5 & 7

DRAWN W.G.SMITH	CHECKED	DIMENSIONS IN MILLIMETRES	TOLERANCES ANGULAR ± ½° INCH DIMENSIONS DECIMAL TO 3 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.
TRACED	APPROVED	SCALE NOT TO BE SCALED		FINISH ——	TITLE REAR INPUT CIRCUIT DIAGRAM.
DATE 8 MAY 79	DATE 17 MAY 79				DRAWING No. 430386

DRAWING SIZE A2

SHEET 1 OF 1.

DRAWING No 400402

FIRST USED ON 1061/1071

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. DO NOT FIT R7 FOR 1061 MODELS.

ISS.	CHANGES
1	RELEASED TO PROD'N IL 17.3.79
2	ECO 956, 957 PARTS LIST CHANGES IL 8.10.79
3	ECO. 972, 973, 967, 953, 956. RL2 & RL3 WERE 1TT. RELAY WIRE WAS PVC. E11 WAS 120K R18 WAS 0K47 R127 100M. ADDED B.J. 25-10-79
4	ECO 999, 1007 PARTS LIST CHANGES R128, C66, C66 & D16 ADDED IL 31.10.79
5	ECO 1010. WIRE T'BOX ASSYS WERE 400397/7 & 400397/5. D10 SHIELD SIDE TAB ADDED. BJ. 5-11-79.
6	ECO 1011/1014 1000, 1036 C65 & D10 REMOVED D10 REPLACED BY C67 FITTED IN PARALLEL WITH R129. VALUE CHANGES TO FL IL 2.1.80
7	ECO. 1058 C68, C69 ADDED. SCREWS TO SECURE GUARD SHIELD WERE 3 OFF GI1015 IL 24.1.80
8	ECO 1089. E7107 WAS 680K 400661 C70 33K 1013310 ADDED W.B.S. TO PCB 80
9	ECO. 1082 PARTS LIST CHANGES IL 27.2.80
10	ECO. 1112, 1095, 1113 C71 R150 ADDED J2-4 WAS 605057 PTs Lt. CHANGE TO R50 IL 14.4.80
11	ECO. 1133 D17 ADDED. IL 13.6.80
12	ECO. 1150 C72 & R155 ADDED IL 24.9.80
13	ECO 1190 PARTS LIST CHANGES IL 5.1.81
14	ECO 1201, 1207 C34 & C72 DELETED. R136 ADDED. OTHER PARTS LIST CHANGES IL 5.4.81
15	ECO 1217 J1 WAS 16 WAY SOCKET. JR 19-8-81
16	ECO. 1352 C67, C71 & R150 DELETED. C34, C74, D18 & D19 ADDED. C48 WAS 12pF C46 WAS 6p8F. D17 CATHODE WAS SOLDERED D14 ANODE IL 7.3.82
17	ECO. 1425 C75 & C76 ADDED D13 WAS IN4148 R118 WAS 82K R36 WAS 1M2 IL 25.11.82
18	ECO 1446 R127 & R155 DELETED R34, R104 & R106 VALUE CHANGE ETC. 10-2-83
19	ECO. 1586 SLEEVE 590002 ADDED TO D18/D19 IL 6.3.84
20	ECO. 1622, 1626.- R73 WAS 120K R101 WAS 220R R136 WAS 27k Q35/Q36 WERE D18/D19 RESP (INASFAR) RE-POSITIONED AS WITH D17 IL 2.5.84
21	ECO 1642 R20 WAS 220K NOW R20 = 750K T1, 2, 3 & 4 NYLATCH NOW FITTED AFTER ATE. BJ. 11-6-84
22	ECO 1730 C77 & Q37 ADDED R129 WAS 2k2 R63 WAS 2k2 R115 WAS 8k2 IL 23.10.84

ALL TEST LINKS TO BE FORMED AS TEST POINTS

NOTE. WIRES OF J2 SOCKET TO BE AS SHOWN.

CLAMP SHIELD 450249 TO BE SECURED WITH M3.6 — POZI-CSK SCREWS GI1007 3 OFF. TO M3 USE STANDOFFS.

R134 CONNECTED IN SERIES WITH C57 & SOLDERED INTO CLOVERLEAF AS SHOWN.

'A'

SLEEVE ALL SOLDERED JOINTS WITH HALF PIECE OF 590001

ALL WIRES TO RELAY PINS 7/0.2 PTFE INSULATED - WHITE 512959.

25mm

35mm

50mm

R133 SOLDERED BETWEEN RL3 PINS 7 & 10. LEADS SLEEVED WITH 590001

VIEW ON BASE OF RELAY RL3

BRASS STRIP 85mm LONG 630107 N.B. STRIP SHOULD BE SOLDERED AS CLOSE AS POSSIBLE TO LEFT HAND SIDE OF COPPER 'LAND' (AS SHOWN). ENSURE THAT SOLDER DOES NOT ENCROACH ON AREA AROUND HEX. STANDOFF POSITION. STRIP SHOULD ALSO BE 3.0mm FROM EDGE OF BOARD TO CLEAR PCB TRACK.

2 OFF SOLDER TERMINAL 620003

CRIMP TERMINAL (FOR BLANKING ONLY) 605056

WIRE/TERMINAL ASSY. 400379/1

FIT C70 ACROSS EGG AS SHOWN

P.C.B. 410156-4

2 OFF P.S.V. TERMINAL 602001

WIRE 110mm PTFE INSULATED 7/0.2 WHITE 512959, PLUG GOLD CRIMP TERMINAL 605057. FITTED AS SHOWN.

8 WAY POLARISED SOCKET 605052

NOTE: TL1, 2, 3, 4 AND ALL NYLATCHES TO BE FITTED AFTER ATE.

LINK C57 TO C61 & R123 VIA CLOVERLEAF TERMINALS USING SLEEVED LINKS 540002/590004

1 OFF 16 WAY RIBBON CABLE ASSY ST1008/C SOLDER END WITHOUT CABLE CLIP TO PCB.

TEST POINTS & TEST LINKS USE 27 SWG. B.T.C. WIRE 540002

R7 IS REMOVED WHEN BOARD IS USED IN 1061

MOUNT M6 ON 'BREAKAWAY' TERMINAL STRIPS. 602006 - 16 OFF

BRASS STRIP 35mm LONG BUTT END OF STRIP TO ADJOINING 85mm STRIP AND SOLDER AS SHOWN

N.B. ENSURE THAT IDENT MARK IS AT TOP OF D11 AS SHOWN

VIEW IN DIRECTION OF ARROW 'B'

SLEEVE LEADS OF C68/C69/C74/C75/C76 BEFORE SOLDERING. USE SLEEVE 590004 A/R.

FIT C49, C51, C58, C59, C60 (GLASS CAPS.) TO PCB MOUNTED ON 'THIN' END.

15 OFF CLOVERLEAF 620005 N.B. TERMINALS SHOULD BE FITTED WITH SOLDER CUPS ON X-Y AXIS, AS SHOWN.

MOUNT M8, M4 & M7 ON 16 WAY D.I.L. SOCKET - 3 OFF 605061

3-48 UNC NUT 615005 2 OFF
M2.5 WASHER 613014 2 OFF

M3 x 8 P/PAN SCREW GI1006 2 OFF
M3 NUT 615002 2 OFF
M3 SHAKEPROOF 613005 2 OFF

RELAY BRACKET 459112 2 OFF

LINK PINS 11 & 13 ON M4 540002 (B.T.C. WIRE) SOLDER ON UNDERSIDE.

MOUNT M5 ON 14 WAY D.I.L. SOCKET 6030

SLEEVE LEADS OF D17 BEFORE SOLDERING. SLEEVE 590004

3 OFF M3 STANDOFF 612021 SECURE FROM UNDERSIDE WITH M3 x 8 P/PAN SCREW GI1016 3 OFF M3 SHAKEPROOF 613005 3 OFF

TEST POINT TERMINAL 620007 6 OFF.

SOLDER WIRE ENDS TO SOLDER TERMINALS & SLEEVE WITH 1/2 PIECE OF 590001

* NYLATCH GROMMET 617011 - 4 OFF

* NYLATCH PLUNGER 617010 - 4 OFF N.B. FIT FROM COMPONENT SIDE OF PCB.

SECTION A-A

SLEEVE LEADS OF C77 AND CENTRE PIN OF Q37 (590004)

NOTE: FIT E506 AS SHOWN ON DRG. FIT TCR506 AS SHOWN WITH FLAT AT TOP.

TCR 506

SLEEVE LINK USING 590004 SLEEVING

CUT THIS TRACK - COMP SIDE OF P.C.B.

WIRE FROM RL2 PIN 1 @ 25mm LG

SOLDER WIRE LINK BETWEEN RELAY COIL TERMINAL & PCB. AS SHOWN (IN SAME POSITION ON RL2 & RL3. 540002

To J2

N.B. Q35/Q36 NOT SHOWN IN THIS VIEW. THIS IS TO CLARIFY POSN OF R128 & Q38. WHEN FITTING Q35/Q36 ACROSS Q32 SLEEVE LEADS BEFORE SOLDERING 590004. SEE MAIN VIEW FOR POSN. CUT CENTRE PIN 3 OF Q35/Q36 CLOSE TO BODY OF DEVICE.

VIEW IN DIRECTION OF ARROW 'A'
SHOWING ONLY RELAY WIRING.

DRAWN IL	CHECKED R2W	DIMENSIONS IN MILLIMETRES	TOLERANCES INCH DIMENSIONS DECIMAL TO 2 PLACES ± .005 DECIMAL TO 3 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 4mm UNLESS OTHERWISE STATED ANGULAR ±.°	MATERIAL AS PARTS LIST	datron ELECTRONICS LTD. NORWICH.		DRAWING SIZE A1
TRACED	APPROVED	SCALE 2:1 NOT TO BE SCALED		FINISH	TITLE A.C. PCB. ASSY. (OPTION 10) 1061/1071	DRAWING No. 400402	SHEET 1 OF 17
DATE 22.8.79	DATE 3.9.79						

DRAWING No. 430402

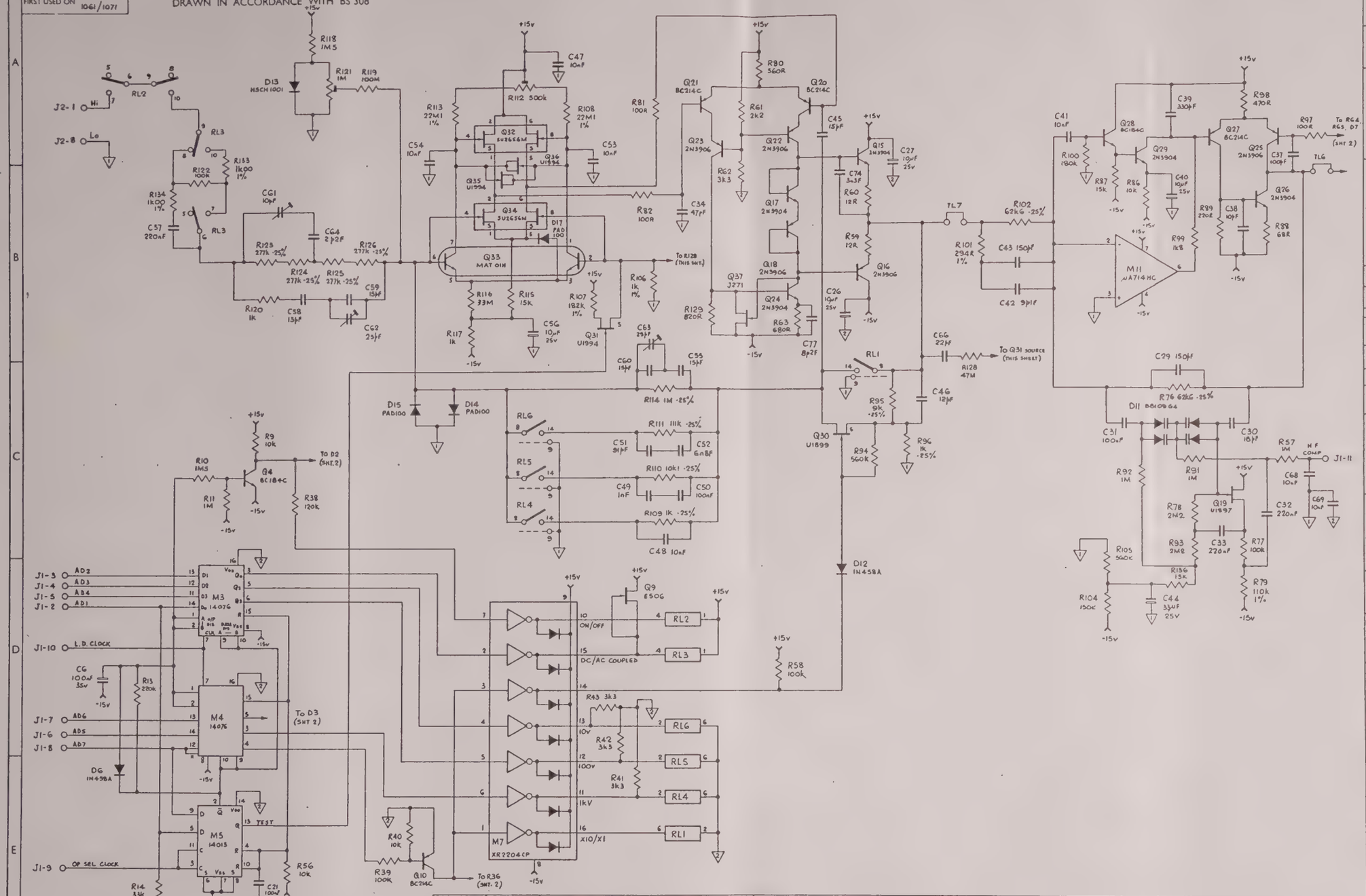
FIRST USED ON 1061/1071

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES



ISS.	CHANGES
1	RELEASED TO PROD'N IL 17.5.79
2	ECO 956, 957 R10 WAS 220k R11 WAS 18k R18 WAS 33k CG WAS 680pF SEE ALSO SHEET 2 IL 8.10.79
3	ECO 993, 996. R11 WAS 120K. R127 100M ADDED D13 WAS OA47 B.J. 24-10-79
4	ECO 999, 1007 R117 WAS 100R W4/11 & 12 LINKED C44 WAS 1µF C56 WAS 10µF C34 WAS 10µF C46 WAS 5pF C63 & C66 ADDED R81 WAS 100R R82 WAS 8K R128 ADDED SEE ALSO SHT. 2 IL 1.11.79
5	ECO 1011, 1016, 1036 C45 WAS 3p3F, C46 WAS 10pF, C34 WAS 27µF TANT. C63 REMOVED. C66 WAS 220pF. R128 WAS 15M R81 R82 WERE 330R C56 WAS 100µF. D10 REMOVED. C67, R129 ADDED. R106 WAS 10K, R105 WAS 68K C64 WAS 5p3F IL 2.1.80
6	ECO.1058 CG64 C65 ADDED IL 23.1.80
7	ECO 1069 SEE SHEET 2 IL 15.2.80
8	SEE SHEET 2
9	ECO. 1112, 1113 C71, R130, R133, R134 ADDED SEE ALSO SHEET 2 IL 16.4.80
10	ECO. 1135. D17 ADDED. IL 13.6.80
11	ECO 1160 R135 & C72 ADDED
12	ECO 1190 D11 WAS MUAM-2 C30 WAS 15pF IL 3.1.80
13	ECO. 1201, 1207 C34 & C72 DELETED R136 ADDED C39 WAS 100pF C44 WAS 10µF C45 WAS 10pF C46 WAS 8pF R79 WAS 110K Q28 WAS 2N3904 IL 8.4.81
14	ECO 1382 R130, C67, C71 DELETED C34, C76, D18 & D13 ADDED C45 WAS 12pF C46 WAS 6p8F D17 WAS CONNECTED TO Q33 PIN 6. (I.E. D17 CATHODE END) IL 8.9.82
15	ECO. 1428 R118 WAS 82k D13 WAS IN4148 SEE ALSO SHT. 2. IL 25.11.82
16	ECO 1446 R187 & R188 DELETED R94 WAS 270K R104 - 1GK R105 - 62K. BJ 16-2-83
17	ECO. 1622, 1626 Q35 & Q36 WERE D18/D19 RESP (IN458A) D17 WAS FITTED FROM Q33 PIN 5 TO 7. R79 WAS 120k R101 WAS 220R R136 WAS 27k IL 2.5.84
18	SEE SHEET 2.
19	ECO 1730 C77 & Q37 ADDED. R63 WAS 2K2 R115 WAS 8K2 R129 WAS 2K2 IL 23.10.84

DRAWN	CHECKED	DIMENSIONS IN
IL	R.J.W	MILLIMETRES
TRACED	APPROVED	SCALE —
DATE 20.8.79	DATE 3.9.79	NOT TO BE SCALED

TOLERANCES UNLESS OTHERWISE STATED
INCH DIMENSIONS: DECIMAL TO 1 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64
METRIC DIMENSIONS: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm
ANGULAR ± °

MATERIAL —

FINISH —

datron ELECTRONICS LTD. NORWICH.

TITLE 1061/1071 AC - PREAMP

DRAWING No. 430402

DRAWING SIZE A1

SHEET 1 OF 2

DRAWING No. 430402

FIRST USED ON

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

NOTES

1. RESISTOR R7 IS NOT FITTED ON A.C. ASSEMBLIES USED IN 1061's INSTRUMENTS.

ISS	CHANGES
1	RELEASED TO PROD'N IL 17.9.79
2	ECO 956. C2 WAS 680nF IL 8.10.79
3	SEE SHT 1. B.J. 24-10-79
4	R83 WAS 10K R74 WAS 1M6 D6 ADDED IL 1.11.79
5	ECO 1011. 1016, 1036 R26 WAS 180k R7 WAS 1M. IL 2.1.80
6	SEE SHEET 1
7	ECO. 1069 R70 WAS 680R R71 WAS 680R C70 ADDED IL 14.2.80
8	ECO 1082 R27 WAS 680k R36 WAS 820K R24 WAS 1M IL 27.2.80
9	ECO. 1172 R50 WAS 100R IL 14.4.80
10	SEE SHEET 1
11	SEE SHEET 1
12	SEE SHEET 1
13	SEE SHEET 1
14	SEE SHEET 1
15	ECO. 1425 R36 WAS 1M2. C75 & C76 ADDED SEE ALSO SHT. 1 IL 25.11.82
16	SEE SHEET 1
17	SEE SHEET 1
18	ECO 1648 R36 WAS 750K BJ 11th JUNE 84.
19	SEE SHEET 1

DRAWN IL	CHECKED RIO	DIMENSIONS IN MILLIMETRES	MATERIAL
TRACED	APPROVED	SCALE NOT TO BE SCALED	FINISH
DATE 20.8.79	DATE 3.9.79	TOLERANCES ANGULAR ± ° INCH DIMENSIONS DECIMAL TO 1 PLACES ± .005 DECIMAL TO 2 PLACES ± .010 FRACTIONAL ± 1/64 METRIC DIMENSIONS DECIMAL TO 2 PLACES ± 1mm DECIMAL TO 1 PLACE ± 2mm WHOLE DIMENSIONS ± 6mm UNLESS OTHERWISE STATED	

datron ELECTRONICS LTD. NORWICH.

TITLE 1061/1071 A.C. RMS SECTION.

DRAWING SIZE A1

DRAWING No. 430402

SHEET 2 OF 2

DRAWING No. 400427

FIRST USED ON

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ALL BURRS TO BE REMOVED

FIT CRIMP TERMINALS TO J4-2 & J4-4 605056

4 WAY CONNECTOR 605051

J4

CRIMP LEADS 400379/1 2 OFF

J5 AND J6 NOT FITTED

J1 24 WAY DIL SOCKET 605102.

J2 16 WAY DIL SOCKET 605002 CLIP 606005

24 WAY CABLE ASSY 573120/C. SOLDER END WITHOUT CABLE CLIP TO P.C.B.

MOUNT M1, M2 AND M3 ON 24 WAY DIL. SOCKET 605064 3 OFF

SLEEVE R8 LEAD WITH PTFE SLEEVE 590004.

22 SWG BTC WIRE 540002 SLEEVE. PTFE 590004

LINK A	LINK B
1065	1061
ONLY	1071
	1081

MOUNT M8, M11-M13 ON 14 WAY DIL SOCKETS 605060 4 OFF.

MOUNT M9 ON 40 WAY DIL SOCKET 605050

MOUNT M6 AND M10 ON 16 WAY DIL SOCKETS 605061 2 OFF

P.C.B 410165-4A

TESTPOINT TERMINALS 620007 5 OFF.

NOTES

ISS	CHANGES
9	ECO. 1538.1588 3 NYLATCHES REMOVED. P.C.B WAS 1552 C7 WAS 10μF TANT C2 WAS 10nF CD. C11-C17 ADDED. IL 28.2.84
10	ECO 1681 R8 ADDED. BJ. 26-7-84.

DRAWN JR	DATE 5·10·83	DIMENSIONS IN MILLIMETRES	METRIC DIMENSIONS ANGULAR ± ½° DECIMAL TO 2 PLACES ± ·1mm DECIMAL TO 1 PLACE ± ·2mm WHOLE DIMENSIONS ± ·4mm UNLESS OTHERWISE STATED	MATERIAL ——	datron ELECTRONICS LTD. NORWICH.	DRAWING SIZE A2
CHKD.	DATE	SCALE 2:1 NOT TO BE SCALED		FINISH ——	TITLE IEEE P.C.B ASSY 1065 1061 1071 1081	DRAWING No. 400427 SHEET 1 OF 5
APPD.	DATE					

JW 2042

DRAWING No. 430427

FIRST USED ON 1065

THIRD ANGLE PROJECTION

DRAWN IN ACCORDANCE WITH BS 308

ISS	CHANGES
A	
B	M7 (X) LK2 DELETED. IL 26 11 80
1	RELEASED 18 12 80
2	ECO 1237 R5 & C8 ADDED 29-3-82. BJ.
3	ECO 1347 M6 AND C9 ADDED JR 7-7-82
4	ECO 1448 M4 AND M5 DELETED IL 16 2 83.
5	ECO 1549. R7/C10 ADDED IL 28.10.83
6	ECO. 1538 C2 WAS 10nF C.D. C7 WAS 10µF TANT. C11-C17 ADDED. IL 28.2.84
7	ECO 1681 R8 ADDED. 20-7-84. BJ.

ASSY 400429

ASSY 400346

TO J3 DIGITAL PCB

TO J8 CENTRE PCB

SPARE GATES

ADDRESS BUS

DATA BUS

X10BD

M1 MC3447P

M2 MC3447P

M3 TMS2532

M9 MC68488P

M10 MC145388CP

J1 J2 J3 J4 J5 J6 J27

R1 100K

R2 10K

R3 100K

R4 100K

R5 1K

R6 560R

R7 1K

R8 3K3.

C1 10µF

C2 100nF

C3 10µF

C4 10µF

C5 1µF

C7, C11-C17 100nF

C8 10nF

C9 680pF

C10 100pF

AN1 100R

LK1 (1088 ONLY)

LK2 (1061 1071 & 1081 ONLY)

TP1 TP2 TP3 TP4 TP6

DRAWN IL	CHECKED D.NEILSON	DIMENSIONS IN MILLIMETRES	MATERIAL
TRACED	APPROVED	SCALE NOT TO BE SCALED	FINISH
DATE 14 10 80	DATE		

TOLERANCES (INCH DIMENSIONS): DECIMAL TO 3 PLACES ± .005; DECIMAL TO 2 PLACES ± .010; FRACTIONAL ± 1/64. (METRIC DIMENSIONS): DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm. ANGULAR ± °. UNLESS OTHERWISE STATED

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DRAWING SIZE A1

TITLE: IEEE 488 OPTION CIRCUIT DIAGRAM 1065. 1061. 1071. 1081

DRAWING No 430427

SHEET 1 OF 1

DRAWING No. 400552

GUARD SHIELD 430288 TO BE SECURED WITH M3 × 8mm POZI-CSK SCREWS 611007 3 off, TO M3 HEX. STANDOFFS.

MOUNTING ICs			
No OF WAYS	PART No	No OFF	USED TO MOUNT
8	605059	4	M2 M8 M13 M16
14	605060	1	M7
16	605061	5	M3. 6. 12 15 17

TEST POINT TERMINAL 620007 12 off

ALTERNATIVE DEVICE FOR D26

TEST LINKS MADE FROM 22 SWG BTC WIRE 540002 A/R

SLEEVE ALL SOLDERED JOINTS WITH HALF PIECE OF 590001

'A'

CRIMP TERMINAL (FOR BLANKING ONLY) 605057

WIRE TERMINAL ASSY 400379/5

ALL WIRES TO RELAY PINS 7/0·2 PTFE INSULATED - WHITE 540008

VIEW SHOWING RELAY PIN Nos.

8 WAY POLARISED SOCKET 605052

WIRE 120mm PTFE INSULATED 7/0·2 WHITE 540008, PLUS GOLD CRIMP TERMINAL 605057 FITTED AS SHOWN

SOLDER TERMINAL 620003 2 off.

CLOVERLEAF TERMINAL 620005. 9 off

SLEEVE LEADS OF C80 BEFORE SOLDERING LEADS INTO CLOVERLEAF - 590004 POSITION CAP AND LEADS AWAY FROM LEVERLEAFS

BRASS STRIP 220mm LONG 630107 N.B. STRIP SHOULD BE SOLDERED AS CLOSE AS POSSIBLE TO LEFT HAND SIDE OF COPPER 'LAND' AS SHOWN

SMALL CLOVERLEAF TERMINAL 620001 · 5 off

PTFE BUSH 620008 2 off

RELAY WIRING		
FROM	TO	LENGTH (mm)
RL2 PIN 1	SOLDER PIN 1	25
RL2 PIN 6	RL2 PIN 8	WIRE, SOLDERED JOIN, 540008
RL2 PIN 7	J3 PIN 1	120
RL2 PIN 10	RL3 PIN 9	40
RL3 PIN 1	SOLDER PIN 2	25
RL3 PIN 7	C/LEAF B	45
RL3 PIN 6	C/LEAF D	40
RL3 PIN 10	C/LEAF C	25
RL3 PIN 8	C/LEAF A	55

PCB 410217-4

F3V TERMINAL 602001 2off

MOUNT M11 ON 'BREAKAWAY' TERMINAL STRIP 602004. 16mm

16 WAY RIBBON CABLE ASSY 871085/C 1 off.

RELAY BRACKET 459112 2off

3·48 UNC NUT 615005 2off
M2·5 WASHER 613014 2off

M3 × 16mm SPACER 612021 SECURE FROM UNDERSIDE OF PCB.
M3 × 8mm POZI-PAN SCREW 611016
M3 CRINKLE WASHER 613025
3off EACH

M3 × 8 P/PAN SCREW 611016 2off
M3 NUT 615002 2off
M3 SHAKEPROOF 2off

SOLDER WIRE ENDS TO SOLDER TERMINALS & SLEEVE WITH ½ PIECE OF 590001

SOLDER WIRE LINKS BETWEEN RELAY TERMS AND PCB, AS SHOWN ON RL2 & RL3 540002

NYLATCH GROMMET 617011 3off

NYLATCH PLUNGER 617010 5off
N.B. FIT FROM COMPONENT SIDE OF PCB.

SECTION A-A

CERAMIC BEAD 630024 2off. (MOUNT D29)

DOT DENOTES OUTER FOIL (RED) END OF POLYSTYRENE CAPACITORS i.e. C92, C41, C43, C46, C57, C70

CROP PIN 8 FROM M22 BEFORE FITTING INTO PCB

LINK MADE FROM 7/0·2 PTFE INSULATED WHITE WIRE 540008 × 70mm FIT TO UNDERSIDE OF PCB.

VIEW IN DIRECTION OF ARROW 'A'
SHOWING RELAY WIRING

N.B. C77 REMOVED FROM VIEW, FOR CLARITY.

ISS.	CHANGES
1	RELEASED 13.9.84
2	ECO 1700 & 1702 R145 & C104 ADDED PCB No 5155.3 AP 14.9.84
3	ECO 1884 C54 WAS 1nF CERAMIC 24.1.85

DRAWN	DATE 4.4.84	DIMENSIONS IN MILLIMETRES	TOLERANCES: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; ANGULAR ± 1°
CHECKED L.O.G.	DATE 10-4-84	SCALE 2:1	UNLESS OTHERWISE STATED
APPD.	DATE 13-9-84	NOT TO BE SCALED	FIRST ANGLE PROJECTION

MATERIAL

FINISH

ASSY DRG & PARTS LIST	400552
CIRCUIT DIAGRAM	430552
CHECK PROCEDURE	460552
CHECK LIST	470552

TITLE: 1061A/62.AC PCB ASSY (OP.12)

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DRAWING No. 400552

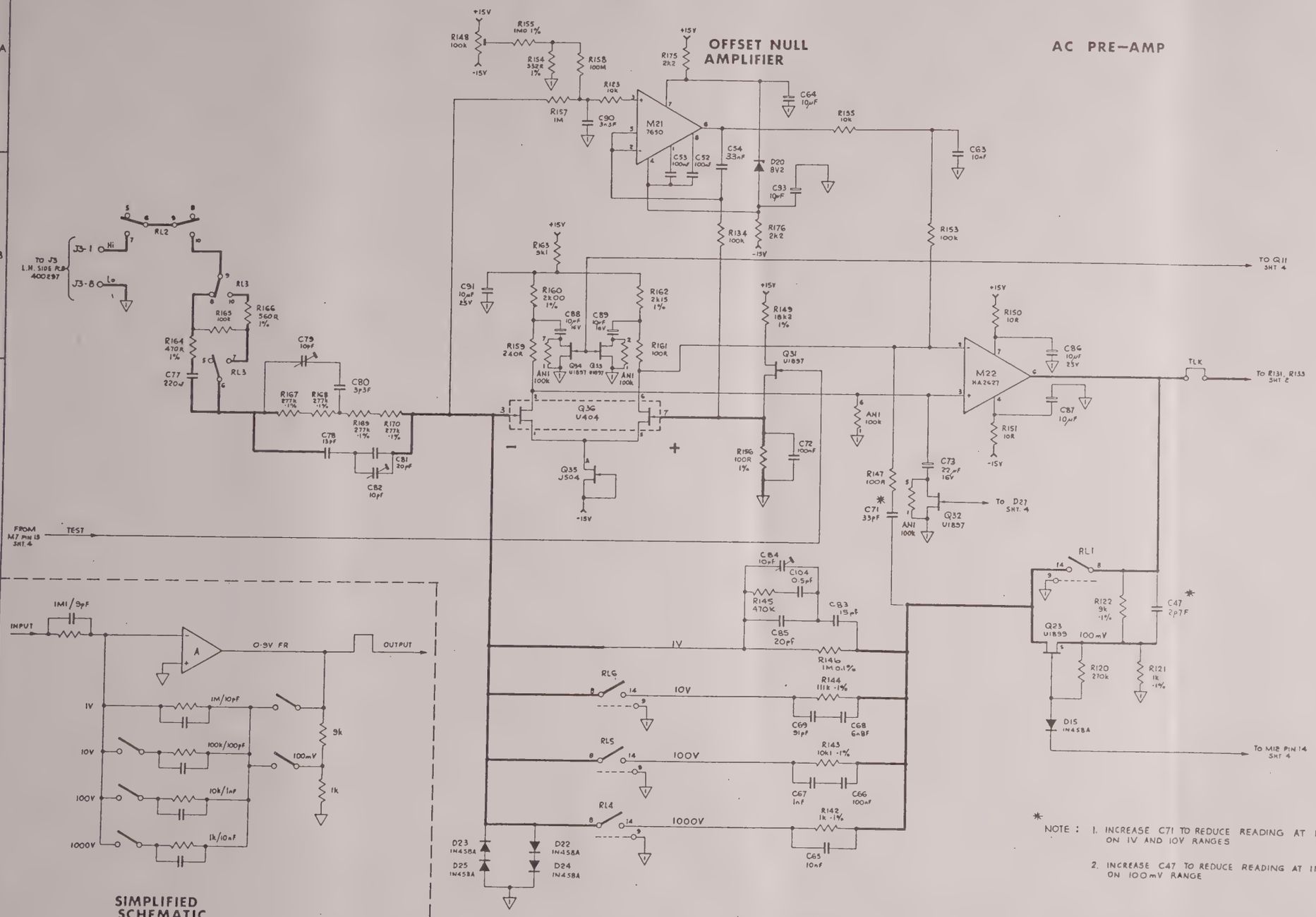
SHEET 1 OF 20

DRAWING No. 430552

BIAS CURRENT

OFFSET NULL AMPLIFIER

AC PRE-AMP



SIMPLIFIED SCHEMATIC

* NOTE : 1. INCREASE C71 TO REDUCE READING AT 1MHz ON 1V AND 10V RANGES

2. INCREASE C47 TO REDUCE READING AT 1MHz ON 100mV RANGE

ISS.	CHANGES
A	7.7.83
B	25.3.84
1	RELEASED 13.9.84
2	ECO.1702 C104 & RL45 ADDED R.P. 14.9.84
3	ECO.1824 C54 WAS 1nF CER DISC. I.L. 24.1.85

DRAWN I.L.	DATE 7.7.83.	DIMENSIONS IN MILLIMETRES	TOLERANCES DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; ANGULAR ± 30	MATERIAL	ASSY DRG & PARTS LIST 400352; CIRCUIT DIAGRAM; CHECK PROCEDURE 460552; CHECK LIST 470552
CHECKED L.O.Gr.	DATE 11-7-83	SCALE	UNLESS OTHERWISE STATED FIRST ANGLE PROJECTION	FINISH	TITLE 1061/62 AC PRE-AMP (OPTION 12)
APP.	DATE 13.9.84	NOT TO BE SCALED			

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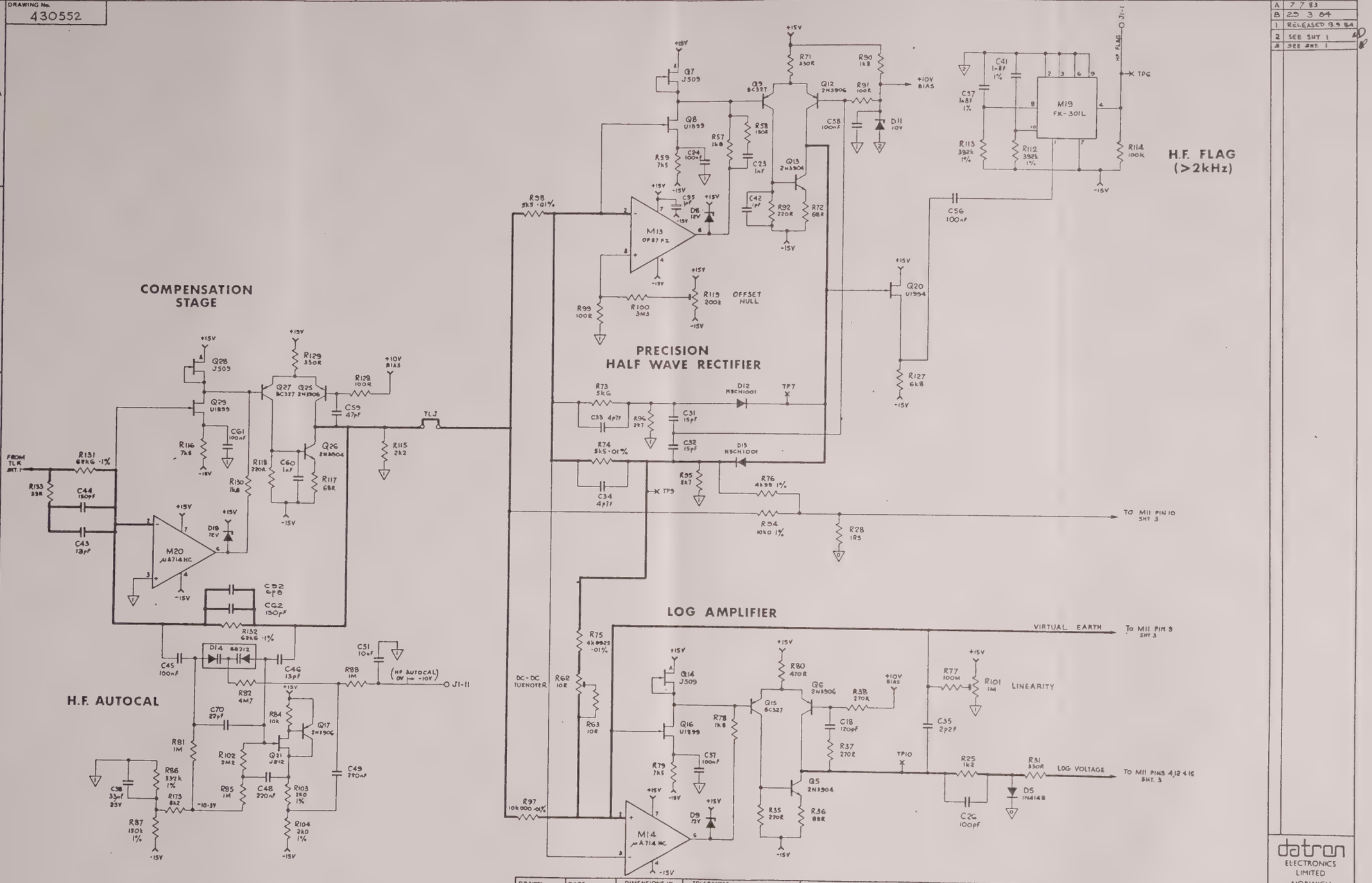
DRAWING No. 430552

SHEET 1 OF 4

DRAWING No. 430552

ISS	CHANGES
A	7 7 83
B	25 3 84
1	RELEASED 13.9.84
2	SEE SHT 1
3	SEE SHT. 1



DRAWN IL	DATE 8 7 83	DIMENSIONS IN MILLIMETRES	TOLERANCES DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 6mm; ANGULAR ± 1°	MATERIAL	ASSY DRG & PARTS LIST 400552	TITLE 1061/62 A.C. (OPTION 12)
CHECKED L.O.G.	DATE 11-7-83	SCALE	UNLESS OTHERWISE STATED FIRST ANGLE PROJECTION	FINISH	CIRCUIT DIAGRAM	
APP'D [illegible]	DATE 13.9.84	NOT TO BE SCALED			CHECK PROCEDURE 460552	
					CHECK LIST 470552	

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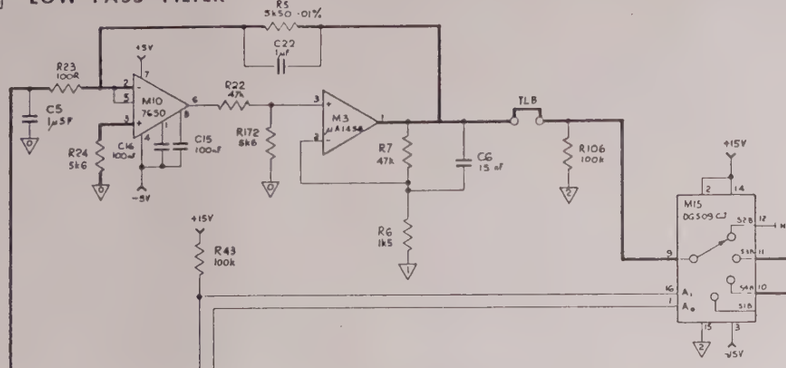
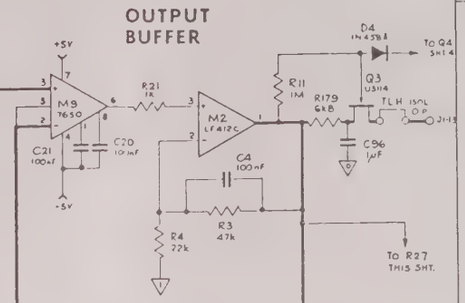
DRAWING No 430552

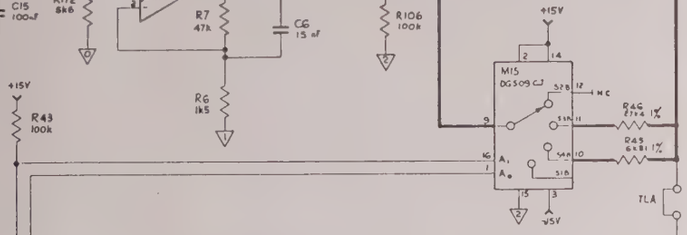
SHEET 2 OF 4

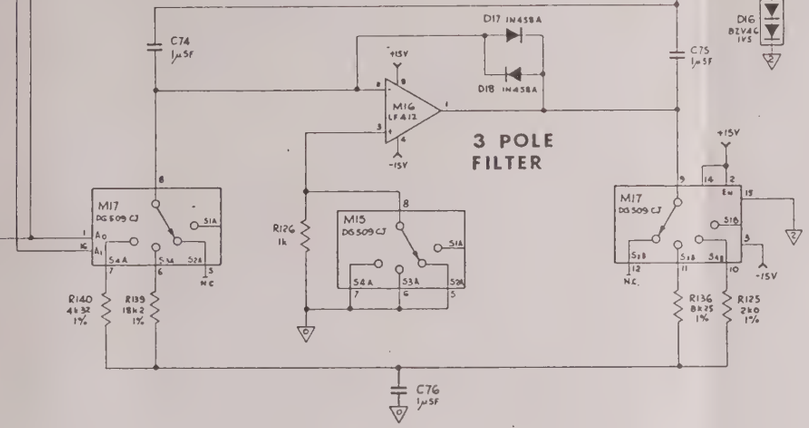
DRAWING No. 430552

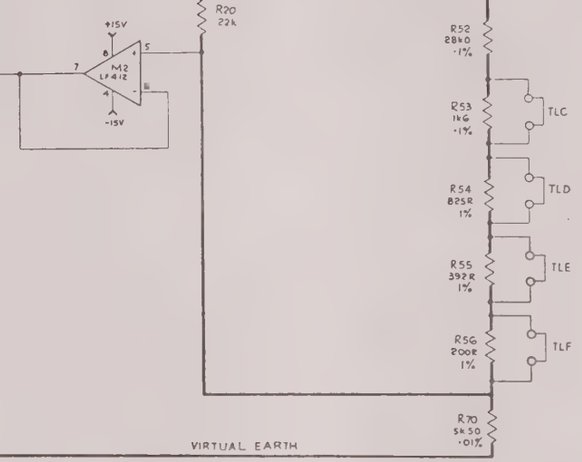
LOW PASS FILTER

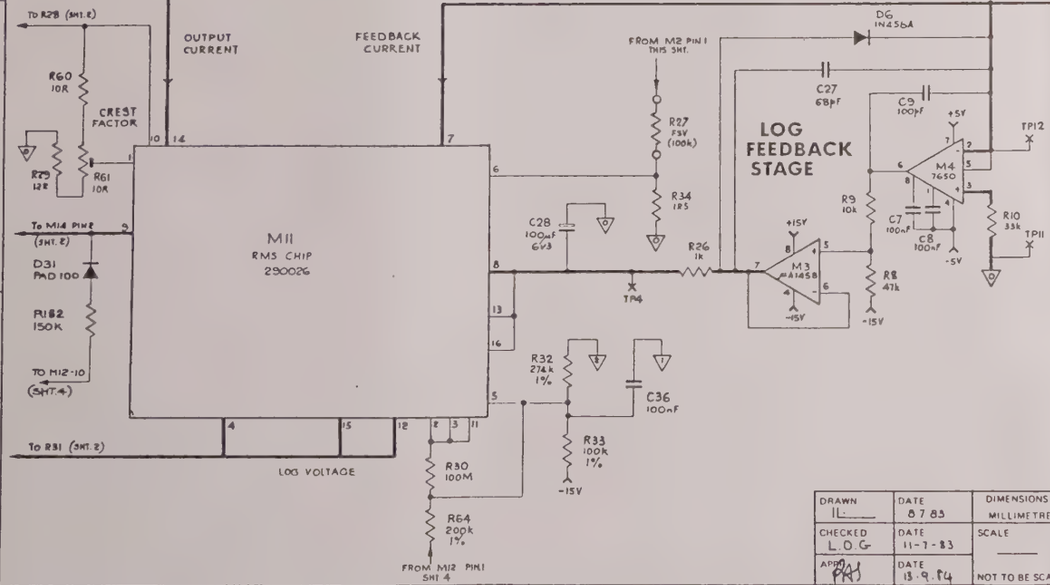


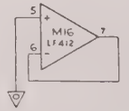












CHANGES	
A	7 7 83
B	25 3 84
1	RELEASED 13 5 84
2	SEE SHT 1
3	SEE SHT 1

DRAWN IL	DATE 8 7 83	DIMENSIONS IN MILLIMETRES	TOLERANCES: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; ANGULAR ± ½°
CHECKED L.O.G	DATE 11-7-83	SCALE —	UNLESS OTHERWISE STATED
APPD RAY	DATE 13.9.84	NOT TO BE SCALED	FIRST ANGLE PROJECTION

MATERIAL —

FINISH —

ASSY DRG No	400552
PARTS LIST	
CIRCUIT DIAGRAM	
CHECK PROCEDURE	460552
CHECK LIST	470552

TITLE: 1061/62 AC RMS CONVERTER (OPTION 12)

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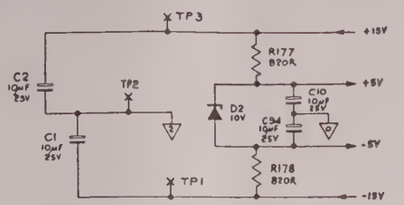
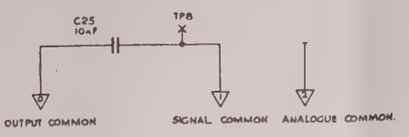
DRAWING No 430552

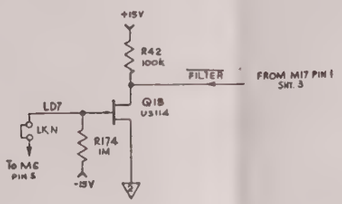
SHEET 3 OF 4

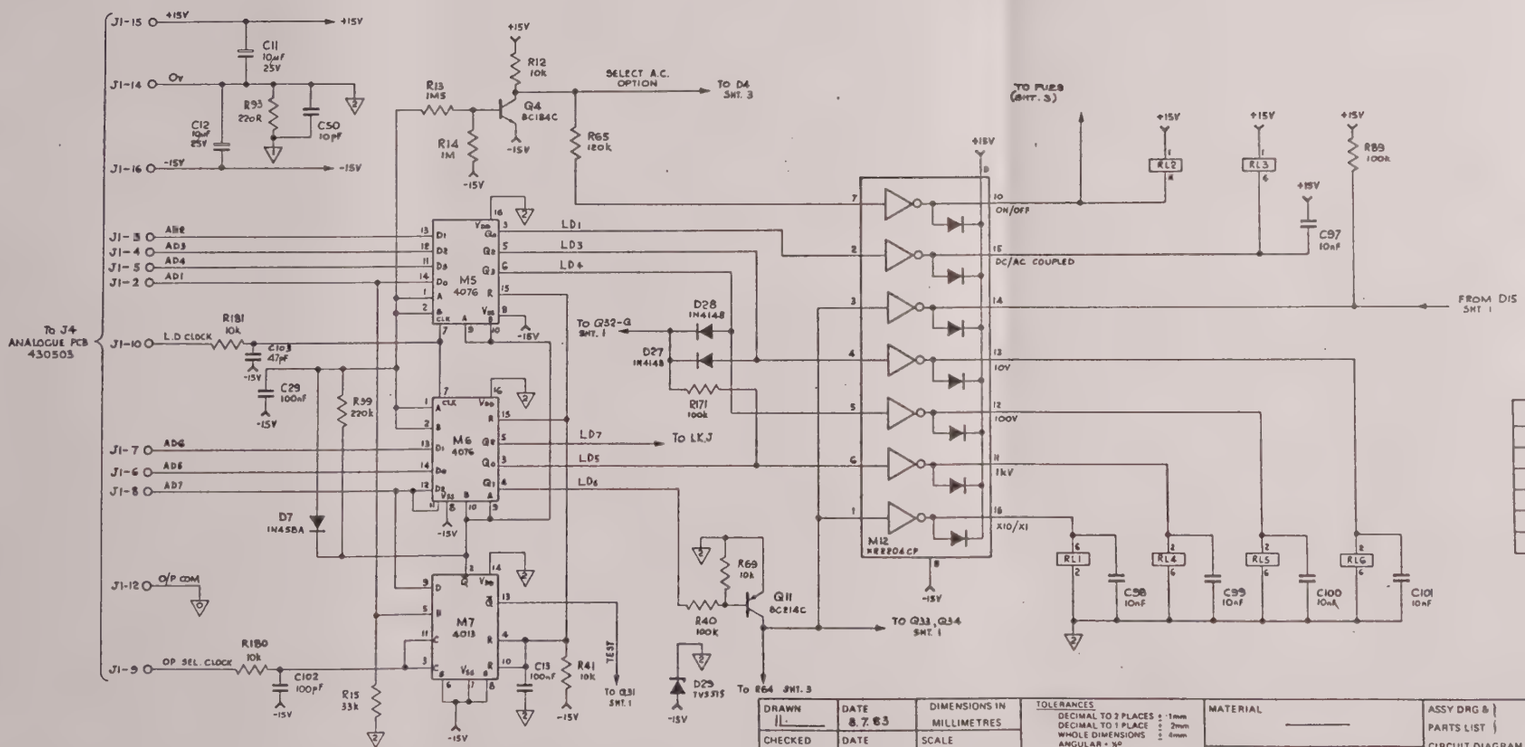
DRAWING No. 430552

ISS	CHANGES
A	7.7.83
B	23 3 84
1	RELEASED 13 9 84
2	SEE SHT 1
3	SEE SHT. 1









RANGE	LD_1	LD_3	LD_4	LD_5	LD_6	LD_7
0·1V	X	0	0	0	1	X
1V	X	0	0	0	0	X
10V	X	1	0	0	0	X
100V	X	0	1	0	0	X
1000V	X	0	0	1	0	X
FILTER	X	X	X	X	X	1

DRAWN	DATE 8.7.83	DIMENSIONS IN MILLIMETRES	TOLERANCES: DECIMAL TO 2 PLACES ± 1mm; DECIMAL TO 1 PLACE ± 2mm; WHOLE DIMENSIONS ± 4mm; ANGULAR ± 1°
CHECKED L.O.G	DATE 11-7-83	SCALE	UNLESS OTHERWISE STATED
APP'D	DATE 13.9.84	NOT TO BE SCALED	FIRST ANGLE PROJECTION

MATERIAL

FINISH

ASSY DRG & PARTS LIST	400552
CIRCUIT DIAGRAM	
CHECK PROCEDURE	460552
CHECK LIST	470552

TITLE: 1061/62 AC RANGING (OPTION 12)

datron ELECTRONICS LIMITED NORWICH

DRAWING No. 430552

SHEET 4 OF 4

datron

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